

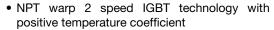
"High Side Chopper" IGBT SOT-227 (Warp 2 Speed IGBT), 70 A



SOT-227

PRODUCT SUMMARY					
V _{CES}	600 V				
I _C DC	70 A at 88 °C				
V _{CE(on)} typical at 70 A, 25 °C	2.23 V				
I _F DC	70 A at 86 °C				
Speed	30 kHz to 150 kHz				
Package	SOT-227				
Circuit	Chopper high side switch				

FEATURES





- Square RBSOA
- Low V_{CE(on)}
- FRED Pt® hyperfast rectifier
- · Fully isolated package
- Very low internal inductance (≤ 5 nH typical)
- Industry standard outline
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- · Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Easy to assemble and parallel
- · Direct mounting to heatsink
- Plug-in compatible with other SOT-227 packages
- · Lower conduction losses and switching losses
- · Low EMI, requires less snubbing

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		600	V
Continuous collector current		T _C = 25 °C	111	
Continuous collector current	I _C	T _C = 80 °C	76	
Pulsed collector current	I _{CM}		120	
Clamped inductive load current	I _{LM}		120	А
Diode continuous forward current		T _C = 25 °C	113	
blode continuous forward current	I _F	T _C = 80 °C	75	
Peak diode forward current	I _{FM}		200	
Gate to emitter voltage	V _{GE}		± 20	V
De la distribution IODT		T _C = 25 °C	447	
Power dissipation, IGBT	P _D	T _C = 80 °C	250] ,,
Davis discipation diada	-	T _C = 25 °C	236	W
Power dissipation, diode	P _D	T _C = 80 °C	132	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	V _{GE} = 0 V, I _C = 1 mA	600	-	-		
		V _{GE} = 15 V, I _C = 35 A	-	1.69	1.88		
Collector to emitter voltage	V	V _{GE} = 15 V, I _C = 70 A	-	2.23	2.44	V	
Collector to enfitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 35 A, T _J = 125 °C	-	2.07	2.31		
		V _{GE} = 15 V, I _C = 70 A, T _J = 125 °C	-	2.89	3.21		
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}, I_{C} = 500 \mu A$	3	3.9	5		
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$	V _{CE} = V _{GE} , I _C = 1 mA (25 °C to 125 °C)	-	-9	-	mV/°C	
Collector to emitter leakage gurrent	1	V _{GE} = 0 V, V _{CE} = 600 V	-	1	100	μA	
Collector to emitter leakage current I _{CES}		V _{GE} = 0 V, V _{CE} = 600 V, T _J = 125 °C	-	0.07	2.0	mA	
Diode reverse breakdown voltage	V_{BR}	R I _R = 1 mA		-	-	V	
		$I_C = 35 \text{ A}, V_{GE} = 0 \text{ V}$	-	1.80	2.33		
Diada famuard valtaga dran	V _{FM}	I _C = 70 A, V _{GE} = 0 V	-	2.13	2.71	V	
Diode forward voltage drop		I _C = 35 A, V _{GE} = 0 V, T _J = 125 °C	-	1.35	1.81		
		I _C = 70 A, V _{GE} = 0 V, T _J = 125 °C	-	1.70	2.32		
Diada variarea lagicara aurent		V _R = V _R rated	-	0.1	50	μA	
Diode reverse leakage currefit	Diode reverse leakage current I _{RM}		-	0.02	3	mA	
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 200	nA	

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg			-	320	-	
Gate to emitter charge (turn-on)	Q_ge	$I_C = 50 \text{ A}, V_{CC} = 400 \text{ V}, V_{GE} = 15 \text{ V}$		-	42	-	nC
Gate to collector charge (turn-on)	Q _{gc}				110	-	
Turn-on switching loss	E _{on}	I _C = 70 A, V _{CC} = 360 V,		-	1.15	-	
Turn-off switching loss	E _{off}	V_{GE} = 15 V, R_g = 5 Ω ,		-	1.16	-	
Total switching loss	E _{tot}	$L = 500 \mu H, T_J = 25 ^{\circ}C$		-	2.31	-	
Turn-on switching loss	E _{on}	I_{C} = 70 A, V_{CC} = 360 V, V_{GE} = 15 V, R_{g} = 5 Ω , L = 500 μ H, T_{J} = 125 °C	Energy losses include tail and diode recovery (see fig. 18)	-	1.27	-	- mJ
Turn-off switching loss	E _{off}			-	1.28	-	
Total switching loss	E _{tot}			-	2.55	-	
Turn-on delay time	t _{d(on)}			-	208	-	ns
Rise time	t _r			-	69	-	
Turn-off delay time	t _{d(off)}			-	208	-	
Fall time	t _f			-	100	-	
Reverse bias safe operating area	RBSOA	T_J = 150 °C, I_C = 120 A, R_g = 22 Ω , V_{GE} = 15 V to 0 V, V_{CC} = 400 V, V_P = 600 V			Fullsquare		
Diode reverse recovery time	t _{rr}			-	59	93	ns
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _R = 200 V		-	4	6	Α
Diode recovery charge	Q_{rr}		-	118	279	nC	
Diode reverse recovery time	t _{rr}	I _F = 50 A, dI _F /dt = 200 A/µs, V _R = 200 V, T _J = 125 °C		-	130	159	ns
Diode peak reverse current	I _{rr}			-	11	13	Α
Diode recovery charge	Q _{rr}			-	715	995	nC



THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T _J , T _{Stg}		-40	-	150	°C
Junction to case	В		-	-	0.28	
Diode	- R _{thJC}		-	-	0.53	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	-	1.3	Nm
Case style	SOT-227					

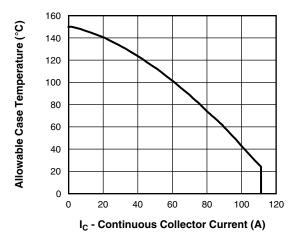


Fig. 1 - Maximum DC IGBT Collector Current vs.

Case Temperature

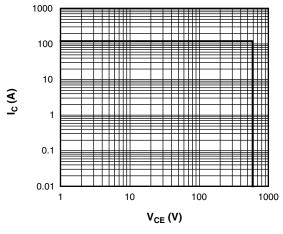


Fig. 2 - IGBT Reverse Bias SOA $T_J = 150$ °C, $V_{GE} = 15$ V

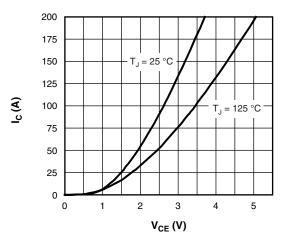


Fig. 3 - Typical IGBT Collector Current Characteristics

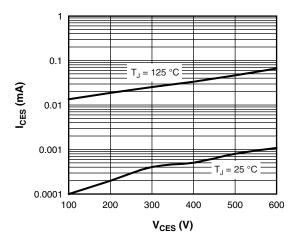


Fig. 4 - Typical IGBT Zero Gate Voltage Collector Current

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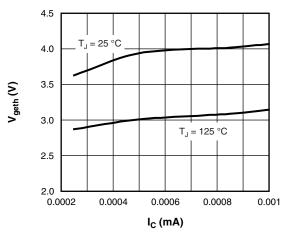
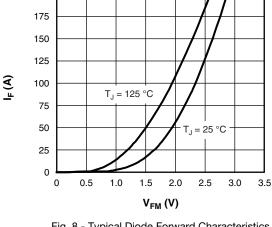


Fig. 5 - Typical IGBT Threshold Voltage



200

Fig. 8 - Typical Diode Forward Characteristics

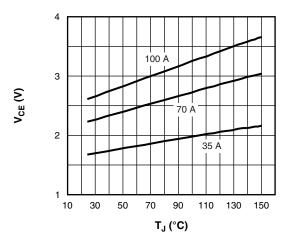


Fig. 6 - Typical IGBT Collector to Emitter Voltage vs. Junction Temperature, V_{GE} = 15 V

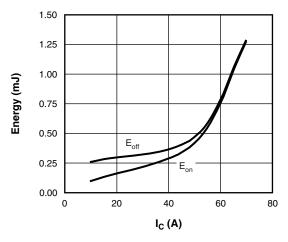


Fig. 9 - Typical IGBT Energy Loss vs. I_C $T_J = 125 \,^{\circ}\text{C}, L = 500 \,\mu\text{H}, V_{CC} = 360 \,\text{V},$ $R_q = 5 \Omega$, $V_{GE} = 15 V$

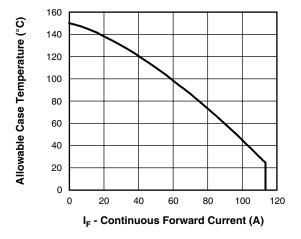


Fig. 7 - Maximum DC Forward Current vs. Case Temperature

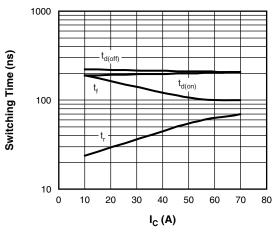
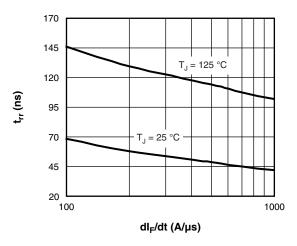
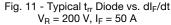


Fig. 10 - Typical IGBT Switching Time vs. I_C $T_J = 125\,^{\circ}C$, $L = 500\,\mu\text{H}$, $V_{CC} = 360\,\text{V}$, $R_q = 5 \Omega$, $V_{GE} = 15 V$

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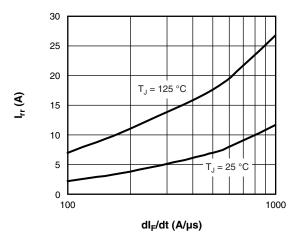


Fig. 12 - Typical I_{rr} Diode vs. dI_F/dt V_{RR} = 200 V, I_F = 50 A

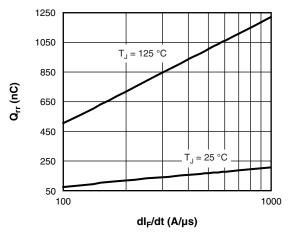


Fig. 13 - Typical Q_{rr} Diode vs. dI_F/dt $V_R = 200 \text{ V}, I_F = 50 \text{ A}$

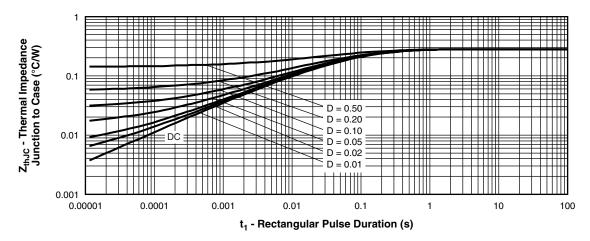


Fig. 14 - Maximum Thermal Impedance ZthJC Characteristics (IGBT)

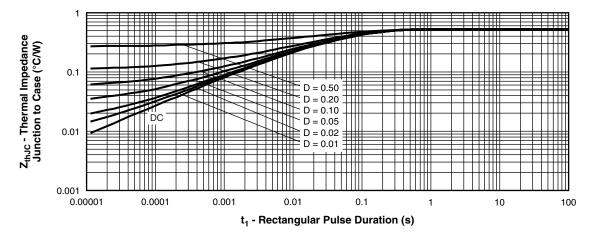
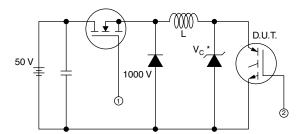
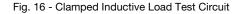


Fig. 15 - Maximum Thermal Impedance Z_{thJC} Characteristics (DIODE)



- * Driver same type as D.U.T.; $V_{\rm C}$ = 80 % of $V_{\rm ce(max)}$ * Note: Due to the 50 V power supply, pulse width and inductor will increase to obtain Id



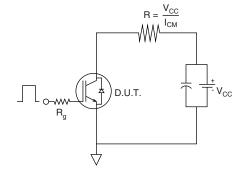


Fig. 17 - Pulsed Collector Current Test Circuit

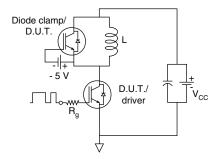


Fig. 18 - Switching Loss Test Circuit

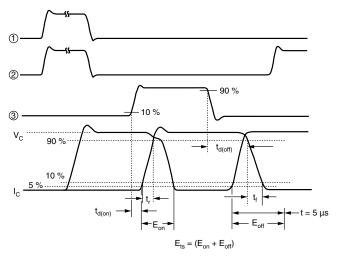
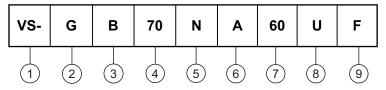


Fig. 19 - Switching Loss Waveforms Test Circuit

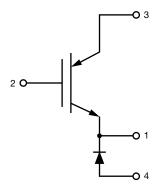
ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- Insulated Gate Bipolar Transistor (IGBT)
- B = IGBT Generation 5
- 4 Current rating (70 = 70 A)
- Circuit configuration (N = High Side Chopper)
- 6 Package indicator (A = SOT-227)
- 7 Voltage rating (60 = 600 V)
- Speed/type (U = Ultrafast IGBT)
- 9 F = F/W FRED Pt[®] diode

CIRCUIT CONFIGURATION

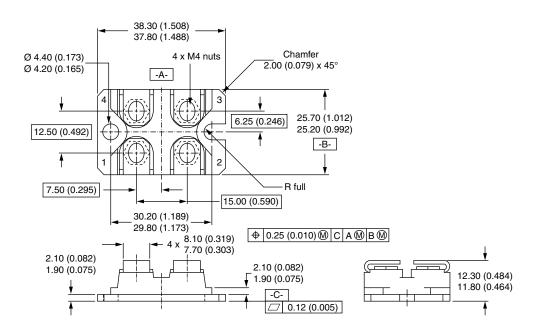


LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95036					
Packaging information http://www.vishay.com/doc?95037					



SOT-227

DIMENSIONS in millimeters (inches)



Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
- · Controlling dimension: millimeter

Document Number: 95036 Revision: 28-Aug-07

Legal Disclaimer Notice



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