Vishay General Semiconductor

High Voltage Surface-Mount Schottky Barrier Rectifier

High Barrier Technology for Improved High Temperature Performance



Cathode O Anode

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2.0 A				
V _{RRM}	90 V, 100 V				
I _{FSM}	50 A				
E _{AS}	11.25 mJ				
V_F at I_F = 2.0 A, T_J = 125 °C	0.62 V				
I_R max. at rated V_R , T_J = 25 °C	1.0 µA				
T _J max.	175 °C				
Package	SMP (DO-220AA)				
Circuit configuration	Single				

FEATURES

- Very low profile typical height of 1.0 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency
- · Low thermal resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMP (DO-220AA) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3_X - halogen-free, RoHS-compliant, and AEC-Q101 qualified

("_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix

meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	SS2PH9	SS2PH10	UNIT	
Device marking code		29	210		
Maximum repetitive peak reverse voltage	ak reverse voltage V _{RRM} 90			V	
Maximum average forward rectified current (fig. 1)	I _{F(AV)}	2.0		А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	50		А	
Non-repetitive avalanche energy at $T_{J=}25$ °C, $I_{AS} = 1.5$ A, L = 10 mH	E _{AS}	11.25		mJ	
Voltage rate of change (rated V _R)	dV/dt	10 000		V/µs	
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +175		°C	



COMPLIANT





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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Maximum instantaneous forward voltage	I _F = 2.0 A	T _J = 25 °C	V _F ⁽¹⁾	0.77	0.80	v
	$I_{\rm F} = 2.0 \rm A$	T _J = 125 °C		0.62	0.66	
Maximum reverse current at rated V_R		T _J = 25 °C	I _R ⁽²⁾	0.1	1.0	- μΑ
		T _J = 125 °C		60	500	
Typical junction capacitance	4.0 V, 1 MHz		CJ	65	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	SS2PH9	SS2PH10	UNIT
	R _{0JA} ⁽¹⁾	110		°C/W
Typical thermal resistance	R _{θJL} ⁽¹⁾	15		
	R _{0JC} ⁽¹⁾	25		

Note

(1) Thermal resistance from junction to ambient and junction to lead mounted on PCB with 15 mm x 15 mm copper pad areas. R_{0JC} is measured at the top center of the body

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
SS2PH9-M3/84A	0.024	84A	3000	7" diameter plastic tape and reel	
SS2PH9-M3/85A	0.024	85A	10 000	13" diameter plastic tape and reel	
SS2PH9HM3_A/H ⁽¹⁾	0.024	Н	3000	7" diameter plastic tape and reel	
SS2PH9HM3_A/I ⁽¹⁾	0.024	Ι	10 000	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

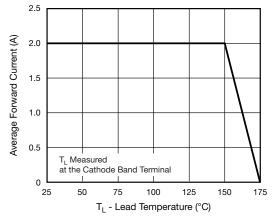


Fig. 1 - Forward Current Derating Curve

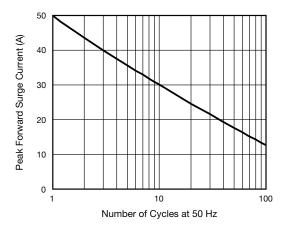
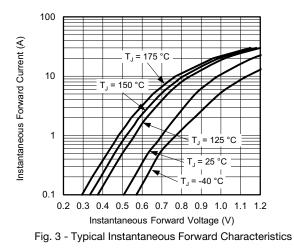
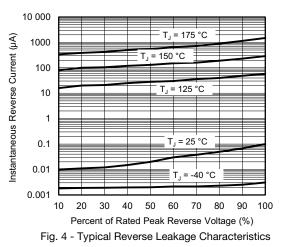


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current





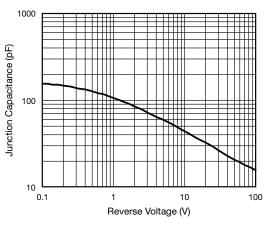


Fig. 5 - Typical Junction Capacitance

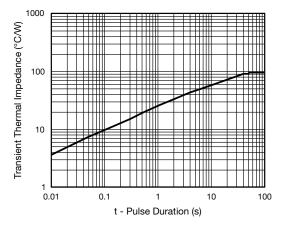


Fig. 6 - Typical Transient Thermal Impedance

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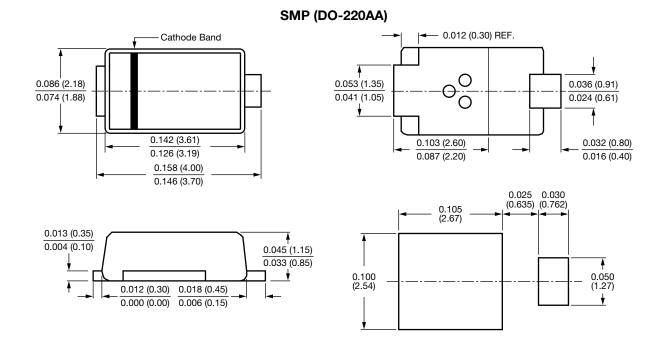
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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