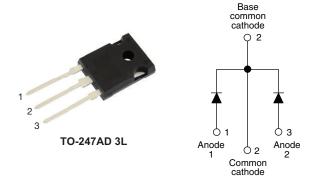


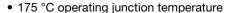
Hyperfast Soft Recovery Diode, 2 x 30 A FRED Pt[®] Gen 4

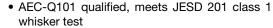


PRODUCT SUMMARY					
Package	TO-247AD 3L				
I _{F(AV)}	2 x 30 A				
V_{R}	600 V				
V _F at I _F	1.37 V				
t _{rr} typ.	see Recovery table				
T _J max.	175 °C				
Diode variation	Common cathode				

FEATURES

- Gen 4 FRED Pt® technology
- Low I_{RRM} and reverse recovery charge
- · Very low forward voltage drop
- Polyimide passivated chip for high reliability standard





 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE

DESCRIPTION

Gen 4 Fred Pt technology, state of the art, ultralow V_F , soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Cathode to anode voltage	V _R		600	V			
Average rectified forward current	I _{F(AV)}	T _C = 122 °C	30	۸			
Non-repetitive peak surge current, per leg	I _{FSM}	$T_C = 25$ °C, $t_p = 8.3$ ms, half sine wave	240				
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	BOL TEST CONDITIONS MIN. T		TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-		
Forward voltage		I _F = 30 A	-	1.65	2	V	
	V _F	I _F = 60 A	-	1.95	-		
		I _F = 30 A, T _J = 125 °C	-	1.44	-		
		I _F = 60 A, T _J = 125 °C	-	1.78	-		
		I _F = 30 A, T _J = 150 °C	-	1.37	1.6		
		I _F = 60 A, T _J = 150 °C	-	1.68	-		
Developed legislation of the set		V _R = V _R rated	-	-	50		
Reverse leakage current	I _R	T _J = 125 °C, V _R = V _R rated	-	-	500	μA	
Junction capacitance	C _T	V _R = 600 V	-	18.3	-	pF	



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time	+	T _J = 25 °C	I _F = 30 A dI _F /dt = 1000 A/μs V _R = 400 V	1	55	1	ns	
	t _{rr}	T _J = 125 °C		-	75	-		
Peak recovery current	I _{RRM}	T _J = 25 °C		-	13	-	А	
		T _J = 125 °C		-	23	-		
Reverse recovery charge	0	T _J = 25 °C		-	500	-	nC	
	Q _{rr}	T _J = 125 °C		-	1250	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance, junction to case	R _{thJC}		-	-	1	°C/W	
Thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.4	-		
Weight			-	6.0	-	g	
weight			-	0.21	-	oz.	
Mounting torque			6.0		12	kgf · cm	
Mounting torque			(5)	_	(20)	(lbf · in)	
Marking device		Case style TO-247AD 3L	C4PH6006LH				

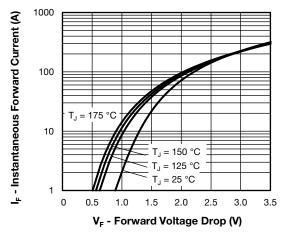


Fig. 1 - Typical Forward Voltage Drop Characteristics

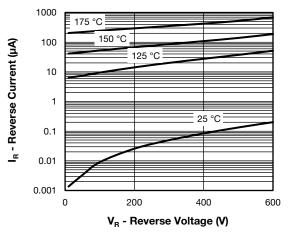


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

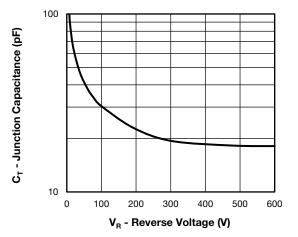


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

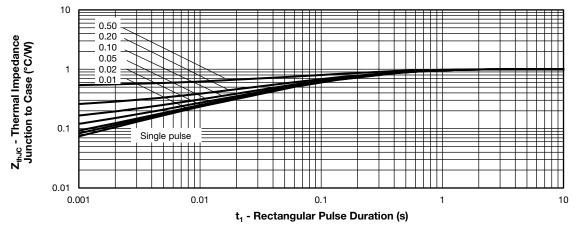


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

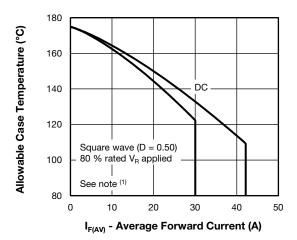


Fig. 5 - Max. Allowable Case Temperature vs. Average Forward Current

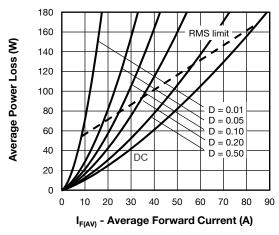


Fig. 6 - Forward Power Loss Characteristics

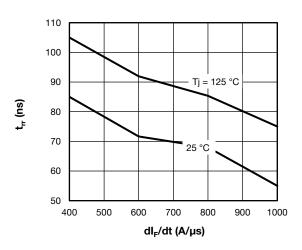


Fig. 7 - Typical Reverse Recovery Time vs. dI_{F}/dt

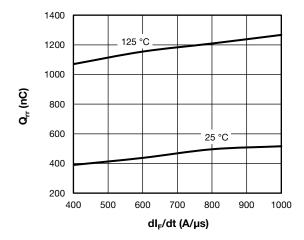


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see Fig. 5)} \\ P_{dREV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_R = \text{rated } V_R \\ \end{array}$

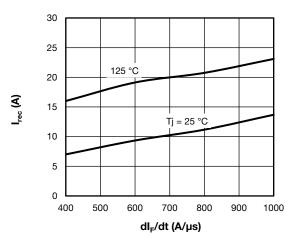


Fig. 9 - Typical Reverse Current vs. dl_F/dt

ORDERING INFORMATION TABLE

Device code VS-C Ρ н 60 Н **N3** 4 06 L (2) (7)9 (4) (5) (6) (10)(3) (8) Vishay Semiconductors product Circuit configuration: C = common cathode FRED Gen 4

P = TO-247 package

Process type:

H = hyperfast recovery

Current rating (60 = 60 A)

Voltage rating (06 = 600 V)

L = long lead

H = AEC-Q101 qualified

Environmental digit:

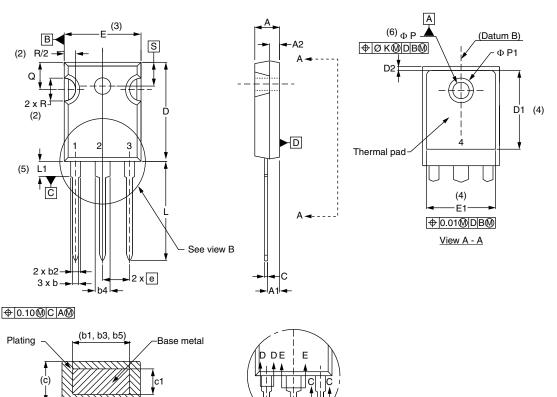
N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-C4PH6006LHN3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626			
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007			

TO-247AD 3L

DIMENSIONS in millimeters and inches



View B

SYMBOL	MILLIMETERS		INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
О	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

Section C - C, D - D, E - E

SYMBOL	MILLIM	IETERS	INC	NOTES	
OTIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215 BSC		
ØΚ	0.254		0.010		
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	ı	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217 BSC		
•	•			•	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



Vishay

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