

Vishay General Semiconductor

Surface-Mount TMBS[®] (Trench MOS Barrier Schottky) Rectifier



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	5 A			
V _{RRM}	60 V			
I _{FSM}	100 A			
V_F at I_F = 2.5 A (T_J = 125 °C)	0.35 V			
T _J max.	150 °C			
Package	DFN3820A			
Circuit configuration	Single			

FEATURES

- Low profile package - typical height of 0.88 mm Available
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- COMPLIANT Meets MSL level 1, per J-STD-020, LF maximum HALOGEN peak of 260 °C FREE
- AEC-Q101 gualified available - Automotive ordering code; base P/NHM3
- Compatible to SMP (DO-220AA) package case outline
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: DFN3820A

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V5NL63	UNIT	
Device marking code		5LF		
Maximum repetitive peak reverse voltage	V _{RRM}	60	V	
Maximum average forward rectified ourrent (fig. 1)	I _{F(AV)} ⁽¹⁾	5	А	
Maximum average forward rectified current (fig. 1)	I _{F(AV)} ⁽²⁾	2.4	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	I _{FSM} 100		
Operating junction temperature range	T _J ⁽³⁾	-40 to +150	°C	
Storage temperature range	T _{STG}	-55 to +150	°C	

Notes

⁽¹⁾ With infinite heatsink

(2) Free air, mounted on FR4 PCB, 2 oz., standard footprint

 $^{(3)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{b,IA}



RoHS

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 2.5 A	T _J = 25 °C		0.44	-	
	$I_F = 5 A$ $I_J = 25 C$	V _F ⁽¹⁾	0.51	0.58	V	
	I _F = 2.5 A	– T _J = 125 °C	VF	0.35	-	
	$I_F = 5 A$			0.46	0.52	
Reverse current	V - 60 V	$V_{R} = 60 V = \frac{T_{J} = 25 °C}{T_{J} = 125 °C}$	I _R ⁽²⁾	-	0.08	- mA
	$v_{\rm R} = 60 v$			4	9	
Typical junction capacitance	4.0 V, 1 MH	4.0 V, 1 MHz		840	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise specified)					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Thermal resistance	R _{0JA} (1)(2)	135	169	°C/W	
	R _{0JM} ⁽³⁾	5	6.3		

Notes

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

(2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint

(3) Thermal resistance junction-to-mount to follow JEDEC 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V5NL63-M3/H	0.023	Н	3500	7" diameter plastic tape and reel	
V5NL63-M3/I	0.023	I	14 000	13" diameter plastic tape and reel	
V5NL63HM3/H ⁽¹⁾	0.023	Н	3500	7" diameter plastic tape and reel	
V5NL63HM3/I ⁽¹⁾	0.023	I	14 000	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

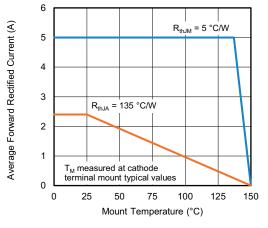


Fig. 1 - Maximum Forward Current Derating Curve

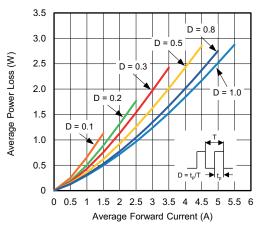


Fig. 2 - Forward Power Loss Characteristics

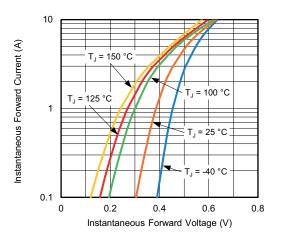


Fig. 3 - Typical Instantaneous Forward Characteristics

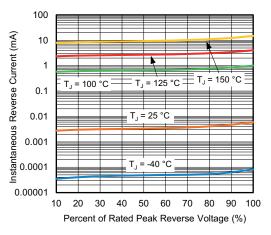


Fig. 4 - Typical Reverse Characteristics

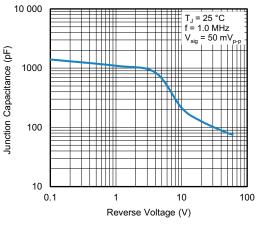


Fig. 5 - Typical Junction Capacitance

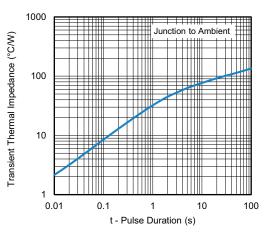


Fig. 6 - Typical Transient Thermal Impedance

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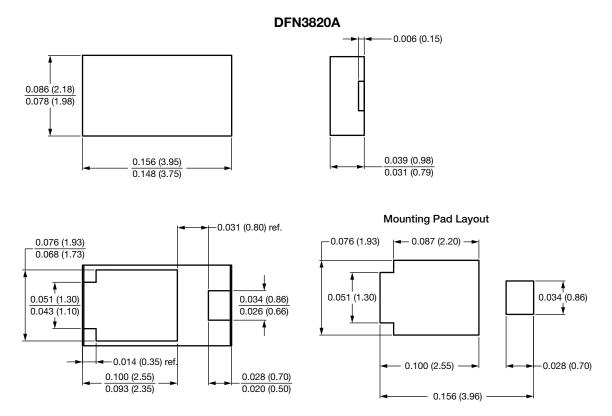
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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