Lower Voltage Ceramic DC Disc Capacitors
1000 V_{DC} Precision Capacitors

FEATURES

- Ultra stable over temperature and voltage
- Used when ultimate stability is required
- Radial leads
- Ceramic singlelayer capacitor
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT

APPLICATIONS

- Temperature compensating
- Resonant circuit

QUICK REFERENCE DATA			
DESCRIPTION	VALUE		
Ceramic Class	1		
Ceramic Dielectric	C0K	C0G	U2J
Voltage (V_{DC})	1000		
Min. Capacitance (pF)	1.0	3.0	33
Max. Capacitance (pF)	2.7	270	680
Mounting	Radial		

INSULATION RESISTANCE

Min. 1000 ΩF or 50 000 MΩ

TOLERANCE ON CAPACITANCE

± 5 %

DISSIPATION FACTOR

0.1 % max. at 1 MHz; 1 V

CATEGORY TEMPERATURE RANGE

-55 °C to +125 °C

CLIMATIC CATEGORY ACC. TO EN 60068-1

55/125/21

OPERATING TEMPERATURE RANGE

-55 °C to +105 °C (1)

Note

(1) For explanation about the difference of operating temperature range and temperature characteristic of capacitance, please see www.vishay.com/doc?48299

DESIGN

The capacitors consist of a ceramic disc of which both sides are silver-plated. Connection leads are made of tinned copper or tinned copper clad steel having diameters of 0.020" (0.51 mm) or 0.025" (0.64 mm).

The capacitors may be supplied with radial kinked or straight leads having lead spacing of 0.250" (6.35 mm) or 0.375" (9.5 mm).

Coating is made of flame retardant epoxy resin in accordance with *"UL 94 V-0"*.

CAPACITANCE RANGE

1.0 pF to 680 pF

RATED VOLTAGE

1000 V_{DC}

DIELECTRIC STRENGTH BETWEEN LEADS

Component test, 100 % test at production line:

2500 V_{DC}, 2 s

CERAMIC DIELECTRIC

C0K, C0G, U2J (class 1)

DIMENSIONS in inches (millimeters)

Fig. 1

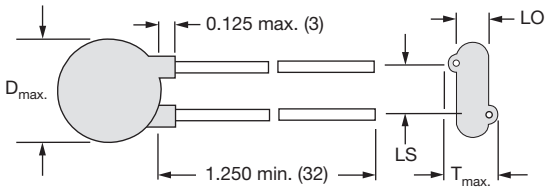
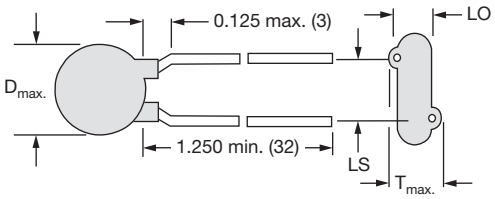


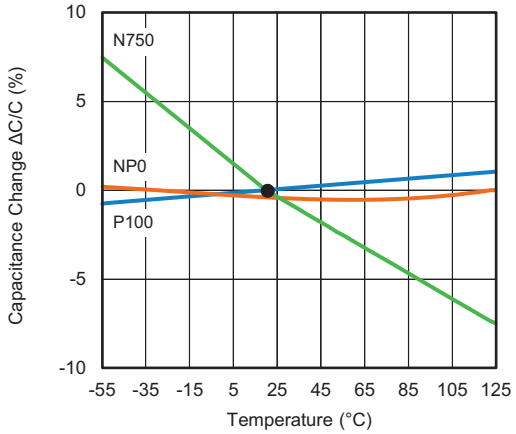
Fig. 2



ORDERING INFORMATION, CERAMIC 1000 V_{DC} PRECISION CAPACITORS

C (pF)	TOL.	$D_{max.}$ DIAMETER INCH (mm)	$T_{max.}$ THICKNESS INCH (mm)	LS LEAD SPACE INCH (mm) ± 1 mm	LO LEAD OFFSET INCH (mm) ± 0.5 mm	WIRE SIZE AWG	WIRE SIZE INCH (mm)	FIG.	ORDERING CODE
C0K (P100)									
1.0	± 0.5 pF	0.250 (6.4)	0.156 (4.0)	0.250 (6.4)	0.098 (2.5)	24	0.020 (0.51)	2	561R10TCCV10
2.2					0.051 (1.3)				561R10TCCV22
2.7					0.043 (1.1)				561R10TCCV27
C0G (NP0)									
3.0	± 0.5 pF	0.250 (6.4)	0.156 (4.0)	0.250 (6.4)	0.063 (1.6)	24	0.020 (0.51)	2	561R10TCCV30
3.3					0.055 (1.4)				561R10TCCV33
3.9					0.055 (1.4)				561R10TCCV39
4.7					0.043 (1.1)				561R10TCCV47
5.0					0.043 (1.1)				561R10TCCV50
5.6					0.039 (1.0)				561R10TCCV56
6.8					0.047 (1.2)				561R10TCCV68
8.2					0.043 (1.1)				561R10TCCV82
10	± 5 %				0.051 (1.3)				561R10TCCQ10
12					0.043 (1.1)				561R10TCCQ12
15					0.039 (1.0)				561R10TCCQ15
18		0.290 (7.4)	0.156 (4.0)	0.250 (6.4)	0.043 (1.1)				561R10TCCQ18
20					0.039 (1.0)				561R10TCCQ20
22					0.039 (1.0)				561R10TCCQ22
25					0.035 (0.9)				561R10TCCQ25
27		0.370 (9.4)	0.156 (4.0)	0.250 (6.4)	0.047 (1.2)				561R10TCCQ27
30					0.051 (1.3)				561R10TCCQ30
33					0.047 (1.2)				561R10TCCQ33
39					0.043 (1.1)				561R10TCCQ39
47		0.440 (11.2)	0.156 (4.0)	0.250 (6.4)	0.051 (1.3)				561R10TCCQ47
50					0.047 (1.2)				561R10TCCQ50
56					0.047 (1.2)				561R10TCCQ56
68		0.490 (12.4)	0.156 (4.0)	0.250 (6.4)	0.047 (1.2)	22	0.025 (0.64)		561R10TCCQ68
82		0.490 (12.4)	0.156 (4.0)	0.375 (9.5)	0.043 (1.1)				561R10TCCQ82
100		0.560 (14.2)	0.156 (4.0)	0.375 (9.5)	0.047 (1.2)			1	561R10TCCT10
120					0.047 (1.2)				561R10TCCT12
150		0.630 (16.0)	0.156 (4.0)	0.375 (9.5)	0.043 (1.1)				561R10TCCT15
180		0.680 (17.3)	0.156 (4.0)	0.375 (9.5)	0.043 (1.1)				561R10TCCT18
220		0.760 (19.3)	0.156 (4.0)	0.375 (9.5)	0.043 (1.1)				561R10TCCT22
270		0.890 (22.6)	0.156 (4.0)	0.375 (9.5)	0.047 (1.2)				561R10TCCT27
U2J (N750)									
33	± 5 %	0.290 (7.4)	0.156 (4.0)	0.250 (6.4)	0.039 (1.0)	24	0.020 (0.51)	2	561R10TCUQ33
68		0.370 (9.4)	0.156 (4.0)	0.250 (6.4)	0.039 (1.0)	22	0.025 (0.64)		561R10TCUQ68
560		0.650 (16.5)	0.156 (4.0)	0.375 (9.5)	0.039 (1.0)	22	0.025 (0.64)	1	561R10TCUT56
680		0.710 (18.0)	0.156 (4.0)	0.375 (9.5)	0.047 (1.2)	22	0.025 (0.64)	1	561R10TCUT68

CAPACITANCE CHANGE VS. TEMPERATURE (Typical)



STORAGE

The capacitors must not be stored in a corrosive atmosphere, where sulphide or chloride gas, acid, alkali or salt are present. Exposure of the components to moisture, should be avoided. The solderability of the leads is not affected by storage of up to 24 months (temperature +10 °C to +40 °C, relative humidity up to 60 % RH). Class 2 ceramic dielectric capacitors are also subject to aging see general information (www.vishay.com/doc?23140).

SOLDERING

SOLDERING SPECIFICATIONS Soldering test for capacitors with wire leads: (according to IEC 60068-2-20, solder bath method)		
	SOLDERABILITY	**RESISTANCE TO SOLDERING HEAT**
Soldering temperature	(235 ± 5) °C	(260 ± 5) °C
Soldering duration	(2 ± 0.5) s	(10 ± 1) s
Distance from component body	≥ 2 mm	≥ 5 mm

SOLDERING RECOMMENDATIONS

Ceramic capacitors are very sensitive to rapid changes in temperature (thermal shock) therefore the solder heat resistance specification (see table above) should not be exceeded. Exposing the capacitor to excessive heating may result in thermal shocks that can crack the ceramic body. Similarly, excessive heating can cause the internal solder junction to melt.

When soldering radial leaded ceramic capacitors with a soldering iron, it should be performed under the following conditions and should not exceed:

- Maximum temperature of iron-tip: 400 °C
- Maximum soldering iron wattage: 50 W
- Maximum soldering time: 3.5 s

Failure to follow the above cautions may result, in worst case, in short circuit or cause fuming or thermo-mechanical damage when the product is used.

Leaded ceramic capacitors are not designed for reflow process or dipping the body into a solder melt.

CLEANING

The components should be cleaned immediately following the soldering operation with vapor degreasers.

CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions:

- Maximum rinse bath capacity output: 20 W/liter
- Maximum rinsing time: 300 s
- Do not vibrate the PCB/PWB directly
- Excessive ultrasonic cleaning may lead to mechanical damage

SOLVENT RESISTANCE

The coating and marking of the capacitors are resistant to the following test method:

IEC 60068-2-45 (method XA)

MOUNTING

We do not recommend modifying the lead terminals, e.g. bending or cropping. This action could break the coating or crack the ceramic insert. In order to avoid such failures we are offering different lead wire designs (e.g. straight, inline, inside crimp, outside crimp etc.) If however, the lead must be modified in any way, we recommend support of the lead with a clamping fixture next to the coating. If a defined product stop is required for mounting on a PCB, a mechanically formed product stop or a mounting tool should be used.

OPERATING VOLTAGE

In case the voltage is applied to the circuit, starting as well as stopping, may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency, pulse, or similar application, it may have self-generated heat due to dielectric dissipation.

Temperature increase due to self-generated heating should not exceed 20 °C while operating at an atmosphere temperature of 25 °C.

When measuring, the surface temperature, make sure that the capacitor is not affected by radiant, conductive and convective heat by its surroundings. Excessive heat may lead to thermo-mechanical deterioration of the capacitor's characteristics and reliability.

RELATED DOCUMENTS	
General Information	www.vishay.com/doc?23140

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