

RoHS

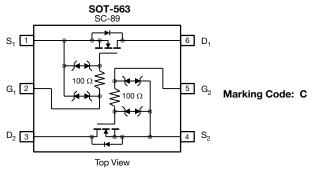
COMPLIANT HALOGEN

FREE

Vishay Siliconix

Dual N-Channel 20 V (D-S) MOSFET

PRODUCT SI	JMMARY	
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (mA)
20	0.70 at V _{GS} = 4.5 V	600
	0.85 at V_{GS} = 2.5 V	500
	1.25 at V _{GS} = 1.8 V	350



Ordering Information: Si1024X-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET: 1.8 V Rated
- Very Small Footprint
- High-Side Switching
- Low On-Resistance: 0.7 Ω
- Low Threshold: 0.8 V (typ.)
- Fast Switching Speed: 10 ns
- 1.8 V Operation
- Gate-Source ESD Protected: 2000 V
- Compliant to RoHS Directive 2002/95/EC

BENEFITS

- Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- · Power Supply Converter Circuits
- · Load/Power Switching Cell Phones, Pagers

ABSOLUTE MAXIMUM RATINGS	$(T_A = 25 °C, unle$	ss otherwise	noted)		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}		20	V
Gate-Source Voltage		V _{GS}	± 6		v
	T _A = 25 °C	1	515	485	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C	Ι _D	370	350	
Pulsed Drain Current ^b	Ilsed Drain Current ^b		650		mA
Continuous Source Current (Diode Conduction) ^a		I _S	450	380	
	T _A = 25 °C	PD	280	250	mW
Maximum Power Dissipation ^a	T _A = 85 °C	۲D	145	130	
Operating Junction and Storage Temperature Ran	ige	T _J , T _{stg}	- 55	to 150	°C
Gate-Source ESD Rating (HBM, Method 3015)		ESD	2	000	V

Notes:

a. Surface mounted on FR4 board.

b. Pulse width limited by maximum junction temperature.

1

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					•	•	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.45		0.9	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$		± 0.5	± 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20 V, V_{GS} = 0 V$		0.3	100	nA	
		V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 85 °C			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 4.5 V$	700			mA	
Drain-Source On-State	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 600 \text{ mA}$		0.41	0.70	Ω	
		$V_{GS} = 2.5 \text{ V}, I_D = 500 \text{ mA}$		0.53	0.85		
Resistance ^a		V _{GS} = 1.8 V, I _D = 350 mA		0.70	1.25	1	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 400 \text{ mA}$		1		S	
Diode Forward Voltage ^a	V _{SD}	I _S = 150 mA, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b					•	•	
Total Gate Charge	Qg			750			
Gate-Source Charge	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 250 mA		75		рС	
Gate-Drain Charge	Q _{gd}			225		1	
Turn-On Time	t _{d(on)}	V_{DD} = 10 V, R_L = 47 Ω		10			
Turn-Off Time	t _{d(off)}	I _D ≅ 200 mA, V _{GEN} = 4.5 V, R _g = 10 Ω 36		36		ns	

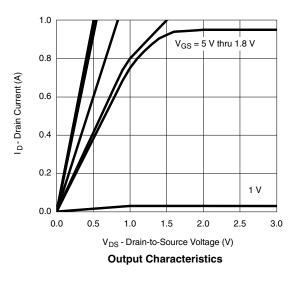
Notes:

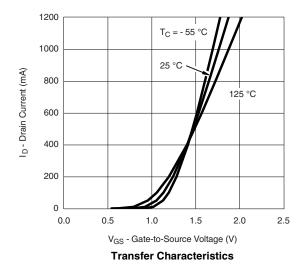
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

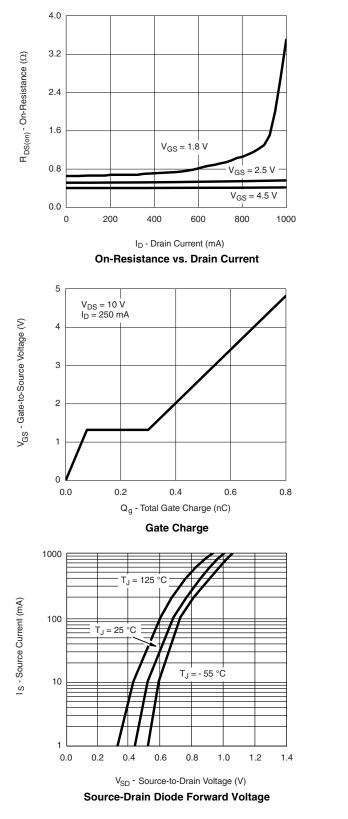


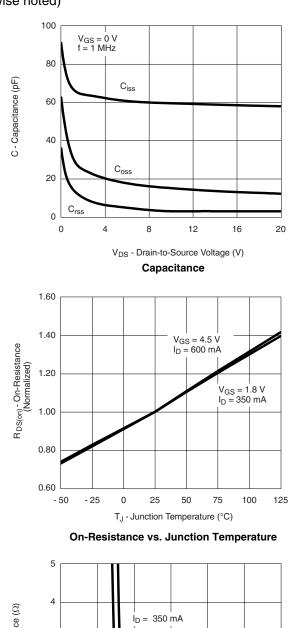


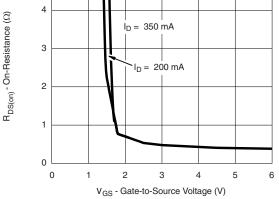
Document Number: 71170 S11-0854-Rev. E, 02-May-11



TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)







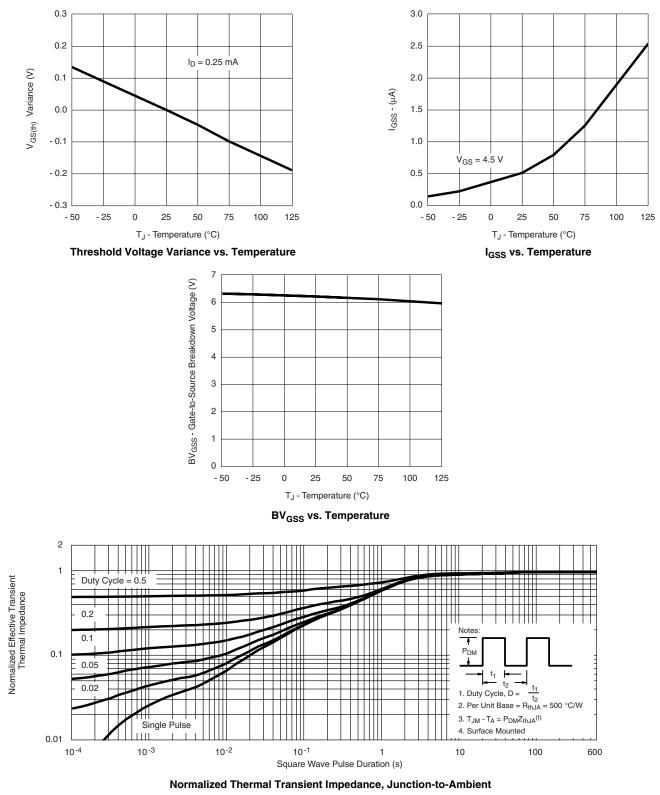
On-Resistance vs. Gate-to-Source Voltage

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3

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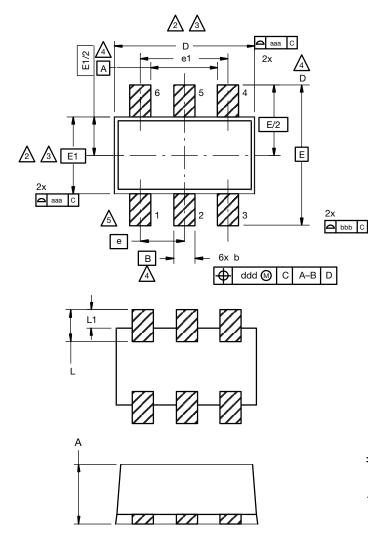
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SC-89 6-Leads (SOT-563F)



Notes

- 1. Dimensions in millimeters.
- Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.
- A Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.

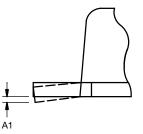
 \underline{A} Datums A, B and D to be determined 0.10 mm from the lead tip.

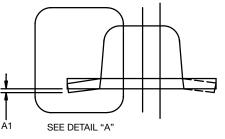
A Terminal numbers are shown for reference only.

A These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.









DIM.	MILLIMETERS			
	MIN.	NOM.	MAX.	
А	0.56	0.58	0.60	
A1	0	0.02	0.10	
b	0.15	0.22	0.30	
С	0.10	0.14	0.18	
D	1.50	1.60	1.70	
E	1.50	1.60	1.70	
E1	1.15	1.20	1.25	
е	0.45	0.50	0.55	
e1	0.95	1.00	1.05	
L	0.25	0.35	0.50	
L1	0.10	0.20	0.30	
C14-0439-Rev DWG: 5880	/. C, 11-Aug-14			

Revision: 11-Aug-14

1 For technical questions, contact: analogswitchtechsupport@vishay.com Document Number: 71612

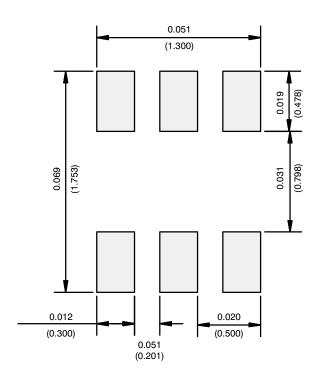
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Application Note 826

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RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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