



# N- and P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY						
	V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)			
		0.060 at V <sub>GS</sub> = 4.5 V	3.4			
N-Channel	20	0.070 at V <sub>GS</sub> = 2.5 V	3.2			
		0.100 at V <sub>GS</sub> = 1.8 V	2.5			
		0.110 at V <sub>GS</sub> = - 4.5 V	- 2.5			
P-Channel	- 20	0.145 at V <sub>GS</sub> = - 2.5 V	- 2.0			
		0.220 at V <sub>GS</sub> = - 1.8V	- 1.0			

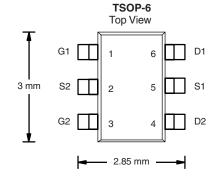
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Fast Switching In Small Footprint
- Very Low R<sub>DS(on)</sub> for Increased Efficiency
- Compliant to RoHS Directive 2002/95/EC



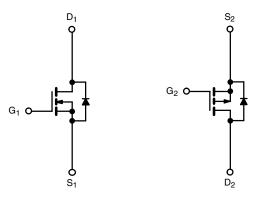
#### **APPLICATIONS**

· Load Switch for Portable Devices



Ordering Information: Si3586DV-T1-E3 (Lead (Pb)-free)

Si3586DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted								
Parameter			N-Channel		P-Channel			
		Symbol	5 s	Steady State	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20		- 20		V	
Gate-Source Voltage	ate-Source Voltage		± 8				V	
Continuous Prois Courset /T 150 °C\2	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	3.4	2.9	- 2.5	- 2.1		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		2.7	2.3	- 2.0	- 1.7		
Pulsed Drain Current		I <sub>DM</sub>	± 8				A	
Continuous Source Current (Diode Conduction	on) <sup>a</sup>	I <sub>S</sub>	1.05	0.75	- 1.05	- 0.75		
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.15	0.83	1.15	0.83	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		0.73	0.53	0.73	0.53		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150				°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Marrian una lumation de Amelianda	t ≤ 5 s	- R <sub>thJA</sub>	93	110			
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		130	150	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	90	90			

Note

a. Surface Mounted on 1" x 1" FR4 board.



<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted									
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit			
Static									
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$ N-		0.40		1.1	V		
		$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	P-Ch	- 0.40		- 1.1	v		
Cata Dadi I saliana	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	N-Ch			± 100	nA		
Gate-Body Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	P-Ch			± 100			
	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	N-Ch			1			
Zava Cata Valtaga Dvain Current		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$ P-Ch $V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 85 ^{\circ}\text{C}$ N-Ch				- 1	μΑ		
Zero Gate Voltage Drain Current						10			
		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	P-Ch			- 10			
0.00.00.00.00.00		$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	N-Ch	I-Ch 5			_		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	P-Ch	- 5			Α		
		$V_{GS} = 4.5 \text{ V}, I_D = 3.4 \text{ A}$	N-Ch		0.047	0.060			
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 2.5 A	P-Ch		0.086	0.110	Ω		
	Б	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 3.2 A	N-Ch		0.054	0.070			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 2.0 A	P-Ch		0.116	0.145			
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 2.5 A	V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 2.5 A N-Ch		0.075	0.100	1		
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 1.0 A	P-Ch		0.170	0.220			
	9 <sub>fs</sub>	$V_{DS} = 5 \text{ V}, I_{D} = 3.4 \text{ A}$	N-Ch		13				
Forward Transconductance <sup>a</sup>		V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 2.5 A	P-Ch		6		S		
	V <sub>SD</sub>	I <sub>S</sub> = 1.05 A, V <sub>GS</sub> = 0 V	N-Ch		0.8	1.1	.,		
Diode Forward Voltage <sup>a</sup>		I <sub>S</sub> = - 1.05 A, V <sub>GS</sub> = 0 V	P-Ch		- 0.8	- 1.1	V		
Dynamic <sup>b</sup>									
Total Gate Charge	Qg	N.O.	N-Ch		4.1	6.0			
Total date onlinge		N-Channel $V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 3.4 \text{ A}$	P-Ch		5	7.5	nC		
Gate-Source Charge	Q <sub>gs</sub> Q <sub>gd</sub> R <sub>g</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 0.4 N	N-Ch		0.65				
		P-Channel	P-Ch		0.68				
Gate-Drain Charge		$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2.5 \text{ A}$	N-Ch P-Ch		0.8 1.3				
			N-Ch		2.6				
Gate Resistance			P-Ch		9.8				
T 0 D 1 T	t <sub>d(on)</sub>		N-Ch		30	45			
Turn-On Delay Time		N-Channel	P-Ch		28	45			
Rise Time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, R_L = 10 \Omega$	N-Ch		52	85			
		$I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_G = 6 \Omega$	P-Ch		55	85			
Turn-Off Delay Time	t <sub>d(off)</sub>	P-Channel	N-Ch		25	40	ns		
		$V_{DD} = -10 \text{ V}, R_{L} = 10 \Omega$	P-Ch		55	85	-		
Fall Time		$I_D \cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_G$ = 6 $\Omega$	N-Ch P-Ch		20 32	30 50			
	ie t <sub>rr</sub>	I <sub>F</sub> = 1.05 A, dI/dt = 100 A/μs	N-Ch		25	40	1		
Source-Drain Reverse Recovery Time					25 25	40	ļ		
		I <sub>F</sub> = - 1.05 A, dl/dt = 100 A/μs P-			20	40			

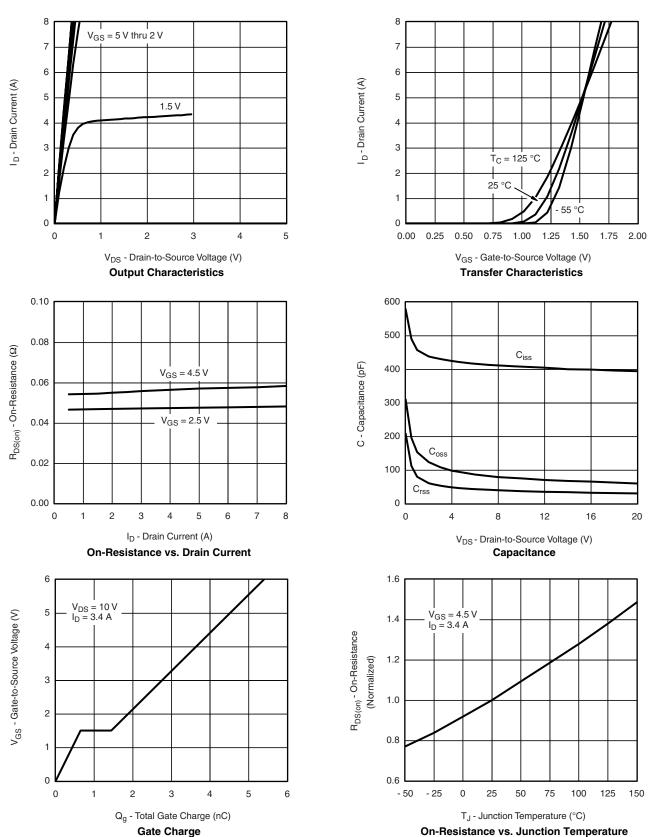
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



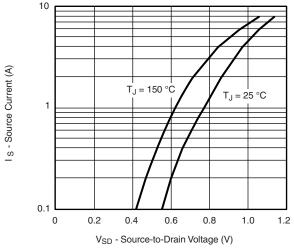


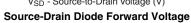
### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

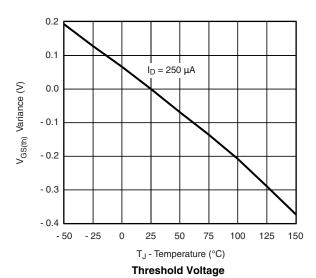


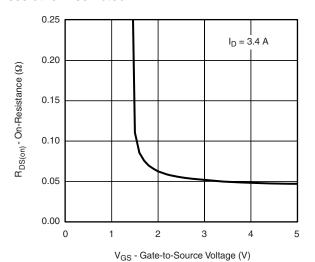
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#### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

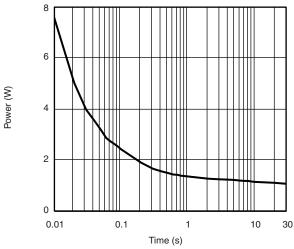




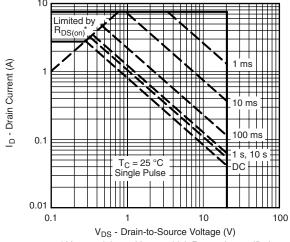




On-Resistance vs. Gate-to-Source Voltage



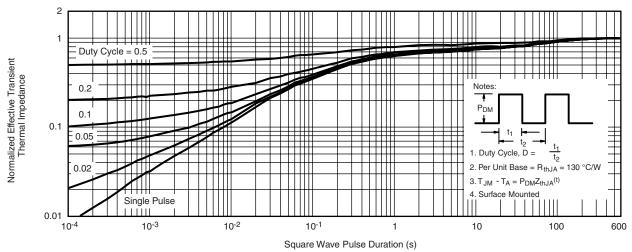
Single Pulse Power (Junction-to-Ambient)



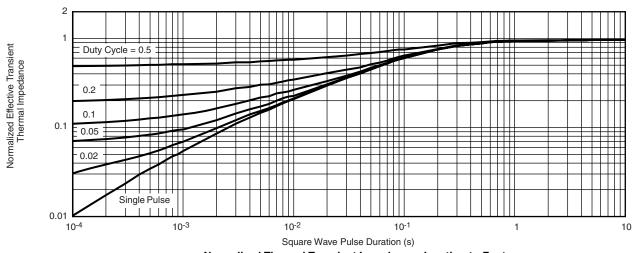
 $^{\star}$   $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



#### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



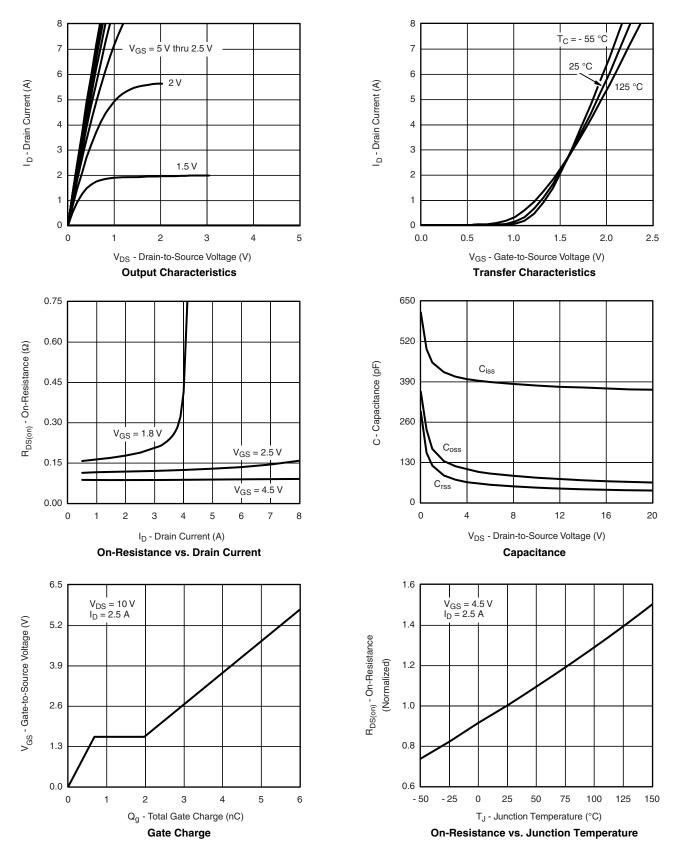
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

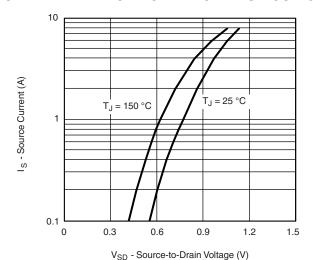


#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

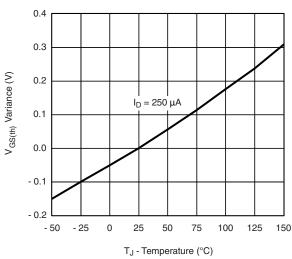




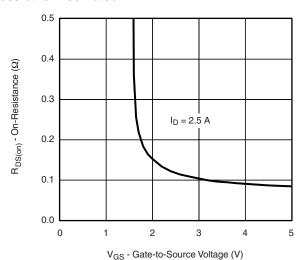
#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



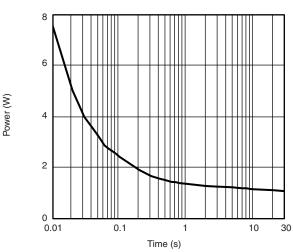
Source-Drain Diode Forward Voltage



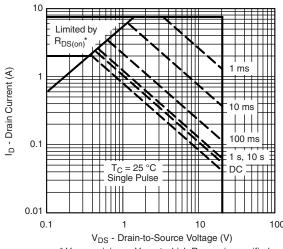
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power (Junction-to-Ambient)

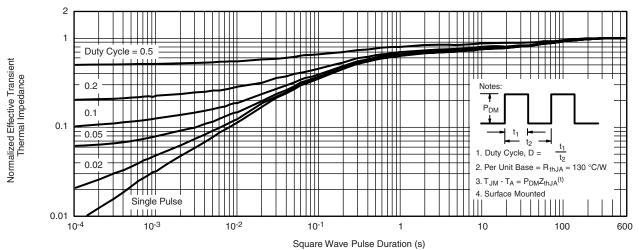


<sup>\*</sup> V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

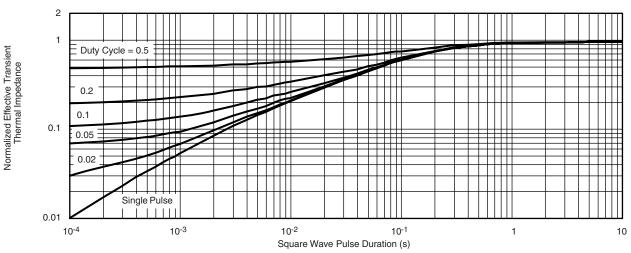
Safe Operating Area, Junction-to-Case



#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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