

Vishay Semiconductors

## **Small Signal Schottky Diodes**



### **DESIGN SUPPORT TOOLS**

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### **MECHANICAL DATA**

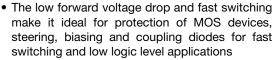
Case: SOD-123

Weight: approx. 9.4 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

#### **FEATURES**

For general purpose applications





 The SD101 series is a metal-on-silicon Schottky barrier device which is protected by a PN junction guardring

ROHS
COMPLIANT
HALOGEN
FREE
GREEN

- AEC-Q101 qualified available (part number on request)
- Base P/N-G3 green, commercial grade
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

PARTS TABLE					
PART	ORDERING CODE CIP		TYPE MARKING	REMARKS	
SD101AW-G	SD101AW-G3-08 or SD101AW-G3-18	Single	SK		
SD101BW-G	SD101BW-G3-08 or SD101BW-G3-18	Single	SL	Tape and reel	
SD101CW-G	SD101CW-G3-08 or SD101CW-G3-18	Single	SM		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
		SD101AW-G	$V_{RRM}$	60	V	
Repetitive peak reverse voltage		SD101BW-G	$V_{RRM}$	50	V	
		SD101CW-G	$V_{RRM}$	40	V	
Power dissipation (infinite heatsink) (1)			P <sub>tot</sub>	400	mW	
Forward continuous current			l <sub>F</sub>	30	mA	
Maximum single cycle surge	10 μs square wave		I <sub>FSM</sub>	2	Α	

#### Note

(1) Valid provided that electrodes are kept at ambient temperature

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air (1)		$R_{thJA}$	300	K/W		
Junction temperature (1)		Tj	125	°C		
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C		
Operating ttemperature range		T <sub>op</sub>	-55 to +125	°C		

### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	Ι <sub>R</sub> = 10 μΑ	SD101AW-G	V <sub>(BR)</sub>	60			V
Reverse breakdown voltage		SD101BW-G	V <sub>(BR)</sub>	50			V
		SD101CW-G	V <sub>(BR)</sub>	40			V
Leakage current	V <sub>R</sub> = 50 V	SD101AW-G	I <sub>R</sub>			200	nA
	V <sub>R</sub> = 40 V	SD101BW-G	I <sub>R</sub>			200	nA
	V <sub>R</sub> = 30 V	SD101CW-G	I <sub>R</sub>			200	nA
	I <sub>F</sub> = 1 mA	SD101AW-G	$V_{F}$			410	mV
		SD101BW-G	$V_{F}$			400	mV
Converd valtage drep		SD101CW-G	$V_{F}$			390	mV
Forward voltage drop		SD101AW-G	$V_{F}$			1000	mV
	I <sub>F</sub> = 15 mA	SD101BW-G	$V_{F}$			950	mV
		SD101CW-G	$V_{F}$			900	mV
	V <sub>R</sub> = 0 V, f = 1 MHz	SD101AW-G	C <sub>D</sub>			2	pF
Diode capacitance		SD101BW-G	C <sub>D</sub>			2.1	pF
		SD101CW-G	C <sub>D</sub>			2.2	рF
Reverse recovery time	$I_F = I_R = 5$ mA, recover to 0.1 $I_R$		t <sub>rr</sub>			1	ns

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

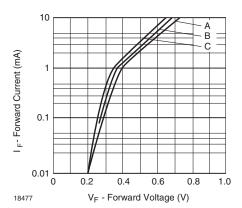


Fig. 1 - Typical Variation of Forward Current vs. Forward Voltage

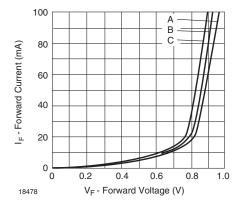


Fig. 2 - Typical Forward Conduction Curve

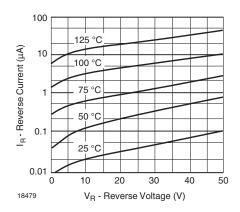


Fig. 3 - Typical Variation of Reverse Current at Various Temperatures

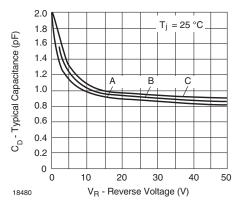


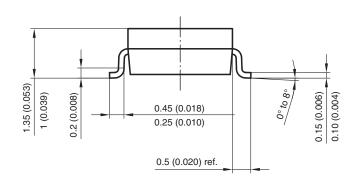
Fig. 4 - Typical Capacitance Curve as a Function of Reverse Voltage

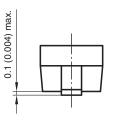


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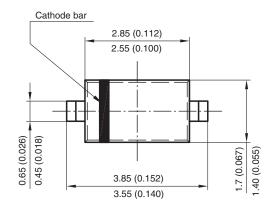
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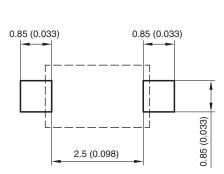
### PACKAGE DIMENSIONS in millimeters (inches): SOD-123





Mounting Pad Layout





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