



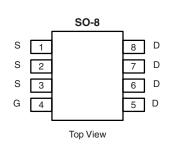
## N-Channel 200-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
200	0.080 at V <sub>GS</sub> = 10 V	4.0			
	0.090 at V <sub>GS</sub> = 6.0 V	3.8			

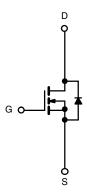
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si4490DY-T1-E3 (Lead (Pb)-free) Si4490DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

Parameter	Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		$V_{DS}$	200		V
Gate-Source Voltage		$V_{GS}$	± 20		V
Ocaliana Daria Ocaza / T. 450 00/8	T <sub>A</sub> = 25 °C	1	4.0	2.85	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C	I <sub>D</sub>	3.2	2.3	
Pulsed Drain Current		I <sub>DM</sub>	40		Α
Avalanch Current	L = 0.1 mH	I <sub>AS</sub>	15		
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.6	1.3	
Mariana Barra Birainating a	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.1	1.56	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	Т ' D	2.0	1.0	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Marine Landing to Australia	t ≤ 10 s	R <sub>thJA</sub>	33	40	°C/W	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		65	80		
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	17	21		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

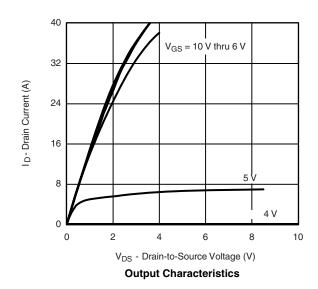
## Vishay Siliconix

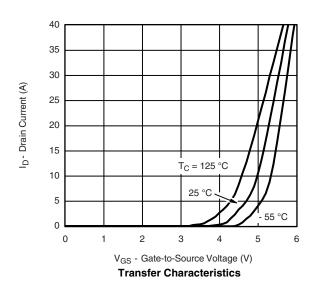


<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zoro Coto Voltago Droin Current	lasa	V <sub>DS</sub> = 160 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 160 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			5	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			Α	
	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4.0 A		0.065	0.080	0	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 6.0 \text{ V}, I_D = 4.0 \text{ A}$		0.070	0.090	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5 A		19		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 2.8 A, V <sub>GS</sub> = 0 V		0.75	1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge Q <sub>g</sub>				34	42		
Gate-Source Charge	$Q_{gs}$			7.5		nC	
Gate-Drain Charge	$Q_{gd}$			12.0			
Gate Resistance	$R_{g}$		0.2	0.85	1.3	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			14	20		
Rise Time t <sub>r</sub>		$V_{DD} = 100 \text{ V}, R_{L} = 25 \Omega$		20	30	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 4.0 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		32	50		
Fall Time	t <sub>f</sub>			25	35		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.8 A, dI/dt = 100 A/μs		70	100		

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



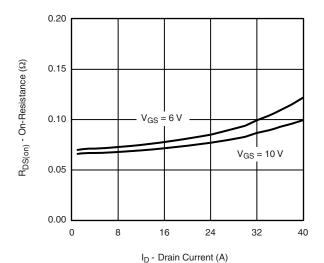


Notes: a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

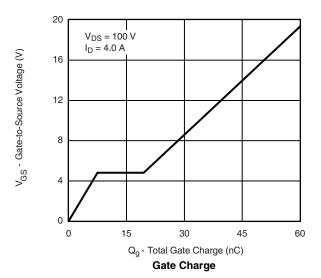


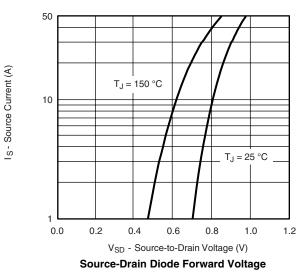


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



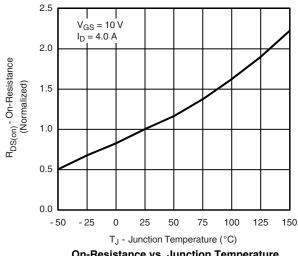
On-Resistance vs. Drain Current



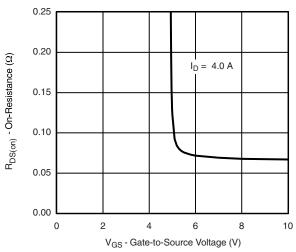


2500 2000  $\mathsf{C}_{\mathsf{iss}}$ C - Capacitance (pF) 1500 1000 500  $C_{oss}$ 0 40 80 120 200

V<sub>DS</sub> - Drain-to-Source Voltage (V) Capacitance



On-Resistance vs. Junction Temperature

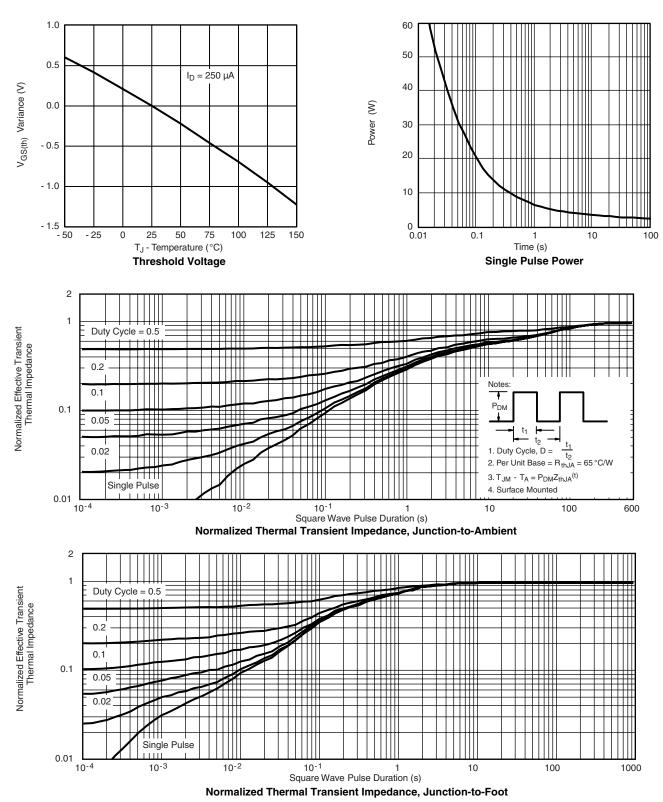


On-Resistance vs. Gate-to-Source Voltage

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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

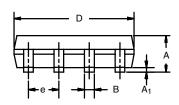


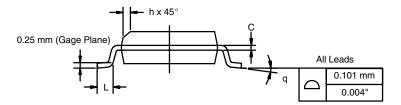
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?71341">www.vishay.com/ppg?71341</a>.



**SOIC (NARROW): 8-LEAD** JEDEC Part Number: MS-012







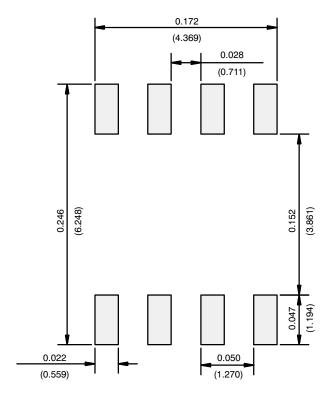
	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

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### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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