

Vishay Siliconix

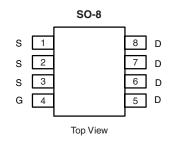
N-Channel 30-V (D-S) MOSFET

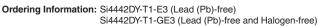
PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)		
30	0.0045 at V _{GS} = 10 V	22		
	0.005 at V _{GS} = 4.5 V	19		
	0.0075 at V _{GS} = 2.5 V	17		

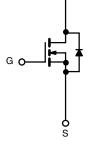
FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFETs: 2.5 V Rated
- 100 % R_g Tested •









N-Channel MOSFET

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ABSOLUTE MAXIMUM RATINGS $T_A = 25 \degree C$, unless otherwise noted					
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 12		v
	T _A = 25 °C	– I _D	22	15	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		17	11	
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	60		A
Continuous Source Current (Diode Conduction) ^a		۱ _S	2.9	1.3	
	T _A = 25 °C	P _D 3.5 1.6 2.2 1	1.6	W	
Maximum Power Dissipation ^a	T _A = 70 °C		2.2	1	VV I
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R _{thJA}	29	35	
Maximum Junction-to-Ambient ^a	Steady State		67	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	13	16	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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SPECIFICATIONS $T_J = 25 \circ C$ Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	-,			- 76.		5111	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	IA 0.6		1.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
	I _{DSS}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$			5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$ 30			А	
		V _{GS} = 10 V, I _D = 22 A		0.0035	0.0045		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 19 A		0.0041	0.005	Ω	
				0.0062	0.0075		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 22 A		100		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = 2.9 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.1	V	
Dynamic ^b							
Total Gate Charge	Qg			36	50		
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 22 A		8		nC	
Gate-Drain Charge	Q _{gd}			10.5			
Gate Resistance	Rg		0.5	1.5	2.6	Ω	
Turn-On Delay Time	t _{d(on)}			17	30		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		11	20		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong\text{1}$ A, V_GEN = 10 V, R_g = 6 Ω		125	180	ns	
Fall Time	t _f			47	70		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.9 A, dl/dt = 100 A/μs		50	80		

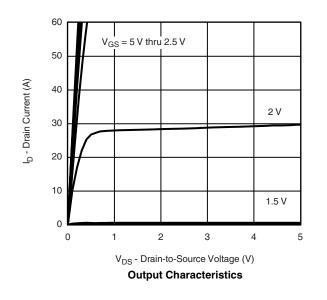
Notes:

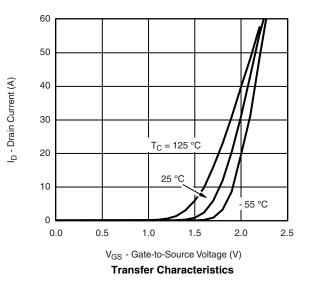
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



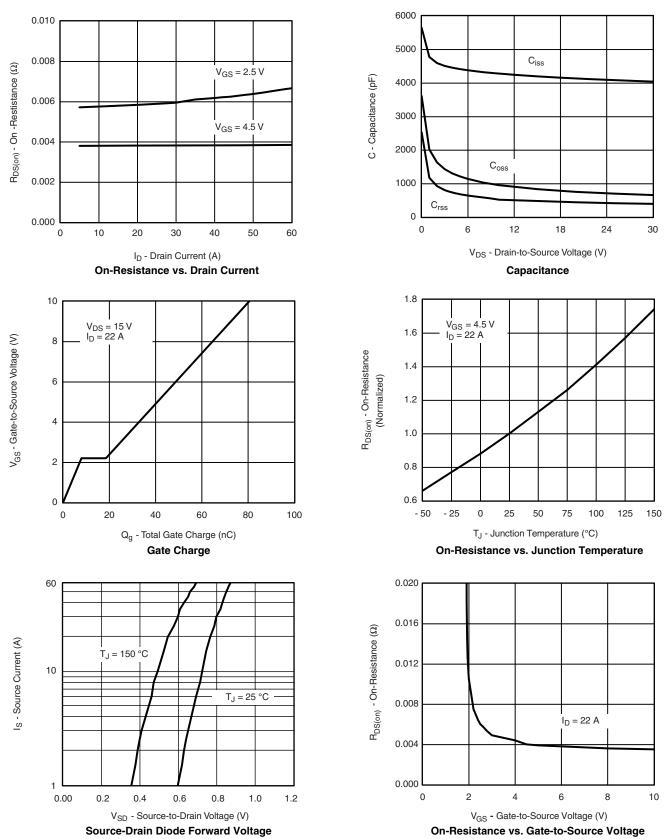


Si4442DY

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

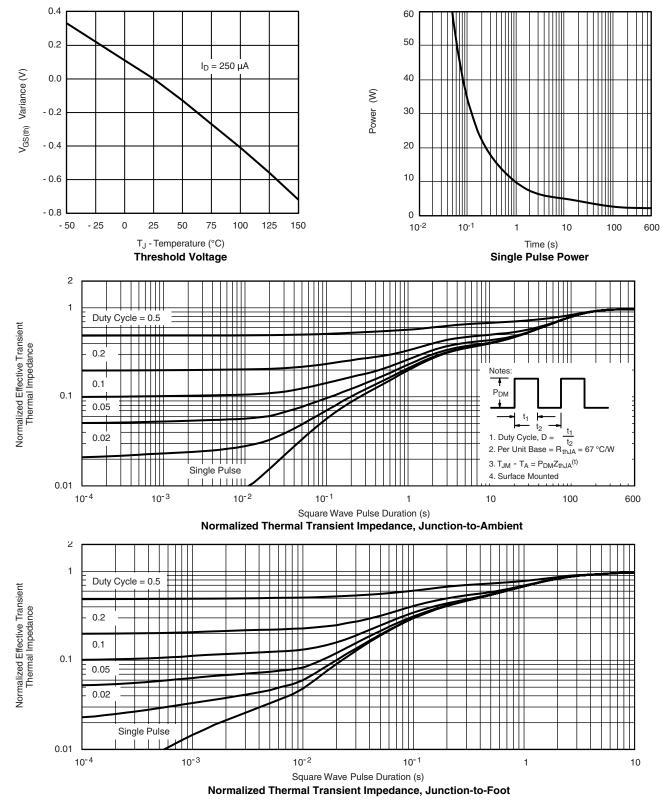
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Si4442DY

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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?71358</u>.

/ISHA



Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012





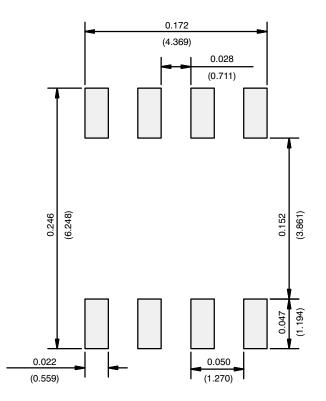
	MILLIM	IETERS	INCHES			
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

Application Note 826

Vishay Siliconix



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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