**Vishay Semiconductors** 

# High Performance Schottky Rectifier, 200 A



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PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	200 A		
V <sub>R</sub>	50 V		
Package	TO-244		
Circuit configuration	Two diodes common cathode		

## **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Center tap module
- Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Designed and gualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

The VS-201CNQ050PbF center tap Schottky rectifier module has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	200	А		
V <sub>RRM</sub>	50		V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	16 000	А		
V <sub>F</sub>	100 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.58	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	201CNQ050PbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	50	M	
Maximum working peak reverse voltage	V <sub>RWM</sub>	50	v	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	. TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per device	<b>I</b> =	50 % duty cycle at T <sub>C</sub> = 146 °C, rectangular waveform		200	А
See fig. 5	per leg	$I_{F(AV)}$ 50 % duty cycle at $I_C = 146$ °C, rectangular waveform		$50\%$ duty cycle at $T_{\rm C} = 140$ C, rectangular wavelonn	100	~
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I <sub>FSM</sub>	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with	16 000	A
			10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	2000	
Non-repetitive avalanche	energy per leg	E <sub>AS</sub>	$E_{AS}$ T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 17 A, L = 1 mH		145	mJ
Repetitive avalanche curre	ent per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		20	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	100 A	T <sub>J</sub> = 25 °C	0.67	V
Maximum forward voltage drop per leg		200 A		0.81	
See fig. 1		100 A	- T <sub>J</sub> = 125 °C	0.58	
		200 A		0.71	
Maximum reverse leakage current per leg See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B} = Rated V_{\rm B}$	10	mA
		T <sub>J</sub> = 125 °C	VR = naleu VR	90	
Maximum junction capacitance per leg	CT	$V_{R}$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 $^{\circ}\mathrm{C}$		5200	pF
Typical series inductance per leg	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

Note

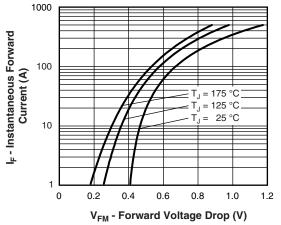
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

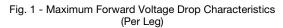
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>	- 55	-	175	°C	
Thermal resistance, junction to case	P	-	-	0.38	°C/W	
per module	R <sub>thJC</sub>	-	-	0.19		
Thermal resistance, case to heatsink	R <sub>thCS</sub>	-	0.10	-		
Weight			68		g	
weight	-	-	2.4		oz.	
Mounting torque		35.4 (4)	-	53.1 (6)		
Mounting torque center hole		30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)	
Terminal torque		30 (3.4)	-	44.2 (5)		
Vertical pull		-	-	80	- lbf ⋅ in	
2" lever pull			35	חויזטו		

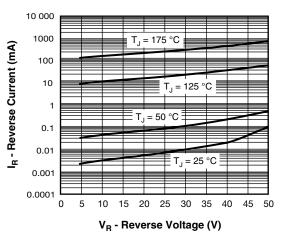


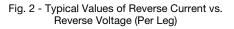
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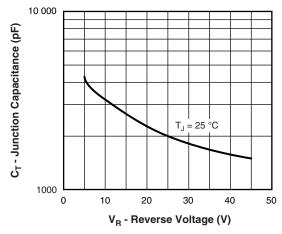


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

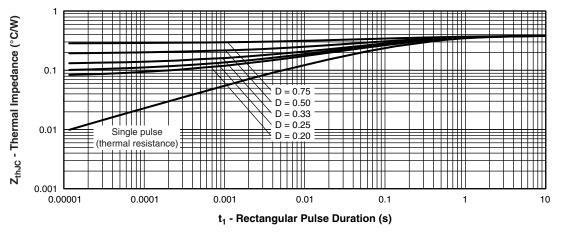


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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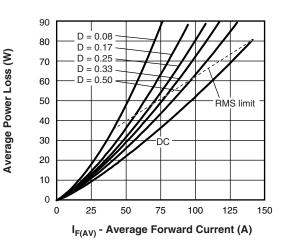
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#### 180 Allowable Case Temperature (°C) 170 160 ĎС 150 140 130 Square wave (D = 0.50) 80 % rated V<sub>R</sub> applied 120 110 100 See note (1) 90 0 20 40 60 80 100 120 140 160 I<sub>F(AV)</sub> - Average Forward Current (A)

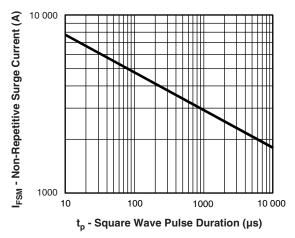




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Fig. 6 - Forward Power Loss Characteristics (Per Leg)





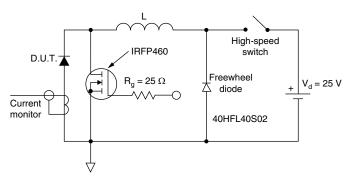


Fig. 8 - Unclamped Inductive Test Circuit

### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \ - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

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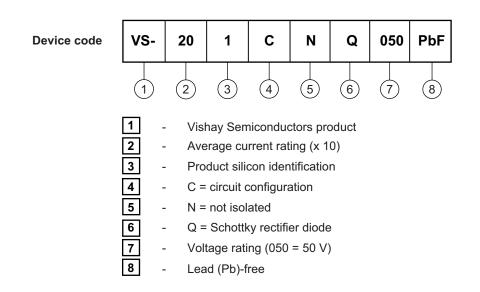
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