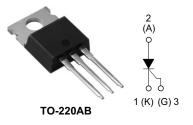


Vishay High Power Products

Phase Control SCR, 10 A



PRODUCT SUMMARY			
V _T at 10 A	< 1.4 V		
I _{TSM}	200 A		
V _{RRM}	800/1200 V		

DESCRIPTION/FEATURES

The 16TTS.. High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level.

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS		
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C, common heatsink of 1 °C/W	13.5	17	А		

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	10	А		
I _{RMS}		16	A		
V _{DRM} /V _{RRM}	Range ⁽¹⁾	800/1200	V		
I _{TSM}		200	А		
V _T	10 A, T _J = 25 °C	1.4	V		
dV/dt		500	V/µs		
dl/dt		150	A/µs		
TJ	Range	- 40 to 125	°C		

Note

⁽¹⁾ For higher voltage up to 1600 V contact factory

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
16TTS08	800	10				
16TTS12	1200	1200	10			

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DADAMETED				VALUES			
PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS		
Maximum average on-state current	I _{T(AV)}	T _C = 98 °C, 180° conduction, half sine wave		10			
Maximum RMS on-state current	I _{RMS}		16		1.		
Maximum peak, one-cycle,	1	10 ms sine pulse, rated V _{RRM} applied	170		A		
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no voltage reapplied	2	200			
Maximum 12t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	1	44	120		
Maximum I ² t for fusing	I ² t	10 ms sine pulse, no voltage reapplied	200		A ² s		
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied		2000			
Maximum on-state voltage drop	V _{TM}	10 A, T _J = 25 °C		1.4			
On-state slope resistance	r _t	– T _J = 125 °C		Z4.0		4.0	mΩ
Threshold voltage	V _{T(TO)}			.1	V		
	1 //	$T_J = 25 \text{ °C}$	0.5		_		
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	$V_{R} = Rated V_{RRM}/V_{DRM}$		0			
Holding current	Ι _Η	Anode supply = 6 V, resistive load, initial $I_T = 1 A$ 16TTS08, 16TTS12		100	mA		
Maximum latching current	١L	Anode supply = 6 V, resistive load		00			
Maximum rate of rise of off-state voltage	dV/dt		5	00	V/µs		
Maximum rate of rise of turned-on current	dl/dt		1	50	A/µs		

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P _{GM}		8.0	W	
Maximum average gate power	P _{G(AV)}		2.0	vV	
Maximum peak positive gate current	+ I _{GM}		1.5	А	
Maximum peak negative gate voltage	- V _{GM}		10	V	
Maximum required DC gate current to trigger	I _{GT}	Anode supply = 6 V, resistive load, $T_J = -65 \ ^{\circ}C$	90	mA	
		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	60		
		Anode supply = 6 V, resistive load, $T_J = 125 \ ^{\circ}C$	35		
	V _{GT}	Anode supply = 6 V, resistive load, $T_J = -65 \ ^{\circ}C$	3.0		
Maximum required DC gate voltage to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	2.0	v	
voluge to trigger		Anode supply = 6 V, resistive load, $T_J = 125 \ ^{\circ}C$	1.0	v	
Maximum DC gate voltage not to trigger	V_{GD}	T 105 °C V Batad value	0.2		
Maximum DC gate current not to trigger	I _{GD}	$T_J = 125 \text{ °C}, V_{DRM} = \text{Rated value}$		mA	

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9	
Typical reverse recovery time	t _{rr}	T - 125 °C	4	μs
Typical turn-off time	tq	T _J = 125 °C		



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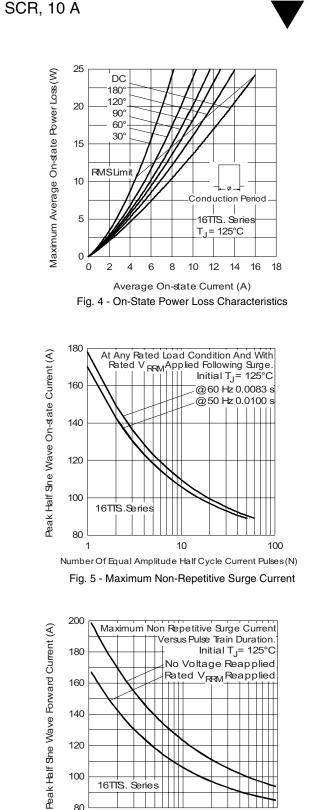
THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	1.3		
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5		
Approvimete weight			2	g		
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
	maximum			12 (10)	(lbf ⋅ in)	
				16T	FS08	
Marking device			Case style TO-220AB	16T	16TTS12	

16TTS.. High Voltage Series

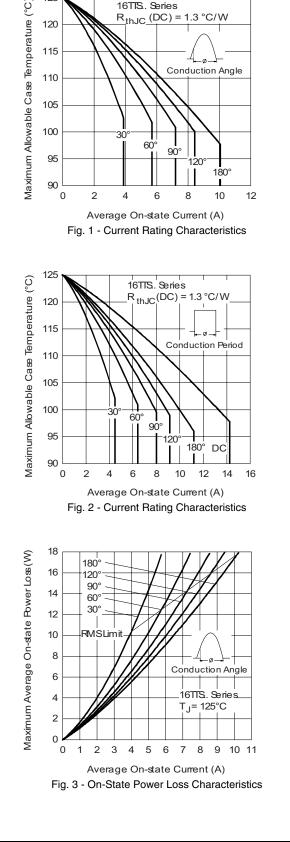
16TTS. Series

125

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0.1

100

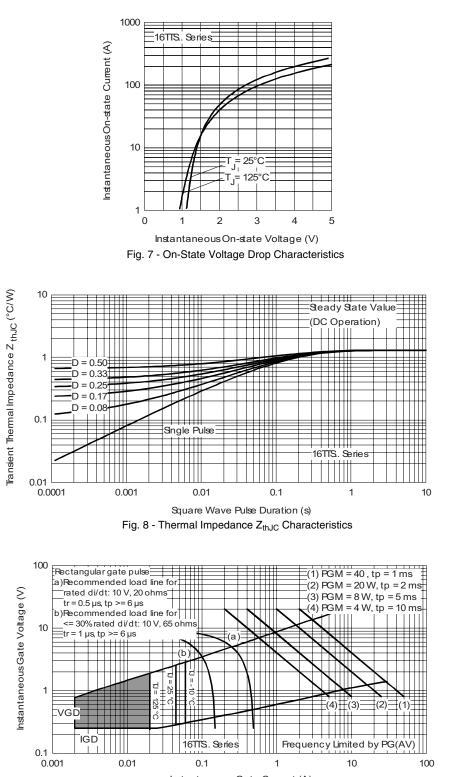
80

0.01

16TTS. Series



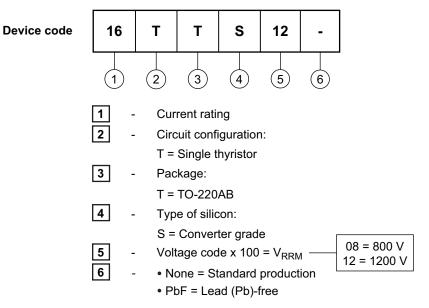
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Instantaneous Gate Current (A) Fig. 9 - Gate Characteristics Vishay High Power Products Phase Control SCR, 10 A



ORDERING INFORMATION TABLE



Note: For higher voltage up to 1600 V contact factory

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95222			
Part marking information	http://www.vishay.com/doc?95225		



Vishay

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