- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <a href="http://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### DESCRIPTION

The VS-12CWQ06FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I <sub>F(AV)</sub>	Rectangular waveform	12	A				
V <sub>RRM</sub>		60	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	320	A				
V <sub>F</sub>	$6 A_{pk}, T_J = 125 \ ^{\circ}C \ (per \ leg)$	0.57	V				
TJ	Range	- 55 to 150	°C				

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-12CWQ06FNHM3	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	60	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	00		

ABSOLUTE MAXIMU		GS				
PARAMETER		SYMBOL	TEST CONDI	VALUES	UNITS	
Maximum average	per leg			6	А	
See fig. 5 per device		I <sub>F(AV)</sub>	50 / 000 your at 10 - 151 O, rectaliguial waveloini		12	~
Maximum peak one cycle non-repetitive surge current See fig. 7		I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	320	A
			10 ms sine or 6 ms rect. pulse	rated $V_{RRM}$ applied	105	
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.2 A, L = 10 mH		7	mJ
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu s$ Frequency limited by T_J maximum V_A = 1.5 x V_R typical		0.8	А

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## Schottky Rectifier, 2 x 6 A

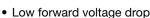
# D-PAK (TO-252AA)

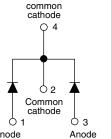
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PRODUCT SUMMARY	
Package	D-PAK (TO-252AA)
I <sub>F(AV)</sub>	2 x 6 A
V <sub>R</sub>	60 V
V <sub>F</sub> at I <sub>F</sub>	0.57 V
I <sub>RM</sub>	35 mA at 125 °C
T <sub>J</sub> max.	150 °C
Diode variation	Common cathode
E <sub>AS</sub>	7 mJ

#### FEATURES





Base



## VS-12CWQ06FNHM3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
		6 A	T <sub>.1</sub> = 25 °C	0.61	V	
Maximum forward	V <sub>FM</sub> <sup>(1)</sup>	12 A	1j=25 0	0.79		
voltage drop per leg See fig. 1	VFM (")	6 A	− T <sub>1</sub> = 125 °C	0.57		
		12 A	1J = 125 C	0.72		
Maximum reverse	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		3	mA	
leakage current per leg See fig. 2		T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	35		
Threshold voltage	V <sub>F(TO)</sub>				V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		24.14	mΩ	
Typical junction capacitance per leg	CT	$V_R$ = 5 $V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C		360	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.0 nH			nH	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

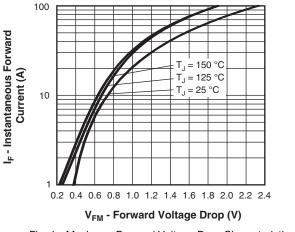
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		$T_{J}$ <sup>(1)</sup> , $T_{Stg}$		- 55 to 150	°C
Maximum thermal resistance, junction to case po	per leg	R <sub>th-IC</sub>	DC operation	3.0	°C/W
	per device		See fig. 4	1.5	
				0.3	g
Approximate weight				0.01	oz.
Marking device			Case style D-PAK	12CWC	06FNH

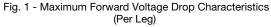
#### Note

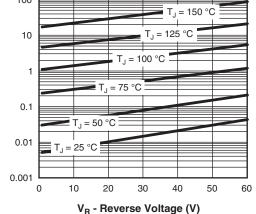
 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink (1)

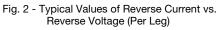
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I<sub>R</sub> - Reverse Current (mA)









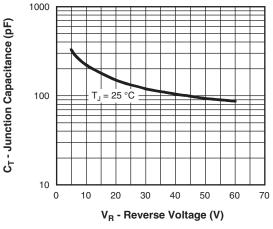


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

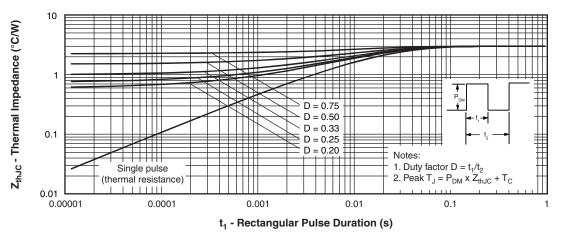
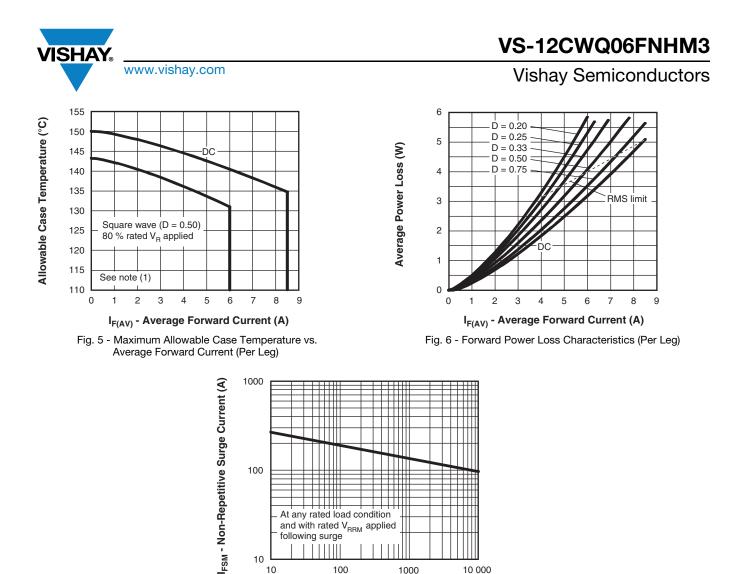


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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t<sub>p</sub> - Square Wave Pulse Duration (μs)

1000

10 000

100

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

10

<sup>&</sup>lt;sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

## VS-12CWQ06FNHM3

## Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

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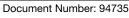
Device code	VS-	12	С	w	Q	06	FN	TRL	Н	М3
		2	3	4	5	6	7	8	9	10
	1	- Visl	nay Sen	nicondu	ctors pro	oduct				
	2	- Cur	rent rati	ng (12 A	4)					
	3	- Cer	nter tap	configur	ation					
	4	- Pac	kage id	entifier:						
		W =	D-PAK							
	5	- Sch	ottky "C	" series	;					
	6	- Vol	tage rati	ng (06 =	= 60 V)					
	7	- FN	= TO-25	52AA						
	8	- • N	one = T	ube						
		• TI	R = Tap	e and re	el					
		• TI	RL = Ta	pe and r	reel (left	oriente	d)			
		• TI	RR = Ta	pe and	reel (rig	ht orien	ted)			
	9	- H=	AEC-Q	101 qua	alified					
	10		vironmer							
	لتن			-		complia	nt. and	termina	tions le	ad (Pb)-i
							.,			()

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-12CWQ06FNHM3	75	3000	Antistatic plastic tube				
VS-12CWQ06FNTRHM3	2000	2000	13" diameter reel				
VS-12CWQ06FNTRRHM3	3000	3000	13" diameter reel				
VS-12CWQ06FNTRLHM3	3000	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95519		
Part marking information	www.vishay.com/doc?95518		
Packaging information	www.vishay.com/doc?95033		

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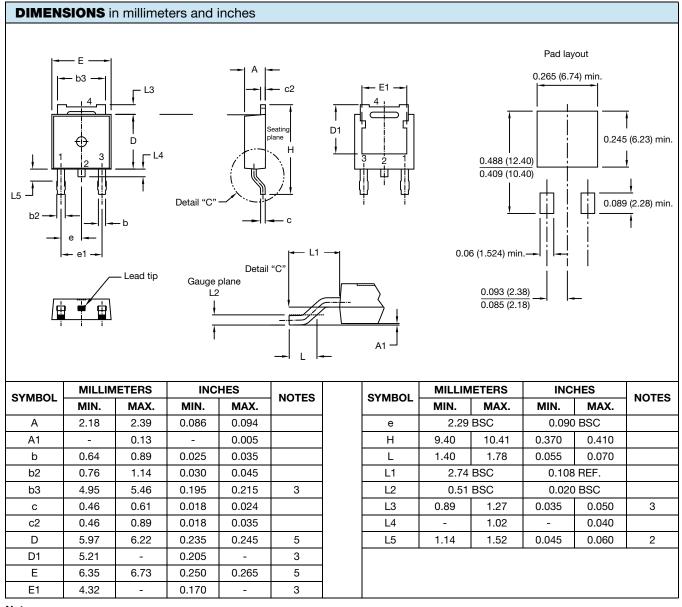


## **Outline Dimensions**



**Vishay Semiconductors** 

## DPAK (TO-252AA)



#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension uncontrolled in L5

<sup>(3)</sup> Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

<sup>(4)</sup> Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(5)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-252AA

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