



# N-Channel 12 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)	
	0.095 at V <sub>GS</sub> = 4.5 V	1.32		
12	0.104 at V <sub>GS</sub> = 2.5 V	1.26	5.25	
	0.114 at V <sub>GS</sub> = 1.8 V	0.88		

### **FEATURES**

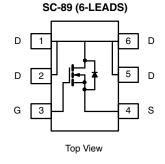
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R<sub>q</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

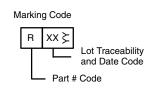


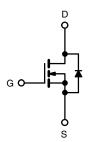
ROHS COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

· Load Switch for Portable Devices







Ordering Information: Si1054X-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	12	V	
Gate-Source Voltage		V <sub>GS</sub>	± 8	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Continuous Drain Current (T <sub>.I</sub> = 150 °C)	T <sub>A</sub> = 25 °C	-	1.32 <sup>b, c</sup>		
Continuous Diairi Current (1) = 130 °C)	T <sub>A</sub> = 70 °C	- I <sub>D</sub>  -	1.05 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	6	7	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	Is	0.2 <sup>b, c</sup>	$\neg$	
N	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.236 <sup>b, c</sup>	w	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	] '	0.151 <sup>b, c</sup>	T vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	440	530	°C/W	
	Steady State	' 'thJA	540	650	C/ <b>VV</b>	

#### Notes:

- a. Based on  $T_A = 25$  °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 650 °C/W.

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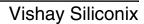


Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	12			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		12.23		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 2.76			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.4		1	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
7 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	I <sub>DSS</sub>	V <sub>DS</sub> = 12 V, V <sub>GS</sub> = 0 V			1	nA	
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 12 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C			10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	6			Α	
Drain-Source On-State Resistance <sup>a</sup>	( )	$V_{GS} = 4.5 \text{ V}, I_D = 1.32 \text{ A}$		0.079	0.095	Ω	
	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 1.26 \text{ A}$		0.087	0.104		
		$V_{GS} = 1.8 \text{ V}, I_D = 0.88 \text{ A}$		0.095	0.114		
Forward Transconductance	9 <sub>fs</sub>	$V_{DS} = 4.5 \text{ V}, I_D = 1.32 \text{ A}$		6.25		S	
Dynamic <sup>b</sup>					I.	'	
Input Capacitance	C <sub>iss</sub>			480			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 6 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		142		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			92		1	
T. 10 . 0	Qg	$V_{DS} = 6 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 1.32 \text{ A}$	5.71 8.5		8.57		
Total Gate Charge				5.25	7.9	nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 6 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 1.32 \text{ A}$		0.83			
Gate-Drain Charge	Q <sub>gd</sub>			1.54			
Gate Resistance	$R_{g}$	f = 1 MHz		3.5	5.25	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			5.5	8.25		
Rise Time $t_r$		$V_{DD} = 6 \text{ V}, R_L = 5.71 \Omega$		13	19.5	1	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 1.05 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		37	55.5	ns	
Fall Time	t <sub>f</sub>			14	21		
<b>Drain-Source Body Diode Characteristic</b>	s					1	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				6	А	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1.0 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			19.3	28.95	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1 10 4 11/11 100 4/		5.8	8.7	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 1.0 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}$		7.4		- ns	
Reverse Recovery Rise Time	t <sub>b</sub>			11.9			

#### Notes:

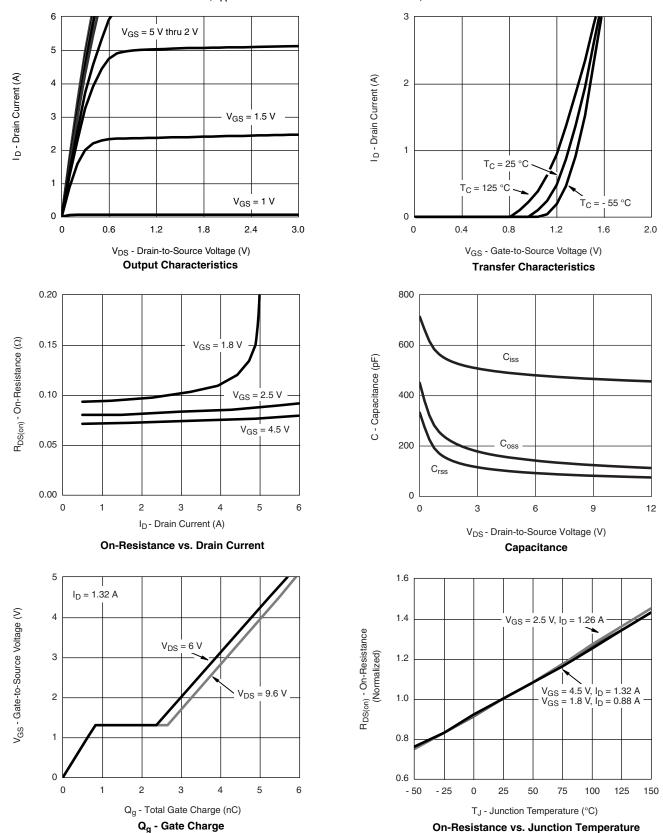
- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



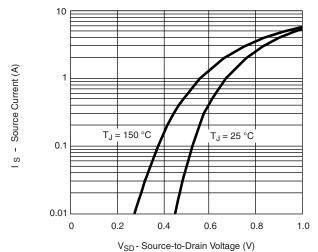


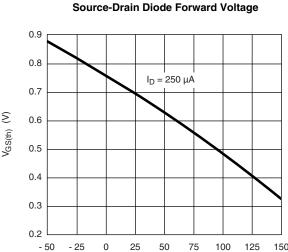
# **TYPICAL CHARACTERISTICS** ( $T_A = 25$ °C, unless otherwise noted)



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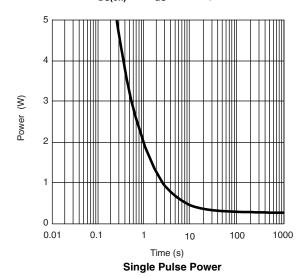


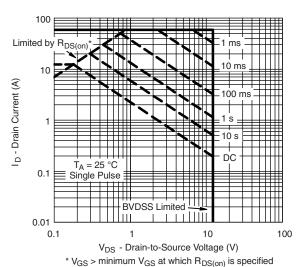
T<sub>J</sub> - Temperature (°C)

**Threshold Voltage** 

I<sub>D</sub> = 1.32 A R<sub>DS(on)</sub> - On-Resistance (Ω) 0.12 T<sub>A</sub> = 125 °C 0.09  $T_A = 25$  °C 0.06 0.03 V<sub>GS</sub> - Gate-to-Source Voltage (V)

 $R_{DS(on)}$  vs.  $V_{GS}$  vs. Temperature



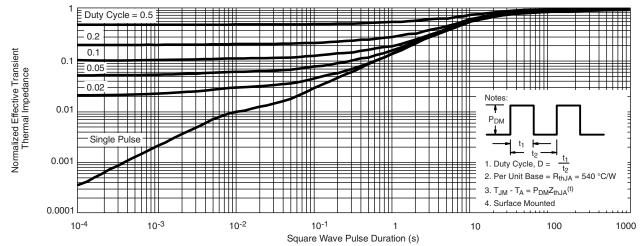


Safe Operating Area, Junction-to-Ambient





# **TYPICAL CHARACTERISTICS** ( $T_A = 25$ °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

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