

FRED Pt® Gen 4 Single Ultrafast Diode, 500 A (INT-A-PAK Power Modules)



INT-A-PAK

PRIMARY CHARACTERISTICS					
V _R 600 V					
I _{F(AV)} at T _C	500 A at 55 °C				
t _{rr} at 25 °C 104 ns					
Туре	Modules - diode, FRED Pt®				
Package INT-A-PAK					
Circuit configuration	Single diode				

FEATURES

- Gen 4 FRED Pt[®] dices technology
- Ultrasoft reverse recovery characteristics
- Low I_{RRM} and reverse recovery charge
- Very low forward voltage drop
- 175 °C operating junction temperature
- UL approved file E78996 for application with maximum case temperature up to 140 °C
- Large creepage distances
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

Gen 4 FRED Pt technology, state of the art, ultra low V_F, soft switching optimized for IGBT F/W diode.

The minimized conduction loss, optimized storage charge, and low recovery current, minimized the switching losses and reduce the over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		600	V	
Continuous forward current		T _C = 25 °C	772		
Continuous forward current	I _F	T _C = 90 °C	519	A	
Single pulse forward current	I _{FSM}	t_p = 10 ms, 50 Hz, sine half wave, initial T_J = 175 °C	4500		
Martin	P _D	T _C = 25 °C	1363	W	
Maximum power dissipation		T _C = 90 °C	772	- vv	
Operating junction temperature range	ting junction temperature range T _J		-40 to +175	°C	
Storage temperature range	T _{Stg}		-40 to +150]	
RMS insulation voltage	V _{INS}	50 Hz, circuit to base, all terminals shorted, t = 1 s	3500	V	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 500 μA	600	-	-	
		I _F = 250 A	-	1.25	-	
For and allowed to a	V_{FM}	I _F = 500 A	-	1.45	1.66	V
Forward voltage drop		I _F = 250 A, T _J = 150 °C	-	1.23	-	
		I _F = 500 A, T _J = 150 °C	-	1.0	-	
Devenue leeke de euwent	I _{RM}	V _R = 600 V	-	2.0	200	μΑ
Reverse leakage current		T _J = 150 °C, V _R = 600 V	-	1.8	-	mA

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	+	T _J = 25 °C		-	104	-	20
neverse recovery time	t _{rr}	T _J = 125 °C		-	193	-	ns
Dools woody on source out		T _J = 25 °C	I _F = 150 A dI/dt = 1000 A/μs	-	59	-	^
Peak recovery current	l _{rr}	T _J = 125 °C	$V_{R} = 300 \text{ V}$	-	122	-	Α
Dovorgo rocovery charge	0	T _J = 25 °C		-	3.5	-	μC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	13.8	-	μΟ

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum therma		R _{thJC}	DC operation	-	-	0.11	K/W
Typical thermal r case to heat sink		R _{thCS}	Mounting surface, flat, smooth and greased	-	0.035	-	r√ vv
Mounting	to heat sink		A mounting compound is recommended and the	4			Nissa
torque ± 10 %	busbar		torque should be rechecked after a period of 3 hours to allow the spread of the compound.	4	_	6	Nm
Approximate we	iaht			-	200	1	g
Approximate we	igrit			-	7.1	-	oz.
Case style					INT-A	N-PAK	

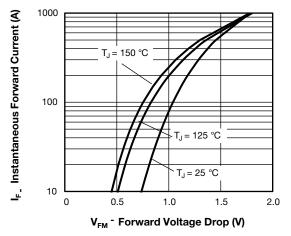


Fig. 1 - Typical Forward Voltage Drop Characteristics

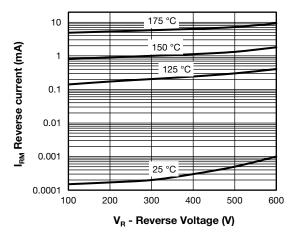


Fig. 2 - Typical Value of Reverse Current vs. Reverse Voltage

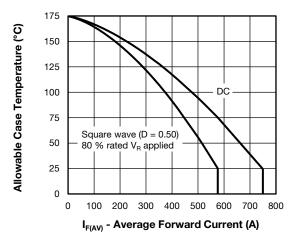


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current

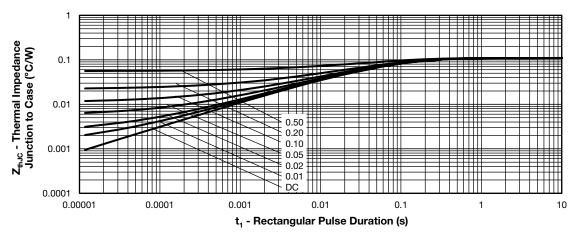


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

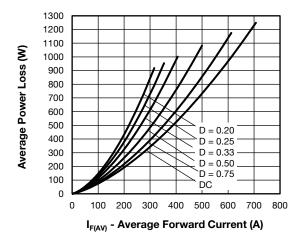


Fig. 5 - Forward Power Loss Characteristics

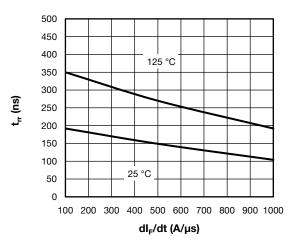


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt $I_{FM} = 150 \text{ A}, V_R = 300 \text{ V}$



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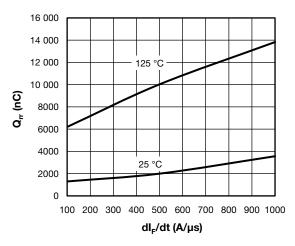


Fig. 7 - Typical Reverse Recovery Charge vs. dI_F/dt $I_{FM} = 150 \text{ A}, V_R = 300 \text{ V}$

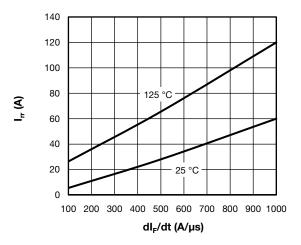
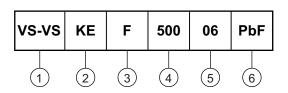


Fig. 8 - Typical Reverse Recovery Current vs. dI_F/dt $I_{FM} = 150 \text{ A}, V_R = 300 \text{ V}$

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

Circuit configuration: KE = single diode

F = FRED Pt[®] ultrafast diode

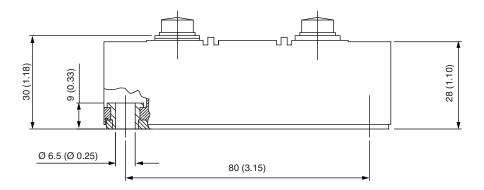
4 - Current rating (500 = 500 A)

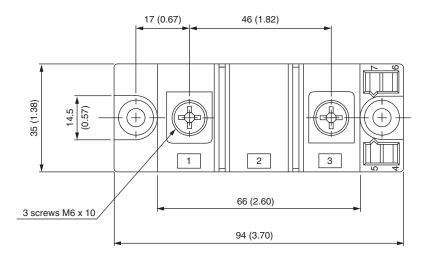
5 - Voltage rating (06 = 600 V)

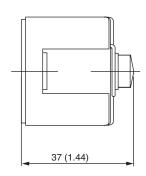
6 - PbF = lead (Pb)-free

CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Single diode	KE	(3)			

DIMENSIONS in millimeters (inches)



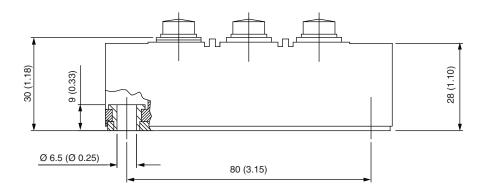


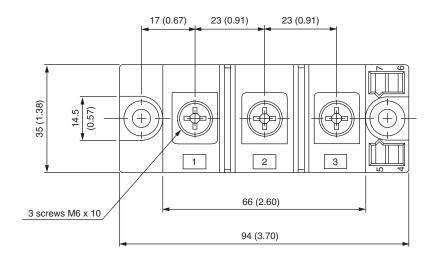


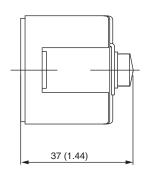


INT-A-PAK DBC

DIMENSIONS in millimeters (inches)







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