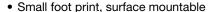


High Performance Schottky Rectifier, 1 A



| PRODUCT SUMMARY | | | |
|----------------------------------|----------------|--|--|
| Package | SMB | | |
| I _{F(AV)} | 1.0 A | | |
| V_{R} | 100 V | | |
| V _F at I _F | 0.78 V | | |
| I _{RM} | 1 mA at 125 °C | | |
| T _J max. | 175 °C | | |
| Diode variation | Single die | | |
| E _{AS} | 1.0 mJ | | |

FEATURES







High frequency operation

- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>
- · Designed and qualified for industrial level

DESCRIPTION

The VS-10BQ100PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS | | | | | |
|-----------------------------------|---|----------------------------|----|--|--|
| SYMBOL | CHARACTERISTICS | CHARACTERISTICS VALUES UNI | | | |
| I _{F(AV)} | Rectangular waveform | 1.0 | A | | |
| V _{RRM} | | 100 | V | | |
| I _{FSM} | t _p = 5 µs sine | 780 | Α | | |
| V _F | 1.0 A _{pk} , T _J = 125 °C | 0.62 | V | | |
| T _J | Range | -55 to +175 | °C | | |

| VOLTAGE RATINGS | | | | |
|--------------------------------------|-----------|---------------|-------|--|
| PARAMETER | SYMBOL | VS-10BQ100PbF | UNITS | |
| Maximum DC reverse voltage | V_{R} | 100 | V | |
| Maximum working peak reverse voltage | V_{RWM} | 100 | V | |

| ABSOLUTE MAXIMUM RATINGS | | | | | |
|---|--------------------|---|---|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum average forward current | I _{F(AV)} | 50 % duty cycle at T _L = 152 °C, rectangular waveform | | 1.0 | Α |
| Maximum peak one cycle non-repetitive surge current | 1 | 5 μs sine or 3 μs rect. pulse | Following any rated load condition and with | 780 | А |
| | IFSM | 10 ms sine or 6 ms rect. pulse | rated V _{RRM} applied | 38 | |
| Non-repetitive avalanche energy | E _{AS} | T _J = 25 °C, I _{AS} = 0.5 A, L = 8 mH | | 1.0 | mJ |
| Repetitive avalanche current | I _{AR} | Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical | | 0.5 | Α |

| ELECTRICAL SPECIFICATIONS | | | | | |
|--|--------------------------------|--|---------------------------------------|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum forward voltage drop See fig. 1 | V _{FM} ⁽¹⁾ | 1 A | T _J = 25 °C | 0.78 | V |
| | | 2 A | | 0.89 | |
| | | 1 A | T _J = 125 °C | 0.62 | |
| | | 2 A | | 0.72 | |
| Maximum reverse leakage current | I _{RM} ⁽¹⁾ | T _J = 25 °C | V _R = Rated V _R | 0.5 | mA |
| See fig. 2 | 'RM ''' | T _J = 125 °C | | 1 | |
| Typical junction capacitance | C _T | $V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C | | 42 | pF |
| Typical series inductance | L _S | Measured lead to lead 5 mm from package body | | 2.0 | nH |
| Maximum voltage rate of charge | dV/dt | Rated V _R 10 0 | | 10 000 | V/µs |

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

| THERMAL - MECHANICAL SPECIFICATIONS | | | | |
|---|--|-----------------------------------|-------------|--------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| Maximum junction and storage temperature range | T _J ⁽¹⁾ , T _{Stg} | | -55 to +175 | °C |
| Maximum thermal resistance, junction to lead | R _{thJL} (2) | DC operation | 36 | °C/W |
| Maximum thermal resistance, junction to ambient | R _{thJA} | | 80 | C/ VV |
| Approximate weight | | | 0.10 | g |
| Approximate weight | | | 0.003 | oz. |
| Marking device | | Case style SMB (similar DO-214AA) | V. | 1J |

Notes

⁽²⁾ Mounted 1" square PCB

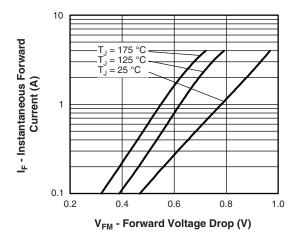


Fig. 1 - Maximum Forward Voltage Drop Characteristics

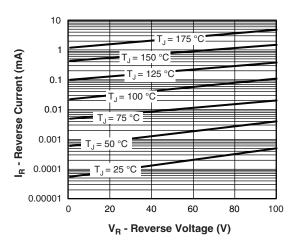


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

⁽¹⁾ $\frac{dP_{tot}}{dT_{.l}} < \frac{1}{R_{th.lA}}$ thermal runaway condition for a diode on its own heatsink

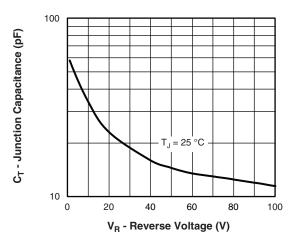


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

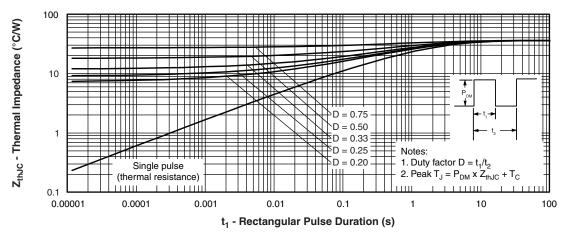


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

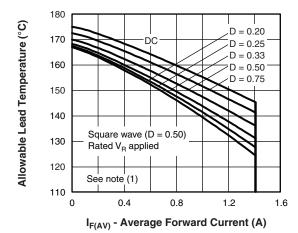


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

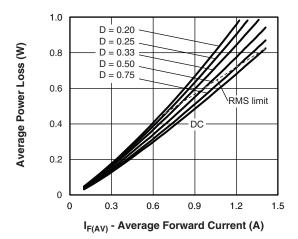


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

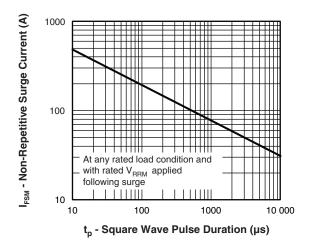


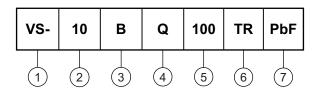
Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} & = \text{Forward power loss} = \text{I}_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (\text{I}_{\text{F(AV)}}/\text{D}) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} & = \text{Inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; \text{I}_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \% \text{ rated } \text{V}_{\text{R}} \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating
- B = single lead diode
- 4 Q = Schottky "Q" series
- 5 Voltage rating (100 = 100 V)
- 6 • None = box (1000 pieces)
 - TR = tape and reel (3000 pieces)
- 7 PbF = lead (Pb)-free

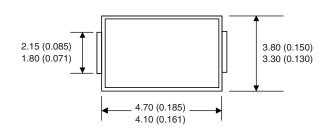
| LINKS TO RELATED DOCUMENTS | | | |
|--|---------------|--------------------------|--|
| Dimensions <u>www.vishay.com/doc?95017</u> | | www.vishay.com/doc?95017 | |
| Part marking information | | www.vishay.com/doc?95029 | |
| Deckering information | Tape and reel | www.vishay.com/doc?95034 | |
| Packaging information | Bulk | www.vishay.com/doc?95397 | |
| SPICE model | | www.vishay.com/doc?95276 | |

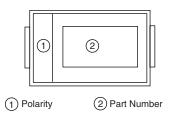


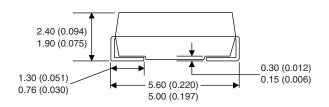
Vishay High Power Products

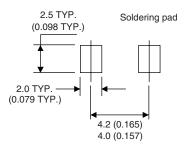
SMB

DIMENSIONS in millimeters (inches)









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Vishay

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