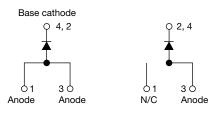


# Ultralow V<sub>F</sub> Ultrafast Rectifier, 15 A FRED Pt<sup>®</sup>



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### TO-252AA (D-PAK)



VS-15AWL06FN-M3

VS-15EWL06FN-M3

PRODUCT SUMMARY								
Package	TO-252AA (D-PAK)							
I <sub>F(AV)</sub>	15 A							
V <sub>R</sub>	600 V							
V <sub>F</sub> at I <sub>F</sub>	0.85 V							
t <sub>rr</sub> (typ.)	60 ns							
T <sub>J</sub> max.	175 °C							
Diode variation	Single die							

## **FEATURES**

- Ultrafast recovery time, extremely low V<sub>F</sub> and soft recovery
- 175 °C maximum operating junction temperature
- For PFC DCM operation
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum *FREE* 
   peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="http://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

## **DESCRIPTION / APPLICATIONS**

State of the art, ultralow  $V_F$ , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V						
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 148 °C	15							
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_J = 25 \ ^{\circ}C$	180	А						
Peak repetitive forward current	I <sub>FM</sub>	$T_{C} = 148 \text{ °C}, f = 20 \text{ kHz}, d = 50 \text{ \%}$	30							
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C						

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-				
Forward voltage	V	I <sub>F</sub> = 15 A	-	0.99	1.05	V			
	V <sub>F</sub>	I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	0.85	0.92				
	1	$V_R = V_R$ rated	-	-	10				
Reverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	120	μA			
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	11	-	pF			
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH			

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### RoHS

COMPLIANT



# VS-15AWL06FN-M3, VS-15EWL06FN-M3

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# Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time		$I_F = 1 \text{ A}, dI_F/dt = 10$	00 A/µs, V <sub>R</sub> = 30 V	-	60	120			
	t <sub>rr</sub>	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 1000 \text{ cm}^{-1}$	-	190	-	ns			
		T <sub>J</sub> = 25 °C		-	220	-	115		
		T <sub>J</sub> = 125 °C	T <sub>J</sub> = 25 °C I <sub>F</sub> = 15 A - dI <sub>F</sub> /dt = 200 A/µs	-	290	-			
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	21	-	А		
Feak recovery current		T <sub>J</sub> = 125 °C		25	-	~			
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	2.6	-			
		T <sub>J</sub> = 125 °C		-	4	-	μC		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C			
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	1.4	1.8	°C/W			
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	70	C/W			
Approximate weight				0.3		g			
Approximate weight				oz.					
Marking device		Case style TO-252AA (D-PAK)	15AWL06FN						
		Case signe 10-202AA (D-FAR)	15EWL06FN						



# VS-15AWL06FN-M3, VS-15EWL06FN-M3

**Vishay Semiconductors** 

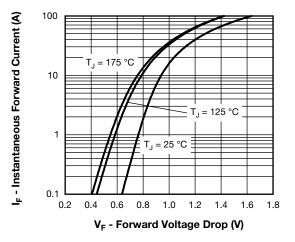
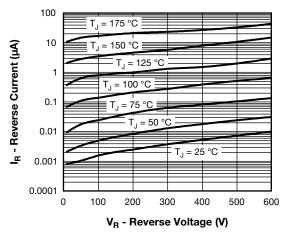


Fig. 1 - Typical Forward Voltage Drop Characteristics





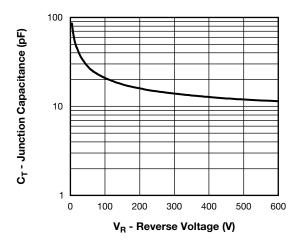
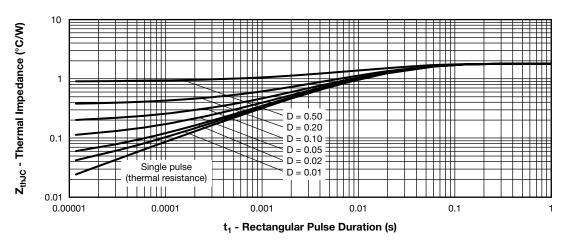


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



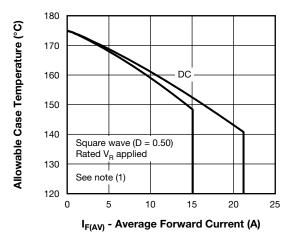


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Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

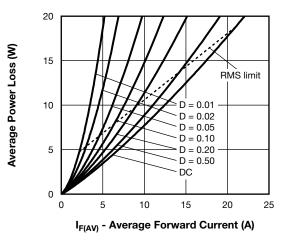


Fig. 6 - Forward Power Loss Characteristics

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ; Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{BEV}$  = inverse power loss =  $V_{B1} \times I_{B} (1 - D)$ ;  $I_{B}$  at  $V_{B1}$  = rated  $V_{B1}$ 

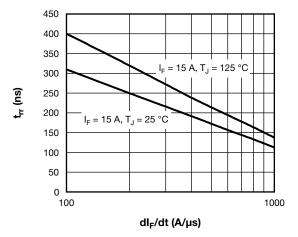


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

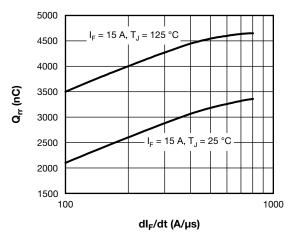


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

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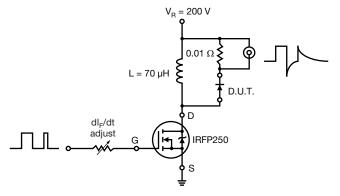


Fig. 9 - Reverse Recovery Parameter Test Circuit

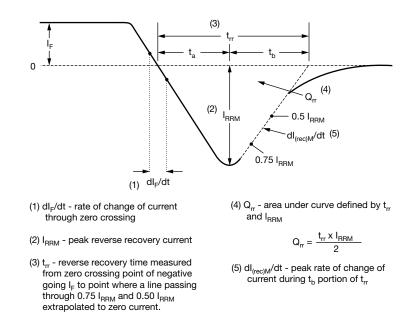
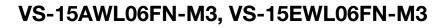


Fig. 10 - Reverse Recovery Waveform and Definitions





## **ORDERING INFORMATION TABLE**

Device code	VS-	15	Α	w	L	06	FN	TRL	-M3
	1	2	3	4	5	6	7	8	9
	1	- Visł	nay Serr	niconduc	ctors pro	duct			
	2	- Cur	rent rati	ng (15 =	= 15 A)				
	3	- Circ	uit conf	iguratior	ו:				
		• A	= single	e diode (	2 anode	es)			
		• E = single diode							
	4	- Package identifier:							
	_	W =	D-PAK						
	5	- L=	hyperfa	st rectifi	er				
	6	- Volt	age rati	ng (06 =	= 600 V)				
	7	- FN	= TO-25	52AA					
	8	• N	one = tu	be					
		• TI	R = tape	and ree	el				
		• TI	RL = tap	e and re	eel (left o	oriented	)		
		• TF	RR = tap	be and r	eel (righ	t oriente	ed)		
	9	- Env	ironmer	ntal digit	:				

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-15AWL06FN-M3	75	3000	Antistatio plantia tuba						
VS-15EWL06FN-M3	75	3000	Antistatic plastic tube						
VS-15AWL06FNTR-M3	2000	2000	13" diameter reel						
VS-15EWL06FNTR-M3	2000	2000							
VS-15AWL06FNTRL-M3	3000	3000	10" diamatar raal						
VS-15EWL06FNTRL-M3	3000	3000	13" diameter reel						
VS-15AWL06FNTRR-M3	3000	3000	10" diamatar raal						
VS-15EWL06FNTRR-M3	3000	3000	13" diameter reel						

LINKS TO RELATED DOCUMENTS								
Dimensions	www.vishay.com/doc?95627							
Part marking information	www.vishay.com/doc?95176							
Packaging information	www.vishay.com/doc?95033							
SPICE model	www.vishay.com/doc?95372							

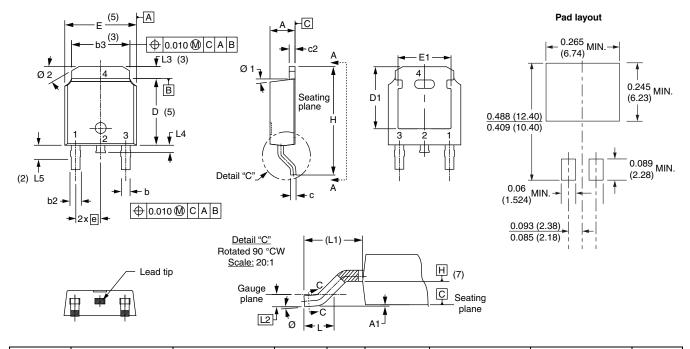
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D-PAK (TO-252AA) "M"

## **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51 BSC		0.51 BSC 0.020 BSC		
с	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

## Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension uncontrolled in L5

<sup>(3)</sup> Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(6)</sup> Dimension b1 and c1 applied to base metal only

<sup>(7)</sup> Datum A and B to be determined at datum plane H

<sup>(8)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-252AA

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