

Vishay Siliconix

RoHS

COMPLIANT

## P-Channel 30-V (D-S) MOSFET

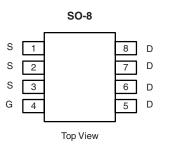
PRODUCT SUMMARY						
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (Typ.)			
- 30	0.0125 at $V_{GS}$ = - 10 V	- 14.9	29.5 nC			
	$0.0205  \text{at V}_{GS} = -4.5  \text{V}$	- 11.6	29.5110			

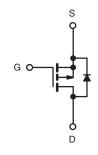
#### **FEATURES**

- Halogen-free
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

#### APPLICATIONS

- Load Switch
- Notebook Adaptor Switch





P-Channel MOSFET

Ordering Information: Si4825DDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 30	V		
Gate-Source Voltage		V <sub>GS</sub>			± 25
	T <sub>C</sub> = 25 °C		- 14.9		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C		- 11.9		
Continuous Drain Current $(T_j = 150 \text{ C})$	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 10.9 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		- 8.6 <sup>a, b</sup>	•	
Pulsed Drain Current	I <sub>DM</sub>	- 60	— A		
Continuous Courses Drain Diada Current	T <sub>C</sub> = 25 °C	1	- 4.1		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 2.2 <sup>a, b</sup>		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 20		
Single-Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	20	mJ	
	T <sub>C</sub> = 25 °C		5.0		
Mauinum Davies Disaination	T <sub>C</sub> = 70 °C		3.2	14/	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.7 <sup>a, b</sup>	W	
	T <sub>A</sub> = 70 °C		1.7 <sup>a, b</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 10 s	R <sub>thJA</sub>	38	46	°C/W		
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	20	25	· C/W		

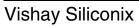
Notes:

a. Surface mounted on 1" x 1" FR4 board.

c. Maximum under Steady State conditions is 85  $^\circ\text{C/W}.$ 

d. Based on T\_C = 25 °C.

b. t = 10 s.





<b>SPECIFICATIONS</b> $T_J = 25 \text{ °C}$ , unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Uni	
Static						r	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 34		m۷	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			5.3		°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.4		- 2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 25 V$			± 100	nA	
Zero Gate Voltage Drain Current	loco	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA	
Zelo Gale Voltage Dialit Guiterit	IDSS	$V_{DS}$ = - 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C		- 5		μΛ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge$ - 10 V, $V_{GS}$ = - 10 V	- 30			A	
Duraine Con State Desistance	Baar	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A		0.010	0.0125	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8 A		0.0165	0.0205		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 10 A		28		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			2550			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		455		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			390			
Tabal O als Observe		$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -10 \text{ A}$		57	86		
Total Gate Charge	Q <sub>g</sub>			29.5	45		
Gate-Source Charge	Q <sub>qs</sub>	$V_{DS}$ = - 15 V, $V_{GS}$ = - 4.5 V, $I_{D}$ = - 10 A		8		nC	
Gate-Drain Charge	Q <sub>gd</sub>			22		1	
Gate Resistance	Ř <sub>q</sub>	f = 1 MHz	0.5	2.2	4.4	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			13	25		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_{L}$ = 1.5 $\Omega$		12	24		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ - 10 A, $V_{GEN}$ = - 10 V, $R_q$ = 1 $\Omega$		40	70		
Fall Time	t <sub>f</sub>	Ğ		9	18		
Turn-On Delay Time	t <sub>d(on)</sub>			48	80	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_{L}$ = 1.5 $\Omega$		92	160	-	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ - 10 Å, $V_{GEN}$ = - 4.5 V, $R_a$ = 1 $\Omega$		34	60		
Fall Time	t <sub>f</sub>			19	35		
Drain-Source Body Diode Characteris						1	
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 4.1		
Pulse Diode Forward Current	I <sub>SM</sub>	5			- 60	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A, V <sub>GS</sub> = 0 V		- 0.75	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	6 60		27	45	ns	
Body Diode Reverse Recovery Charge	rse Becovery Charge O			16	27	nC	
Reverse Recovery Fall Time	ta	$I_F = -10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^\circ\text{C}$		12			
Reverse Recovery Rise Time	t <sub>b</sub>			15		ns	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

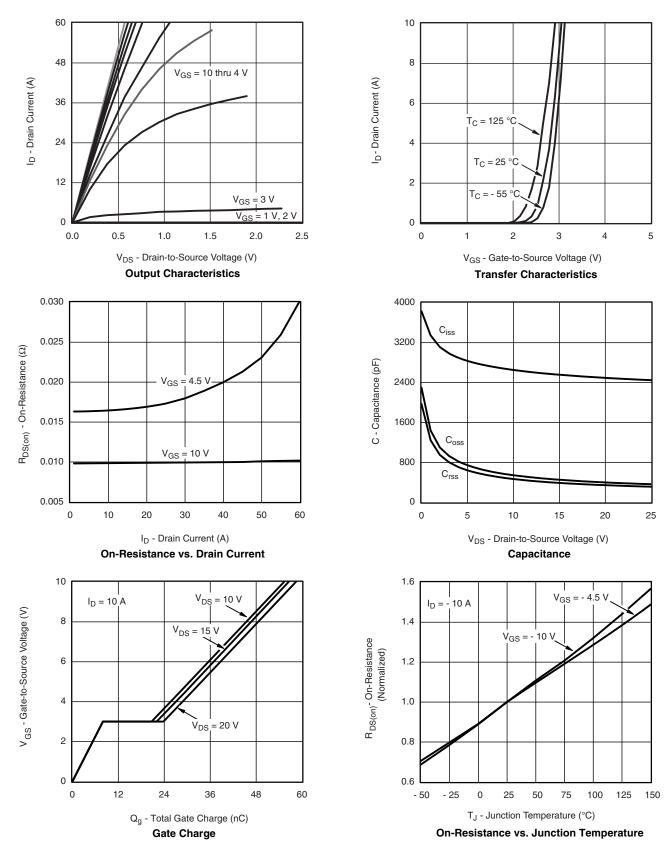
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

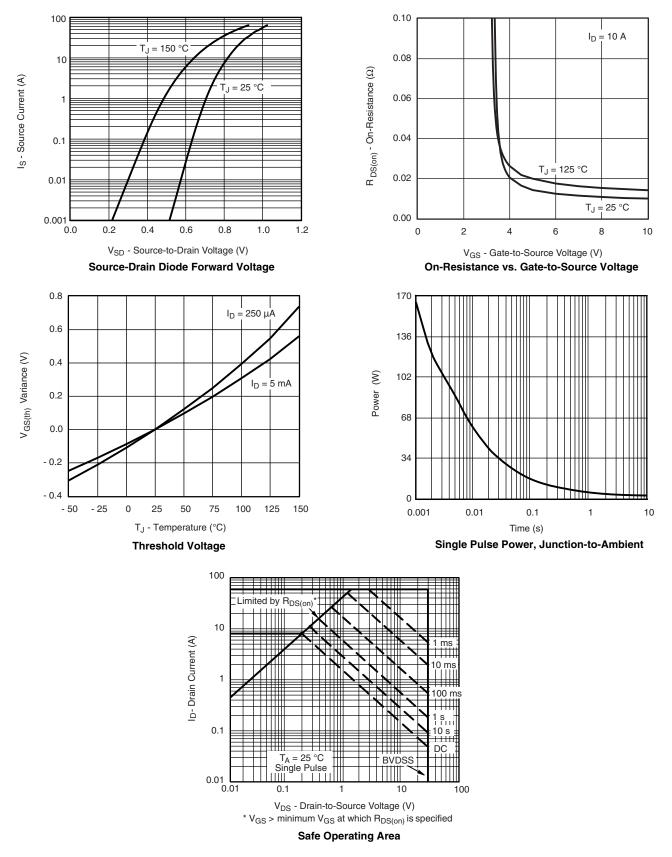


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### **Vishay Siliconix**

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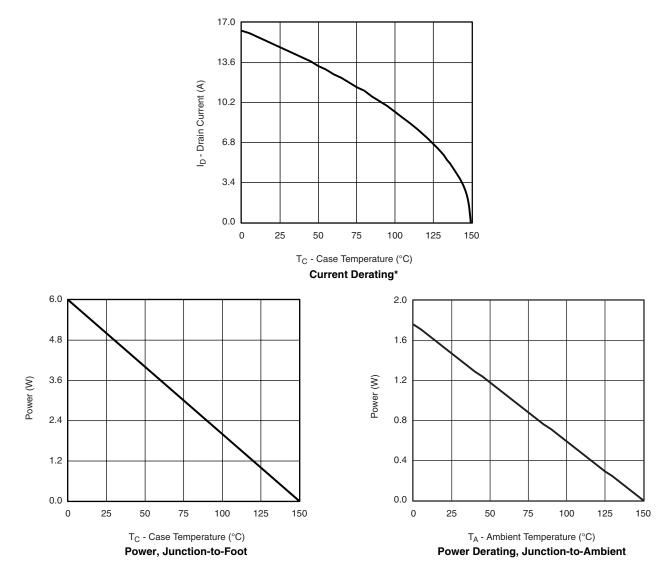
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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#### **MOSFET TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

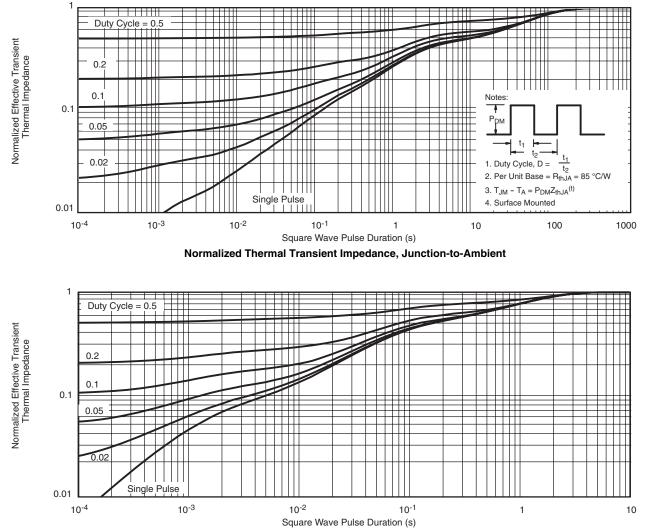


\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



#### **Vishay Siliconix**

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?68926.



# Package Information

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#### SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012





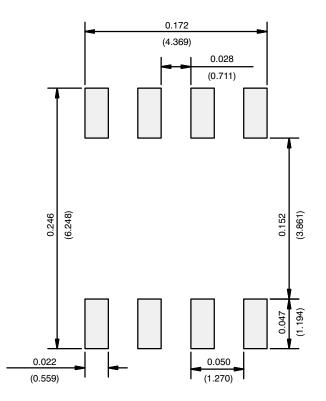
	MILLIM	IETERS	INCHES			
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

## **Application Note 826**

Vishay Siliconix



**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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