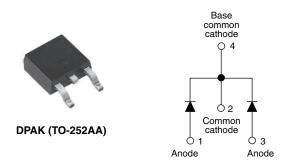
Vishay Semiconductors

www.vishay.com

Ultra fast Rectifier, 2 x 3 A FRED Pt[®]



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 3 A				
V _R	200 V				
V _F at I _F	0.9 V				
t _{rr} typ.	See Recovery table				
T _J max.	175 °C				
Package	DPAK (TO-252AA)				
Circuit configuration	Common cathode				

FEATURES

- Ultra fast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test



- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Vishay Semiconductors' 200 V series are the state of the art hyper fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyper fast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	V _{RRM}		200	V
Average rectified forward current per device	I _{F(AV)}	Total device, rated V _R , T _C = 159 °C	6	
Non-repetitive peak surge current	I _{FSM}		50	А
Peak repetitive forward current per diode	I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 159 °C	6	
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	200	-	-		
		I _F = 3 A	-	0.9	1	v	
Forward voltage	V _F	I _F = 3 A, T _J = 125 °C	-	0.78	0.9		
		I _F = 6 A	-	1	1.2		
		I _F = 6 A, T _J = 125 °C	-	0.89	1.08		
Deveree leekere eurrent	1	$V_{R} = V_{R}$ rated	-	-	5		
Reverse leakage current	I _R	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	100	μA	
Junction capacitance	CT	V _R = 200 V	-	12	-	pF	
Series inductance	Ls	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

 Revision: 09-Dec-2019
 1
 Document Number: 94743

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 $^{\circ}$ C unless otherwise specified)										
PARAMETER	SYMBOL	TEST	TEST CONDITIONS			MAX.	UNITS			
		I _F = 1.0 A, dI _F	$dt = 50 \text{ A/}\mu\text{s}, \text{V}_{\text{R}} = 30 \text{ V}$	-	20	35				
Reverse recovery time	t _{rr}	T _J = 25 °C		-	19	-	ns			
		T _J = 125 °C	l _F = 3 A V _R = 160 V	-	26	-				
Deals receivers ourrent		T _J = 25 °C		-	3.1	-	•			
Peak recovery current	I _{RRM}	IRRM	IRRM	IRRM	T _J = 125 °C	v _R = 160 v dI _F /dt = 200 A/µs	-	4.6	-	A
	0	T _J = 25 °C		-	30	-	20			
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	60	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	-65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}	-	-	5	°C/W	
M. 5.14		-	0.3	-	g	
Weight		-	0.01	-	oz.	
Mounting torque		6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style DP	AK (TO-252AA)	6CWH	02FNH	

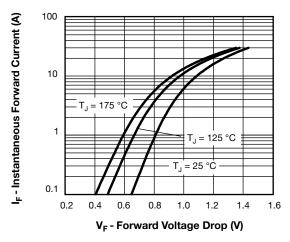


Fig. 1 - Maximum Forward Voltage Drop Characteristics

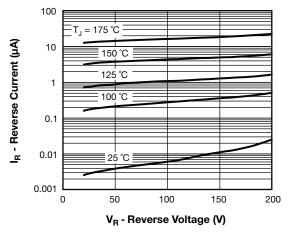


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

VS-6CWH02FNHM3

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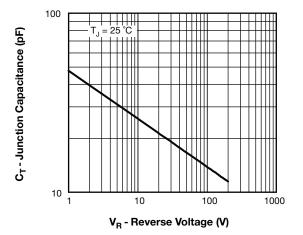


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

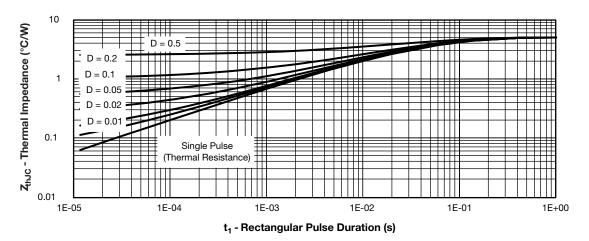
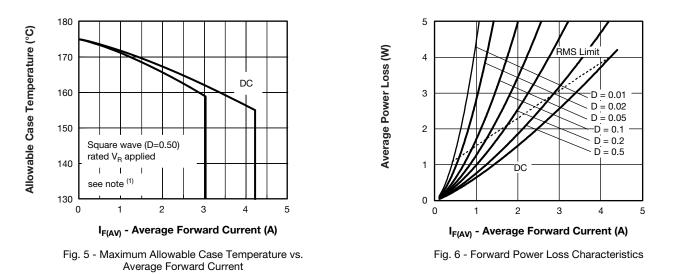


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics



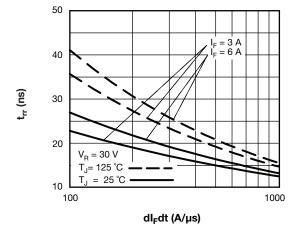
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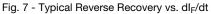
3

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Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

VS-6CWH02FNHM3

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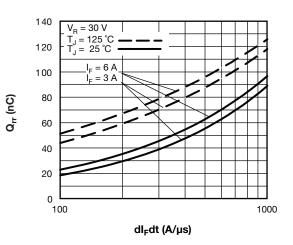


Fig. 8 - Typical Stored Charge vs. dl_F/dt

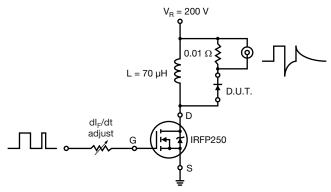


Fig. 9 - Reverse Recovery Parameter Test Circuit

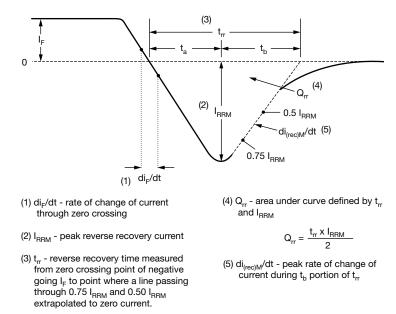


Fig. 10 - Reverse Recovery Waveform and Definitions

 Revision: 09-Dec-2019
 4
 Document Number: 94743

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ORDERING INFORMATION TABLE

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SHAY

Device code	VS-	6	с	w	н	02	FN	TRL	н	М3
		2	3	4	5	6	7	8	9	10
	 Vishay Semiconductors product Current rating (6 = 6 A) Center tap configuration Package identifier: W = DPAK H = hyperfast recovery 									
		- FN - • No	Voltage rating (02 = 200 V) FN = TO-252AA • None = tube (50 pieces) • TR = tape and reel							
	9 - 10 -	• TF • TF • H = • Env	RL = tap RR = tap AEC-Q rironmer	e and re be and re 101 qua ntal digit	eel (left eel (righ lified :	t oriente	ed)	terminat	tions lea	ad (Pb)-fr

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-6CWH02FNHM3	75	3000	Antistatic plastic tube			
VS-6CWH02FNTRHM3	2000	2000	13" diameter reel			
VS-6CWH02FNTRRHM3	3000	3000	13" diameter reel			
VS-6CWH02FNTRLHM3	3000	3000	13" diameter reel			

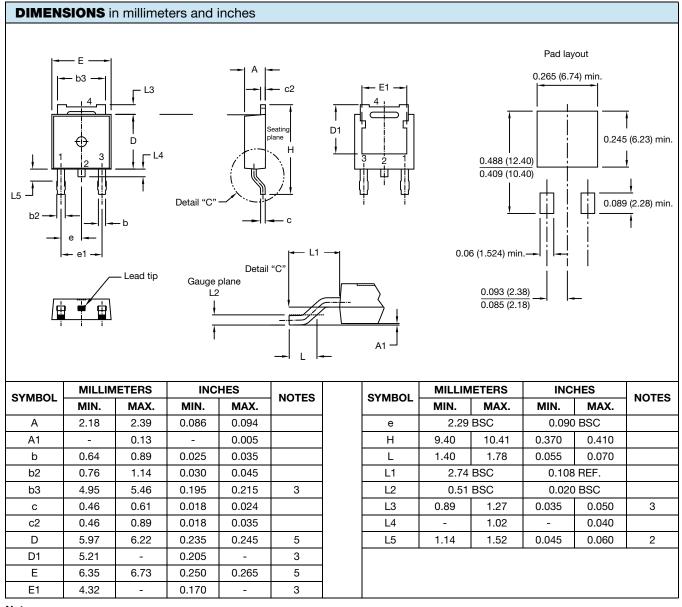
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?95033			

Outline Dimensions



Vishay Semiconductors

DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

⁽⁴⁾ Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA

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