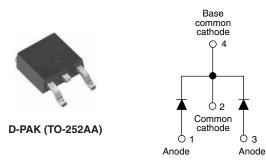
Vishay Semiconductors



Schottky Rectifier, 2 x 3.5 A



PRODUCT SUMMARY					
Package	D-PAK (TO-252AA)				
I _{F(AV)}	2 x 3.5 A				
V _R	100 V				
V _F at I _F	See Electrical table				
I _{RM}	4.9 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	5 mJ				

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-6CWQ10FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN						
I _{F(AV)}	Rectangular waveform	7	А					
V _{RRM}		100	V					
I _{FSM}	t _p = 5 μs sine	440	А					
V _F	$3 A_{pk}, T_J = 125 \ ^\circ C \text{ (per leg)}$	0.63	V					
TJ	Range	- 40 to 150	°C					

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-6CWQ10FNHM3	UNITS			
Maximum DC reverse voltage	V _R	100	V			
Maximum working peak reverse voltage	V _{RWM}	100	v			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS				
Maximum average per leg		$=_{(AV)}$ 50 % duty cycle at T _C = 135 °C, rectangular waveform		3.5			
See fig. 5 per device				7	А		
Maximum peak one cycle non-repetitive surge current per leg		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	440	Â		
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	70			
Non-repetitive avalanche energy per leg E _{AS}		T _J = 25 °C, I _{AS} = 1 A, L = 10 mH		5.0	mJ		
Repetitive avalanche current per leg		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.5	А		

Revision: 21-Aug-13



FREE





Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		3 A	T _{.1} = 25 °C	0.81	V	
Maximum forward	V _{FM} ⁽¹⁾	6 A	1j=25 C	0.96		
voltage drop per leg See fig. 1	VFM ()	3 A	T 105 %O	0.63		
		6 A	T _J = 125 °C	0.74		
Maximum reverse	I _{RM} ⁽¹⁾	T _J = 25 °C		1	mA	
leakage current per leg See fig. 2	IRM ("	T _J = 125 °C	V _R = Rated V _R	4.9		
Threshold voltage	V _{F(TO)}	T T maximum				
Forward slope resistance	r _t	$I_{J} = I_{J} maximum$	$T_J = T_J$ maximum			
Typical junction capacitance per leg	CT	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C			pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.0 nH			nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/µs			V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

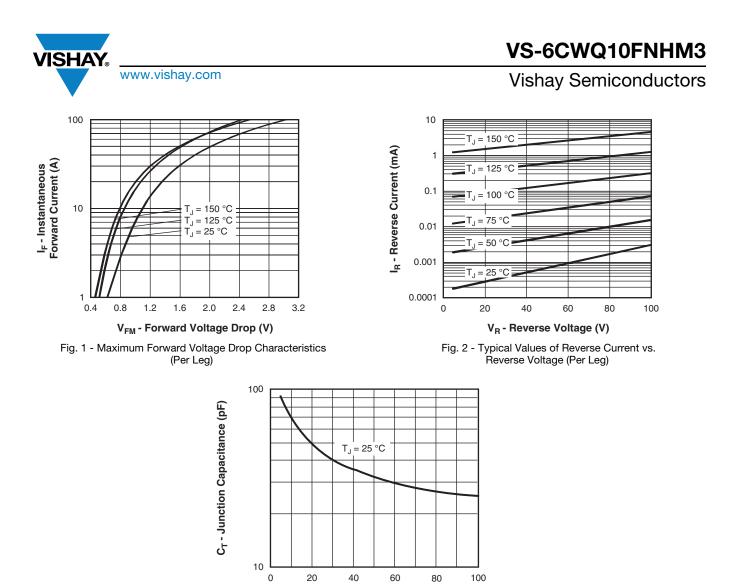
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_{J} ⁽¹⁾ , T_{Stg}		- 40 to 150	°C
Maximum thermal resistance,	per leg	R _{thJC}	DC operation	4.70	°C/W
junction to case	per device	nthJC	See fig. 4	2.35	0/ 11
Approvimete weight				0.3	g
Approximate weight				0.01	oz.
Marking device			Case style D-PAK	6CWQ	10FNH

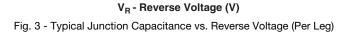
Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

Revision: 21-Aug-13

2





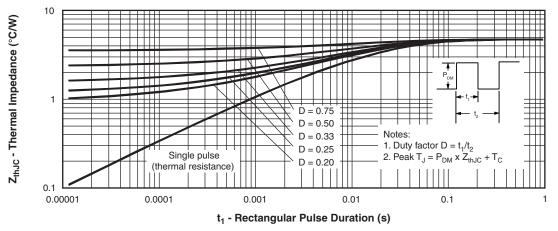
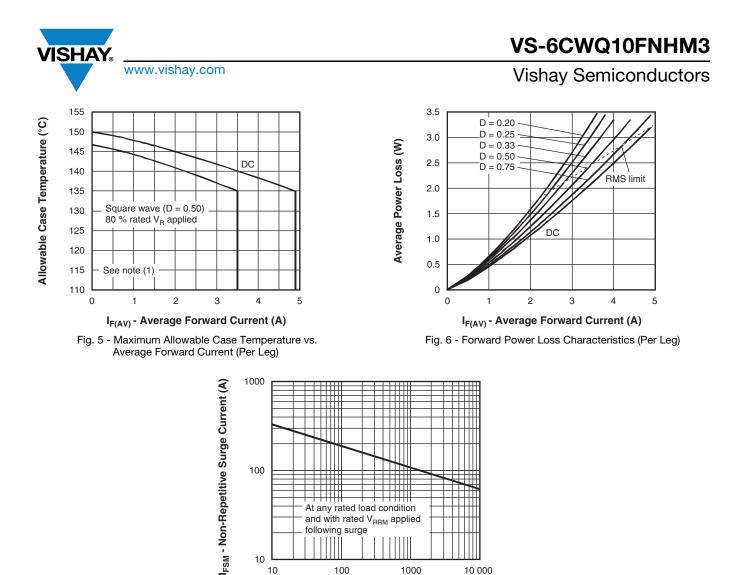


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

3

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFI Downloaded From Oneyac.com w.vishay.com/doc?91000



t_p - Square Wave Pulse Duration (μs)

1000

10 000

100

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

10

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

Vishay Semiconductors

ORDERING INFORMATION TABLE

www.vishay.com

SHA'

Device code	VS-	6	С	w	Q	10	FN	TRL	н	M3
	1	2	3	4	5	6	7	8	9	10
	1	- Vis	hay Sen	nicondu	ctors pro	oduct				
	2	- Cui	rrent rati	ng (7 A))					
	3	- Cei	nter tap	configur	ation					
	4	- Pao	ckage id	entifier:						
		W =	= D-PAK	ζ.						
	5	- Scł	nottky "C)" series						
	6	- Vol	tage rati	ing (10 =	= 100 V))				
	7	- FN	= TO-2	52AA						
	8	- • N	one = T	ube						
		• T	R = Tap	e and re	el					
		• T	RL = Ta	pe and r	eel (left	oriente	d)			
		• T	RR = Ta	pe and	reel (rig	ht orien	ted)			
	9	- H=	AEC-Q	101 qua	alified					
	10	- Env	vironme	ntal digit						
		1/2		ion froo	DALC	oomolio	nt and	tormino	tionala	

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-6CWQ10FNHM3	75	3000	Antistatic plastic tube			
VS-6CWQ10FNTRHM3	2000	2000	13" diameter reel			
VS-6CWQ10FNTRRHM3	3000	3000	13" diameter reel			
VS-6CWQ10FNTRLHM3	3000	3000	13" diameter reel			

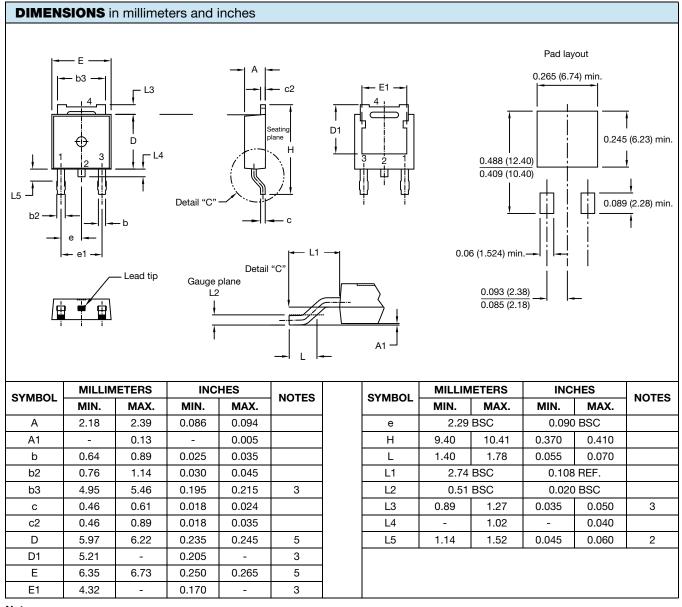
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				

Outline Dimensions



Vishay Semiconductors

DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

⁽⁴⁾ Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFI Downloaded From Oneyac.com w.vishay.com/doc?91000



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

单击下面可查看定价,库存,交付和生命周期等信息

>>Vishay(威世)