P-Channel 100-V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)[a]	Q_g (Typ)
–100	0.043 at V_{GS} = –10 V	–38	105 nC

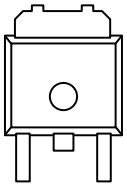
FEATURES

- TrenchFET® Power MOSFET

Pb-free

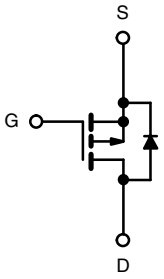
RoHS COMPLIANT

TO-252



Top View

Drain Connected to Tab



P-Channel MOSFET

Ordering Information: SUD50P10-43–E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C UNLESS OTHERWISE NOTED)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	–100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 175 °C)	T_C = 25 °C	I_D	–38[a]	A
	T_C = 70 °C		–31.8[a]	
	T_A = 25 °C		–9.4[b, c]	
	T_A = 70 °C		–7.8[b, c]	
Pulsed Drain Current		I_{DM}	–50	
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	–50[a]	
	T_A = 25 °C		–6.9[b, c]	
Avalanche Current	L = 0.1 mH	I_{AS}	–40	
Single-Pulse Avalanche Energy		E_{AS}	80	mJ
Maximum Power Dissipation	T_C = 25 °C	P_D	136	W
	T_C = 70 °C		95	
	T_A = 25 °C		8.3[b, c]	
	T_A = 70 °C		5.8[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	–50 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient[b, d]	t ≤ 10 sec	R_{thJA}	15	18	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	0.85	1.1	

Notes:
a. Package limited.
b. Surface mounted on 1" x 1" FR4 Board.
c. t = 10 sec.
d. Maximum under steady state conditions is 40 °C/W.

SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

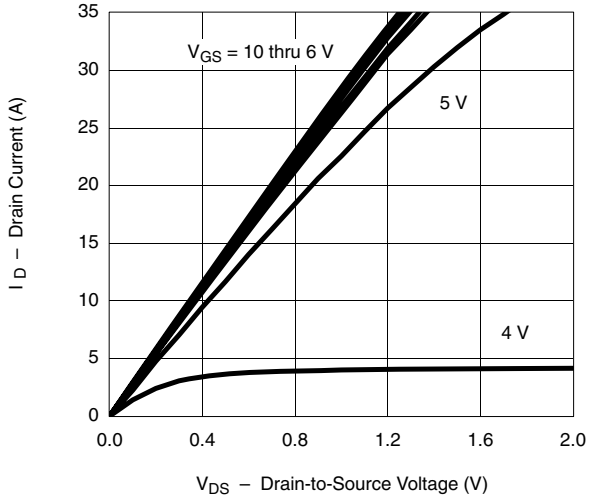
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = –250 µA	–100			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = –250 µA		–105		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			7		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = –250 µA	–2	–3	–4	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = –100 V, V_{GS} = 0 V			–1	µA
		V_{DS} = –100 V, V_{GS} = 0 V, T_J = 55 °C			–10	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} = –10 V	–35			A
Drain-Source On-State Resistance[a]	$r_{DS(on)}$	V_{GS} = –10 V, I_D = –9.4 A		0.036	0.043	Ω
Forward Transconductance[a]	g_{fs}	V_{DS} = –15 V, I_D = –9.4 A		30		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = –50 V, V_{GS} = 0 V, f = 1 MHz		5230		pF
Output Capacitance	C_{oss}			230		
Reverse Transfer Capacitance	C_{rss}			165		
Total Gate Charge	Q_g	V_{DS} = –50 V, V_{GS} = –10 V, I_D = –9.4 A		105	160	nC
Gate-Source Charge	Q_{gs}			21		
Gate-Drain Charge	Q_{gd}			29		
Gate Resistance	R_g	f = 1 MHz		4.1		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = –50 V, R_L = 6.4 Ω I_D ≅ –7.8 A, V_{GEN} = –10 V, R_g = 1 Ω		30	50	ns
Rise Time	t_r			115	175	
Turn-Off Delay Time	$t_{d(off)}$			80	120	
Fall Time	t_f			60	90	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			–50	A
Pulse Diode Forward Current[a]	I_{SM}				–50	
Body Diode Voltage	V_{SD}	I_S = –7.8 A		–0.8	–1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = –7.8 A, di/dt = 100 A/µs, T_J = 25 °C		60	90	ns
Body Diode Reverse Recovery Charge	Q_{rr}			180	270	nC
Reverse Recovery Fall Time	t_a			48		ns
Reverse Recovery Rise Time	t_b			12		

Notes
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

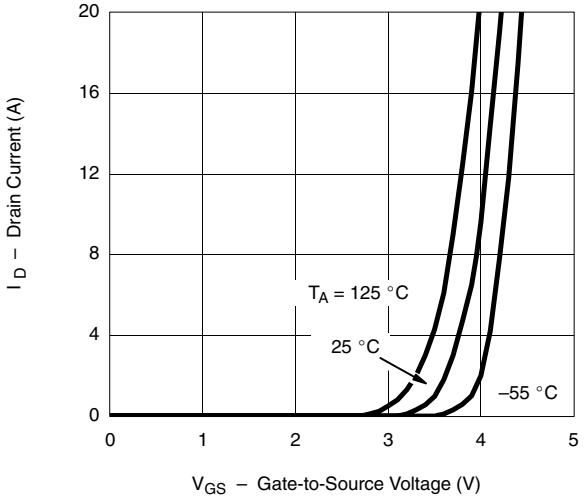
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

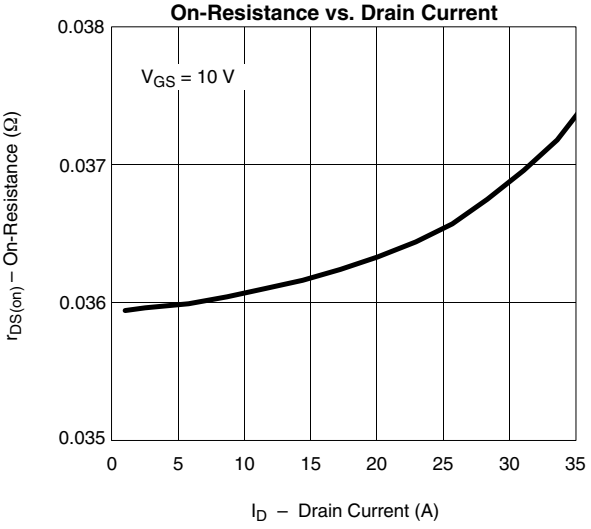
Output Characteristics



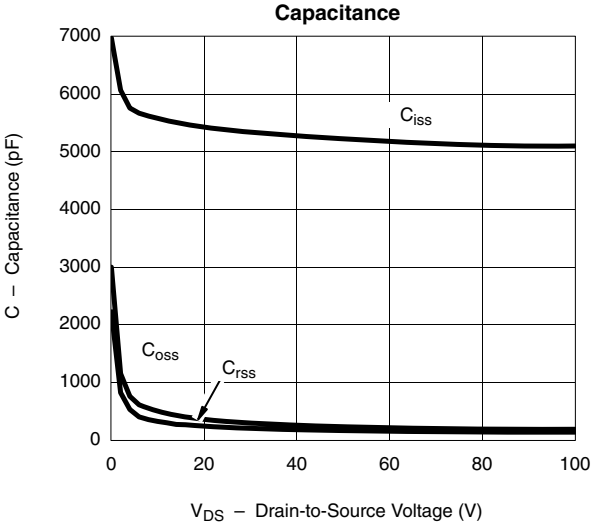
Transfer Characteristics



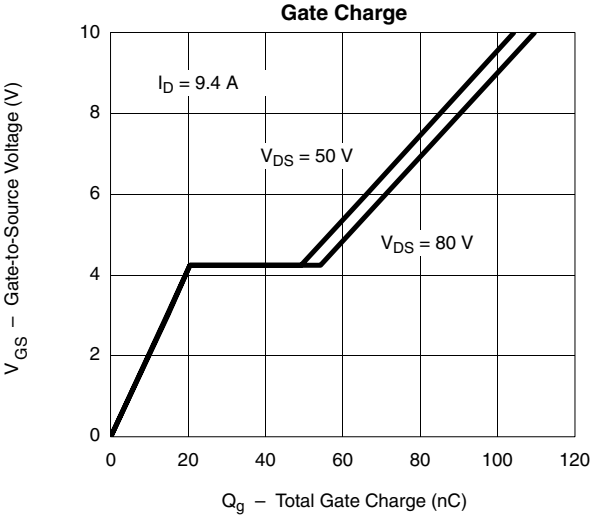
On-Resistance vs. Drain Current



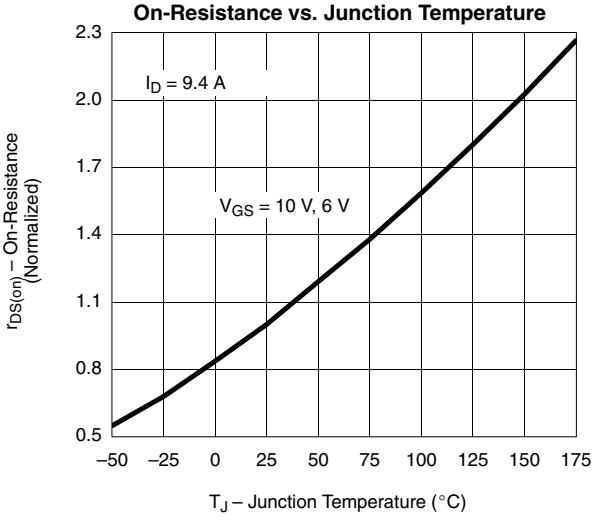
Capacitance



Gate Charge

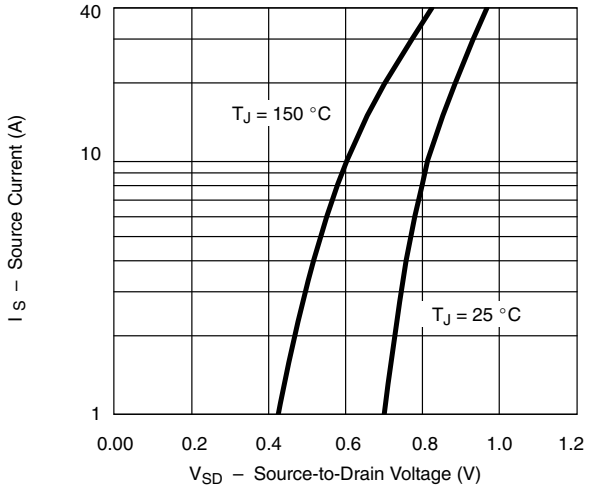


On-Resistance vs. Junction Temperature

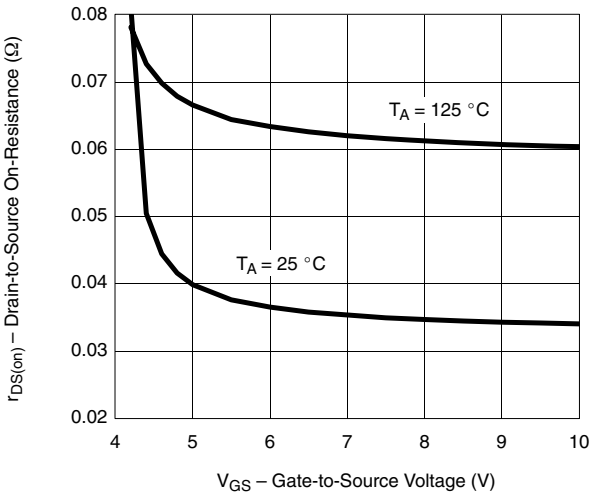


TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

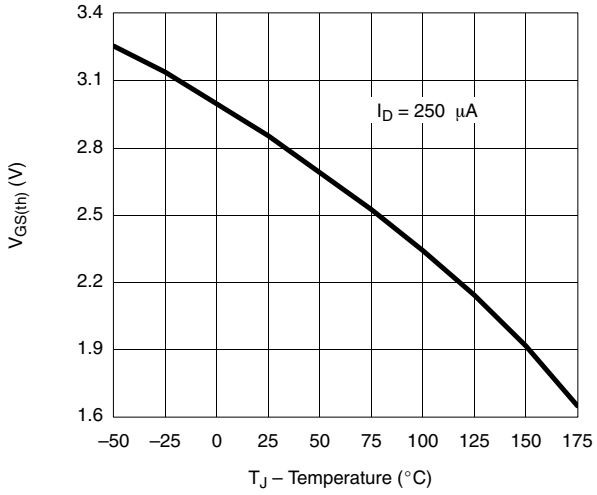
Source-Drain Diode Forward Voltage



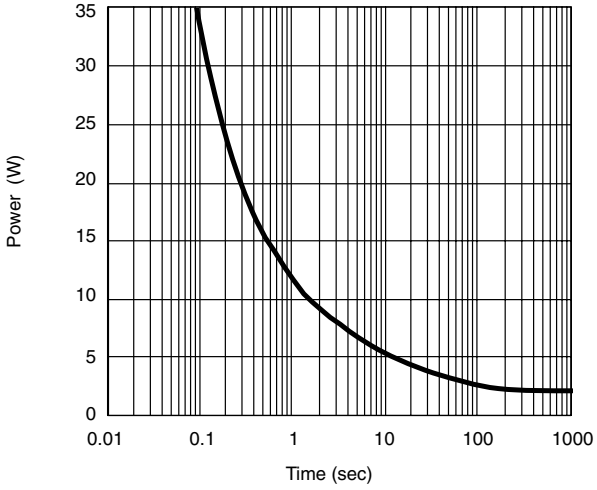
On-Resistance vs. Gate-to-Source Voltage



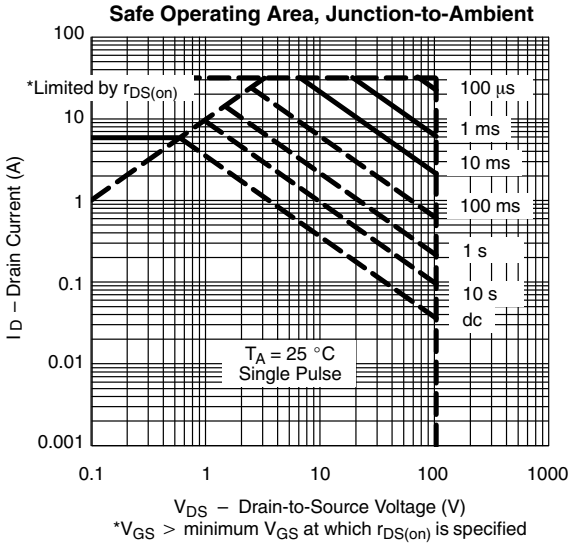
Threshold Voltage



Single Pulse Power, Junction-to-Ambient



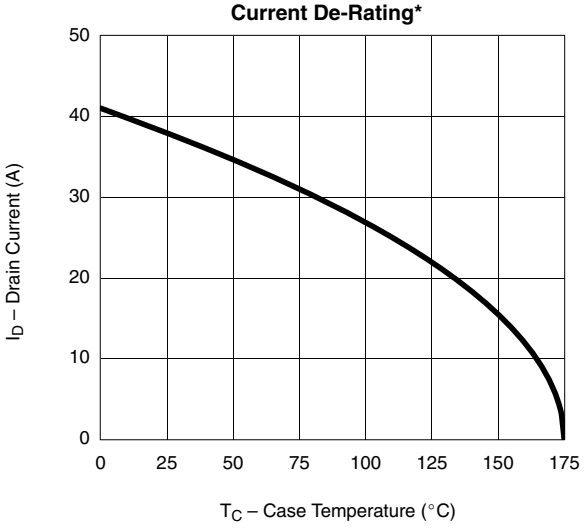
Safe Operating Area, Junction-to-Ambient



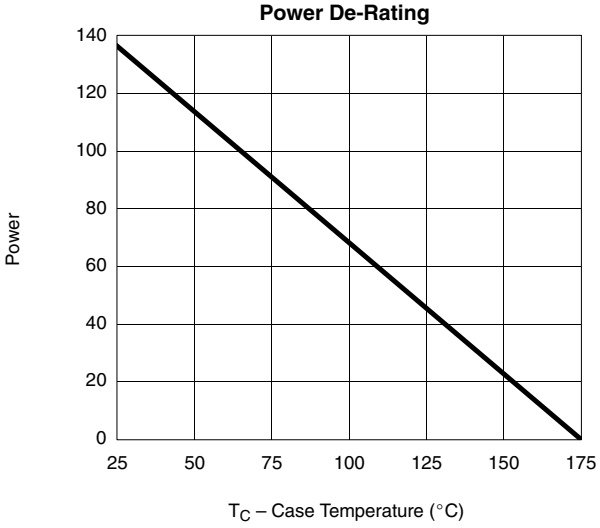
*V_{GS} > minimum V_{GS} at which $r_{DS(on)}$ is specified

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

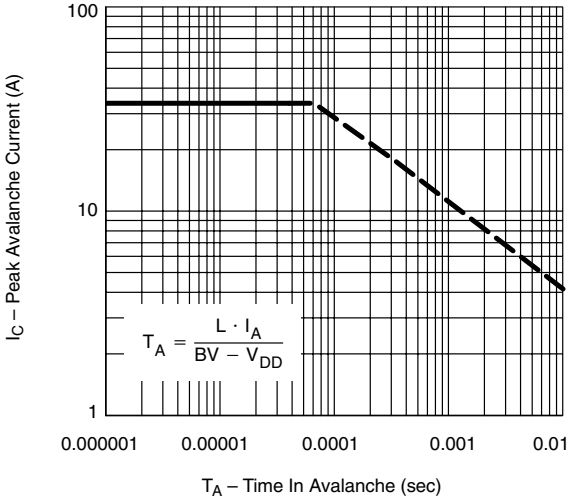
Current De-Rating*



Power De-Rating

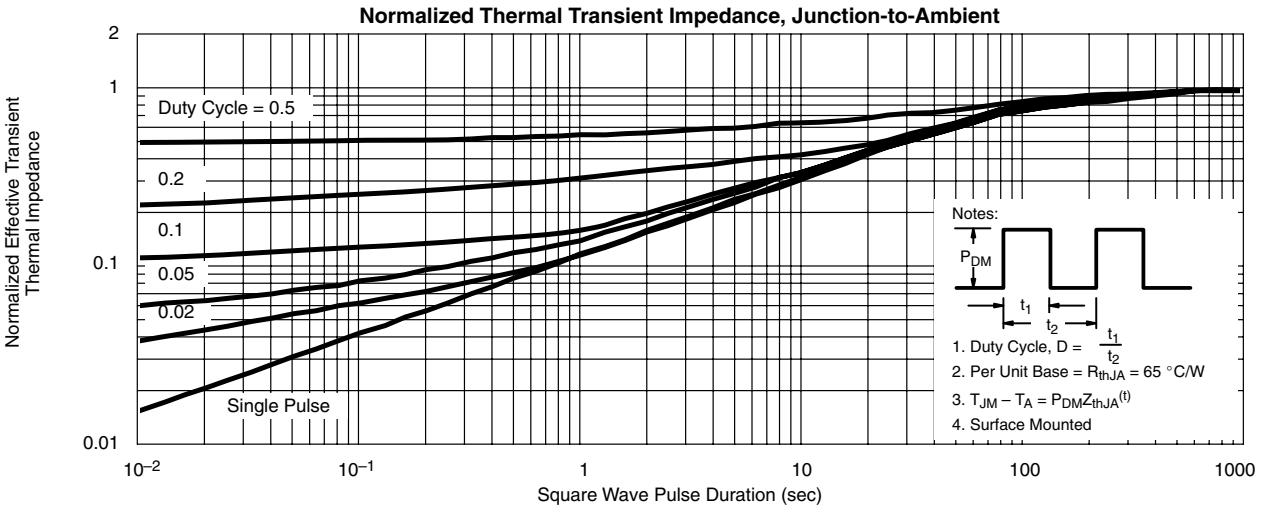


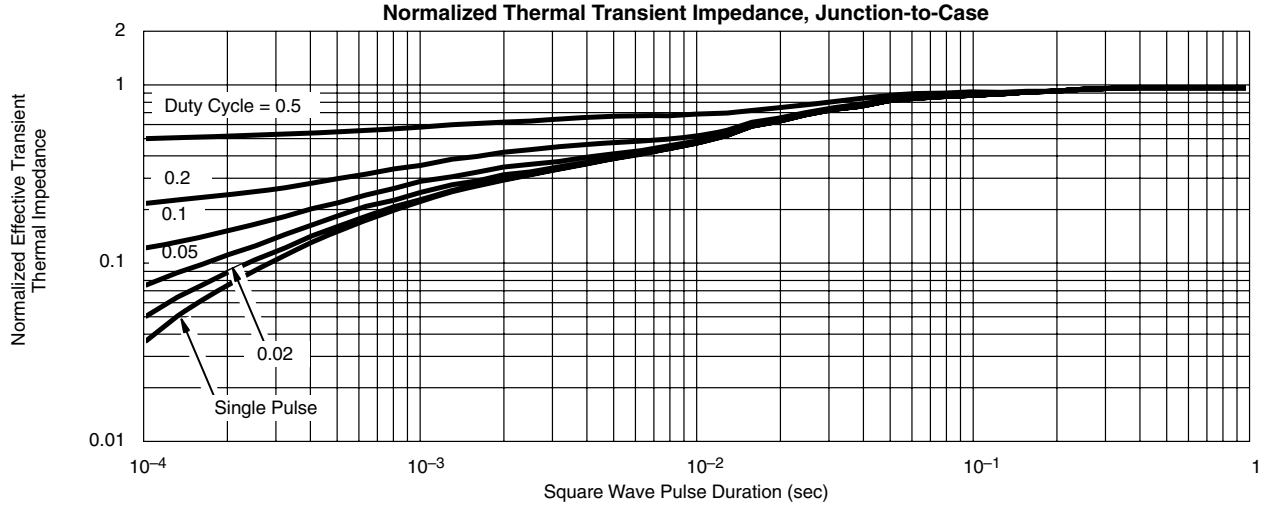
Single Pulse Avalanche Capability



*The power dissipation P_D is based on $T_{J(max)}$ = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)





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