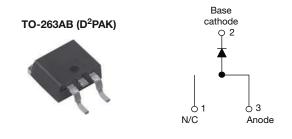
VS-6TQ035SPbF, VS-6TQ040SPbF, VS-6TQ045SPbF

VISHAY.



Vishay Semiconductors

High Performance Schottky Rectifier, 6 A



PRODUCT SUMMARY							
Package	TO-263AB (D ² PAK)						
I _{F(AV)}	6 A						
V _R	35 V, 40 V, 45 V						
V _F at I _F	0.53 V						
I _{RM}	7 mA at 125 °C						
T _J max.	175 °C						
Diode variation	Single die						
E _{AS}	8 mJ						

FEATURES

- 175 °C T_J operation
- High frequency operation
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-6TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES							
I _{F(AV)}	Rectangular waveform	6	А						
V _{RRM}	Range	35 to 45	V						
I _{FSM}	t _p = 5 μs sine	690	А						
V _F	6 A _{pk} , T _J = 125 °C	0.53	V						
TJ	Range	-55 to +175	°C						

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS-6TQ035SPbF	VS-6TQ040SPbF	VS-6TQ045SPbF	UNITS					
Maximum DC reverse voltage	V _R	35	40	45	V					
Maximum working peak reverse voltage	V _{RWM}		40	45	v					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at $T_C = 164 \text{ °C}$	6						
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse Following any rated load condition and with rated		690	А				
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	140					
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.	8	mJ					
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximu	1.20	А					

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
		6 A	T _{.1} = 25 °C	0.60					
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	12 A	1)=23 0	0.73	v				
	VFM (''	6 A	T _{.1} = 125 °C	0.53	v				
		12 A	- 1j = 125 C	0.64					
Maximum reverse leakage current	I _{BM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B} = \text{Rated } V_{\rm B}$	0.8	mA				
See fig. 2	IRM (")	T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	7					
Threshold voltage	V _{F(TO)}			0.35	V				
Forward slope resistance	r _t	$T_J = T_J$ maximum		18.23	mΩ				
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal rang	400	pF					
Typical series inductance	L _S	Measured lead to lead 5 m	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C			
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.2				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W			
Approximate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf · cm			
Mounting torque maximum				12 (10)	(lbf · in)			
Marking device				6TQ03	85S			
			Case style D ² PAK	6TQ04	IOS			
				6TQ045S				



VS-6TQ035SPbF, VS-6TQ040SPbF, VS-6TQ045SPbF

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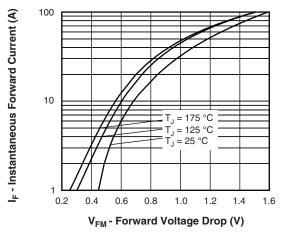


Fig. 1 - Maximum Forward Voltage Drop Characteristics

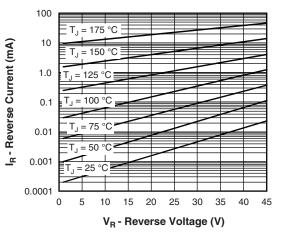


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

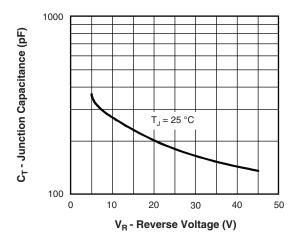
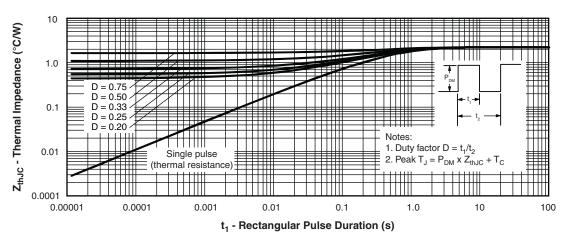


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

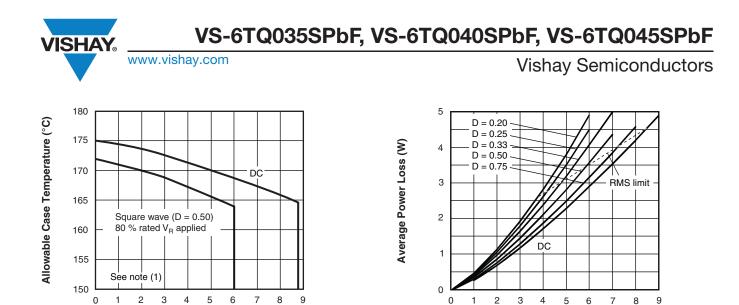




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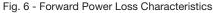
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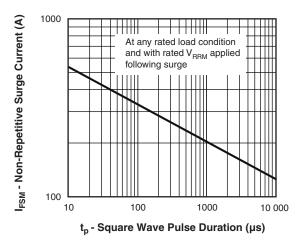


Fig. 7 - Maximum Non-Repetitive Surge Current

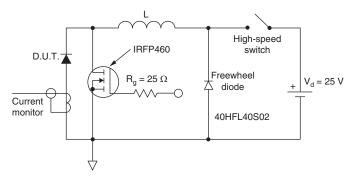


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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VS-6TQ035SPbF, VS-6TQ040SPbF, VS-6TQ045SPbF

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ORDERING INFORMATION TABLE

Device code	VS-	6	т	Q	045	S	TRL	PbF	
		2	3	4	5	6	7	8	
	1 - 2 - 3 -								
	4 - 5 - 6 -	Vol	ottky "C tage rati D ² PAK	040 =	= 35 V = 40 V = 45 V				
	7 -	• T	None = tube (50 pieces)TRL = tape and reel (left oriented)						
	8 -			pe and r (Pb)-fre		nt orient	ed)		

ORDERING INFORMATION (Example)										
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-6TQ035SPBF	50	1000	Antistatic plastic tubes							
VS-6TQ035STRRPBF	800	800	13" diameter plastic tape and reel							
VS-6TQ035STRLPBF	800	800	13" diameter plastic tape and reel							
VS-6TQ040SPBF	50	1000	Antistatic plastic tubes							
VS-6TQ040STRRPBF	800	800	13" diameter plastic tape and reel							
VS-6TQ040STRLPBF	800	800	13" diameter plastic tape and reel							
VS-6TQ045SPBF	50	1000	Antistatic plastic tubes							
VS-6TQ045STRRPBF	800	800	13" diameter plastic tape and reel							
VS-6TQ045STRLPBF	800	800	13" diameter plastic tape and reel							

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95046						
Part marking information	www.vishay.com/doc?95054						
Packaging information	www.vishay.com/doc?95032						

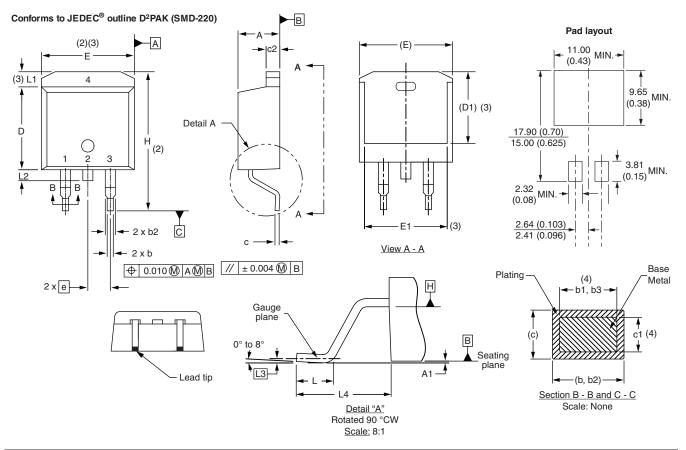
Outline Dimensions



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D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES		MILLIN	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010) BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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