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V40D120C-M3, V40D120CHM3

Vishay General Semiconductor

Dual High-Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.45$ V at $I_F = 5$ A

SMPD (TO-263AC) Top View **Bottom View**

Cathode

eSMP[®] Series

DESIGN SUPPORT TOOLS AVAILABLE

Anode 1 O



PRIMARY CHARACTERISTICS			
I _{F(AV)}	2 x 20 A		
V _{RRM}	120 V		
I _{FSM}	250 A		
V_F at I_F = 20 A (T_A = 125 °C)	0.64 V		
T _J max.	150 °C		
Package	SMPD (TO-263AC)		
Circuit configuration	Common cathode		

FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 gualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test Polarity: as marked

MAXIMUM RATINGS (T_A = 25 °C unless otherwise noted) SYMBOL UNIT PARAMETER V40D120C Maximum repetitive peak reverse voltage V_{RRM} 120 V 40 per device Maximum average forward rectified current А I_{F(AV)} (fig. 1) per diode 20 Peak forward surge current 10 ms single half sine-wave 250 А IFSM superimposed on rated load Voltage rate of change (rated V_B) dV/dt 10 000 V/µs Operating junction and storage temperature range -40 to +150 °C T_J, T_{STG}





RoHS COMPLIANT

HALOGEN

FREE



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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.51	-	V
	I _F = 10 A			0.63	-	
	I _F = 20 A			0.78	0.89	
	$I_F = 5 A$	T _A = 125 °C		0.45	-	
	I _F = 10 A			0.55	-	
	I _F = 20 A			0.64	0.71	
Reverse current at rated V_R per diode	V _R = 90 V	T _A = 25 °C	I _R (2)	11	-	μA
		T _A = 125 °C		9.4	-	mA
	V _R = 120 V	T _A = 25 °C		_	500	μA
		T _A = 125 °C		23	70	mA

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40D120C	UNIT	
Typical thermal resistance	per diode	R _{θJC}	1.9		
	per device		1.0	°C/W	
	per device	R _{0JA} ⁽¹⁾⁽²⁾	45		

Notes

⁽³⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{0JA}$

⁽⁴⁾ Free air, without heatsink

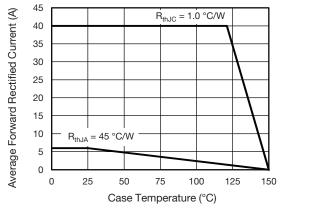
ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
SMPD (TO-263AC)	V40D120C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel	
SMPD (TO-263AC)	V40D120CHM3/I (1)	0.55	l	2000/reel	13" diameter plastic tape and reel	

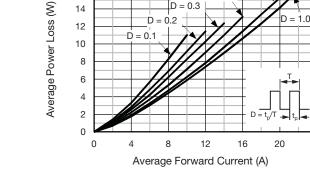
Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)





18

16

Fig. 1 - Forward Current Derating Curve

Fig. 2 - Forward Power Loss Characteristics Per Diode

Revision: 18-Mar-2019

2

Document Number: 87951

24

D = 0.8

0.5

D = 0.3

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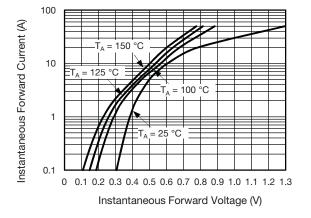


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

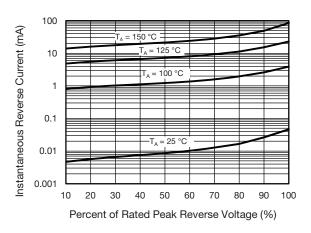


Fig. 4 - Typical Reverse Characteristics Per Diode

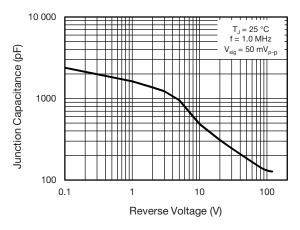


Fig. 5 - Typical Junction Capacitance Per Diode

V40D120C-M3, V40D120CHM3

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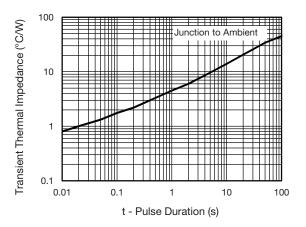


Fig. 6 - Typical Transient Thermal Impedance Per Device

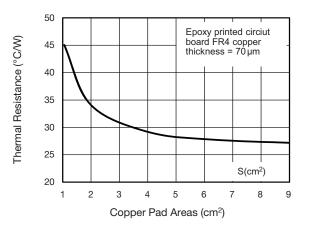


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

Revision: 18-Mar-2019

3

Document Number: 87951

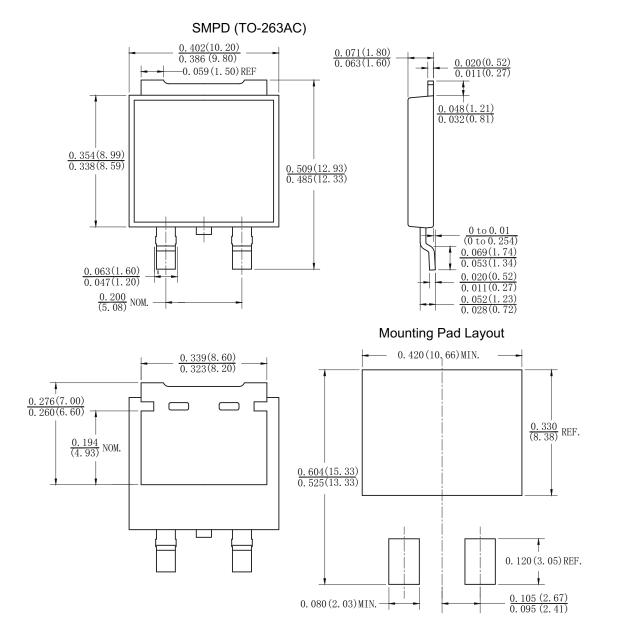
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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