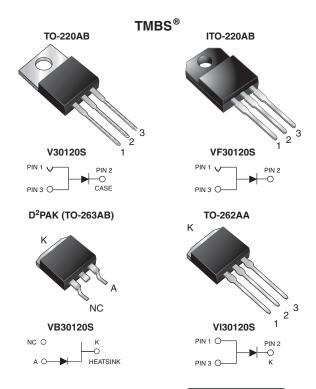
## V30120S-E3, VF30120S-E3, VB30120S-E3, VI30120S-E3

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## **High Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.43 \text{ V}$  at  $I_F = 5 \text{ A}$ 



#### **DESIGN SUPPORT TOOLS**

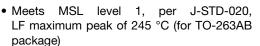




PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	30 A				
V <sub>RRM</sub>	120 V				
I <sub>FSM</sub>	300 A				
V <sub>F</sub> at I <sub>F</sub> = 30 A	0.74 V				
T <sub>J</sub> max.	150 °C				
Package	TO-220AB, ITO-220AB, D <sup>2</sup> PAK (TO-263AB), TO-262AA				
Circuit configuration	Single				

#### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation





- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB, ITO-220AB, TO-262AA package)
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB, ITO-220AB, D2PAK (TO-263AB), and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	V30120S	VF30120S	VB30120S	VI30120S	UNIT	
Maximum repetitive peak reverse voltage	$V_{RRM}$	120				V	
Maximum average forward rectified current (fig. 1)	I <sub>F(AV)</sub>	30				Α	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode	I <sub>FSM</sub>	300			А		
Non-repetitive avalanche energy at T <sub>J</sub> = 25 °C, L = 100 mH	E <sub>AS</sub>	180			mJ		
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz, $T_J$ = 38 °C $\pm$ 2 °C	I <sub>RRM</sub>	0.5			Α		
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	10 000			V/µs		
Isolation voltage (ITO-220AB only) from terminal to heatsink t = 1 min	V <sub>AC</sub>	1500			V		
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150				°C	

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT		
Instantaneous forward voltage per diode (1)	I <sub>F</sub> = 5 A		$V_F$	0.50	-	V		
	I <sub>F</sub> = 15 A	T <sub>A</sub> = 25 °C		0.70	-			
	I <sub>F</sub> = 30 A			0.99	1.10			
	I <sub>F</sub> = 5 A			0.43	-	V		
	I <sub>F</sub> = 15 A	T <sub>A</sub> = 125 °C		0.60	-	1		
	I <sub>F</sub> = 30 A			0.74	0.82			
Reverse current per diode (2)	V <sub>R</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub>	18	-	μΑ		
		T <sub>A</sub> = 125 °C		12	-	mA		
	V <sub>R</sub> = 120 V	T <sub>A</sub> = 25 °C		-	500	μΑ		
		T <sub>A</sub> = 125 °C		22	35	mA		

#### **Notes**

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

 $^{(2)}$  Pulse test: Pulse width  $\leq$  40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	V30120S	VF30120S	VB30120S	VI30120S	UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	1.6	4.0	1.6	1.6	°C/W	

ORDERING INFORMATION (Example)								
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
TO-220AB	V30120S-E3/4W	1.88	4W	50/tube	Tube			
ITO-220AB	VF30120S-E3/4W	1.75	4W	50/tube	Tube			
TO-263AB	VB30120S-E3/4W	1.39	4W	50/tube	Tube			
TO-263AB	VB30120S-E3/8W	1.39	8W	800/reel	Tape and reel			
TO-262AA	VI30120S-E3/4W	1.46	4W	50/tube	Tube			

### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

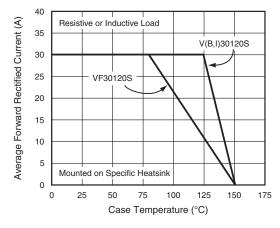


Fig. 1 - Forward Current Derating Curve

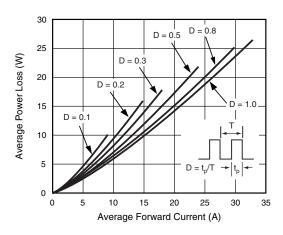


Fig. 2 - Forward Power Loss Characteristics Per Diode

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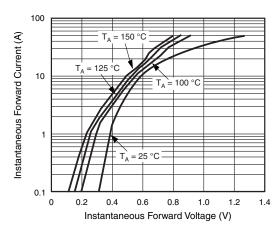


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

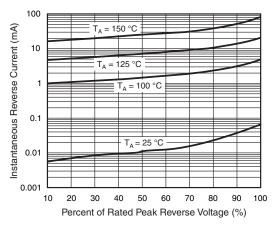


Fig. 4 - Typical Reverse Characteristics Per Diode

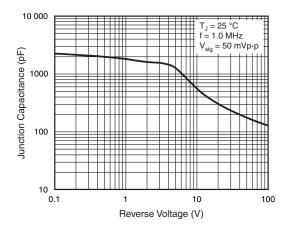


Fig. 5 - Typical Junction Capacitance Per Diode

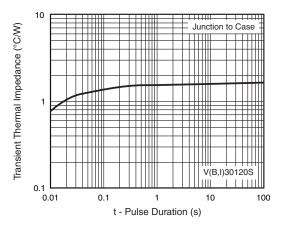


Fig. 6 - Typical Transient Thermal Impedance Per Diode

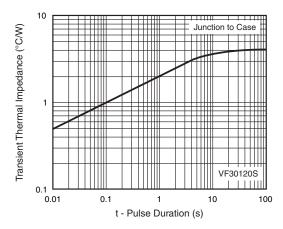


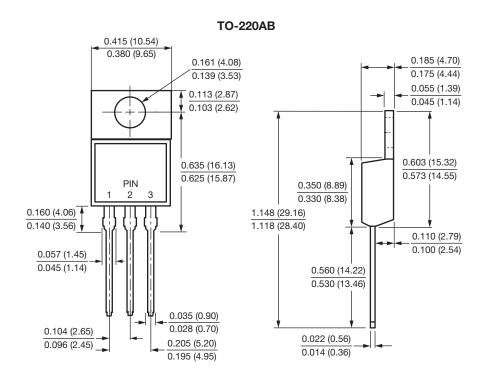
Fig. 7 - Typical Transient Thermal Impedance Per Diode

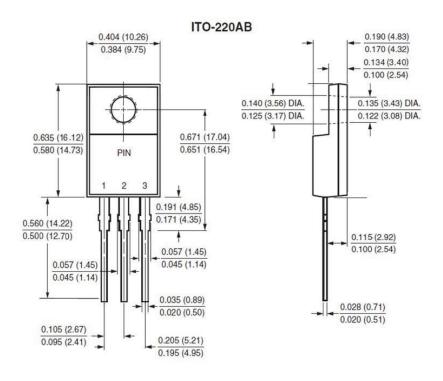


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### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

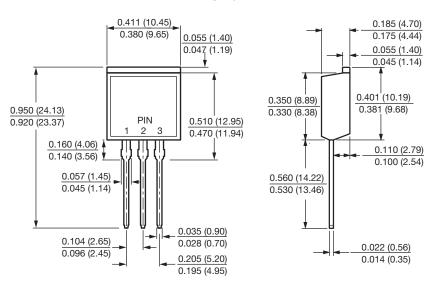


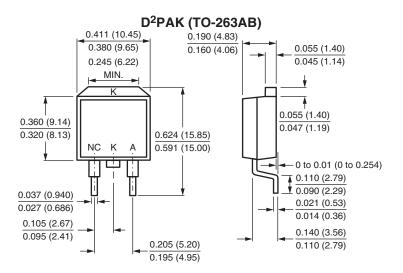


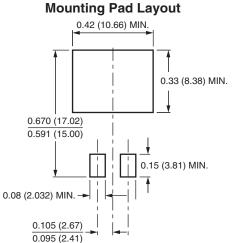
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#### TO-262AA









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