

AUTOMOTIVE

RoHS

COMPLIANT HALOGEN

FREE

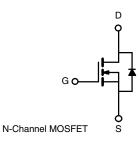
Automotive N-Channel 60 V (D-S) 175 °C MOSFET

| PRODUCT SUMMARY | |
|--|--------|
| V _{DS} (V) | 60 |
| $R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$ | 0.006 |
| I _D (A) | 119 |
| Configuration | Single |
| Package | TO-220 |



FEATURES

- TrenchFET[®] power MOSFET
- · Package with low thermal resistance
- AEC-Q101 qualified d
- 100 % $\rm R_g$ and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



| ABSOLUTE MAXIMUM RATINGS | (T _C = 25 °C, unles | s otherwise noted |) | |
|--|--------------------------------|-----------------------------------|-------------|------|
| PARAMETER | | SYMBOL | LIMIT | UNIT |
| Drain-Source Voltage | | V _{DS} | 60 | v |
| Gate-Source Voltage | | V _{GS} | ± 20 | v |
| Continuous Drain Current | T _C = 25 °C | Ŀ | 119 | |
| Continuous Drain Current | T _C = 125 °C | I _D | 68 | |
| Continuous Source Current (Diode Conductio | n) ^a | I _S | 120 | А |
| Pulsed Drain Current ^b | | I _{DM} | 480 | |
| Single Pulse Avalanche Current | L = 0.1 mH | I _{AS} | 65 | |
| Single Pulse Avalanche Energy | L = 0.1 mm | E _{AS} | 211 | mJ |
| Mauianum Dauran Diasia atian b | T _C = 25 °C | D | 175 | w |
| Maximum Power Dissipation ^b | T _C = 125 °C | P _D | 56 | vv |
| Operating Junction and Storage Temperature | Range | T _J , T _{stg} | -55 to +175 | °C |

| THERMAL RESISTANCE RATINGS | | | | |
|----------------------------|------------------------|-------------------|-------|------|
| PARAMETER | | SYMBOL | LIMIT | UNIT |
| Junction-to-Ambient | PCB Mount ^c | R _{thJA} | 40 | °C/W |
| Junction-to-Case (Drain) | | R _{thJC} | 0.88 | 0/W |

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR4 material).
- d. Parametric verification ongoing.



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| PARAMETER | SYMBOL | TES | T CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|---|--------------------------|--|--|------|--------|--------|------|
| Static | | • | | | | | 1 |
| Drain-Source Breakdown Voltage | V _{DS} | V _{GS} | = 0, I _D = 250 μA | 60 | - | - | v |
| Gate-Source Threshold Voltage | V _{GS(th)} | V _{DS} = | = V _{GS} , I _D = 250 μΑ | 2.5 | 3.0 | 3.5 | v |
| Gate-Source Leakage | I _{GSS} | V _{DS} = | $0 \text{ V}, \text{V}_{\text{GS}} = \pm 20 \text{ V}$ | - | - | ± 100 | nA |
| | | $V_{GS} = 0 V$ | V _{DS} = 60 V | - | - | 1 | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{GS} = 0 V$ | V _{DS} = 60 V, T _J = 125 °C | - | - | 50 | μA |
| | | $V_{GS} = 0 V$ | V _{DS} = 60 V, T _J = 175 °C | - | - | 250 | |
| On-State Drain Current ^a | I _{D(on)} | V _{GS} = 10 V | $V_{DS} \ge 5 V$ | 120 | - | - | Α |
| | | V _{GS} = 10 V | I _D = 30 A | - | 0.0045 | 0.0060 | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | V _{GS} = 10 V | I _D = 30 A, T _J = 125 °C | - | - | 0.0104 | Ω |
| | | $V_{GS} = 10 V$ | I _D = 30 A, T _J = 175 °C | - | - | 0.0129 | |
| Forward Transconductance b | 9 _{fs} | V _{DS} = 15 V, I _D = 30 A | | - | 94 | - | S |
| Dynamic ^b | | | | | | | |
| Input Capacitance | C _{iss} | | | - | 5196 | 6495 | |
| Output Capacitance | C _{oss} | $V_{GS} = 0 V$ | V _{DS} = 25 V, f = 1 MHz | - | 708 | 885 | pF |
| Reverse Transfer Capacitance | C _{rss} |] | | - | 336 | 420 | |
| Total Gate Charge ^c | Qg | | | - | 96.5 | 145 | |
| Gate-Source Charge ^c | Q _{gs} | $V_{GS} = 10 V$ | $V_{DS} = 30 \text{ V}, I_D = 75 \text{ A}$ | - | 24.6 | - | nC |
| Gate-Drain Charge ^c | Q _{gd} |] | | - | 27.2 | - | |
| Gate Resistance | R _g | f = 1 MHz | | 0.3 | 1 | 1.7 | Ω |
| Turn-On Delay Time ^c | t _{d(on)} | | | - | 16 | 24 | |
| Rise Time ^c | t _r | V_{DD} = 30 V, R_L = 0.4 Ω I_D \cong 75 A, V_{GEN} = 10 V, R_g = 1 Ω | | - | 14 | 21 | ns |
| Turn-Off Delay Time ^c | t _{d(off)} | | | - | 34 | 51 | |
| Fall Time ^c | t _f |] | | - | 9 | 14 | |
| Source-Drain Diode Ratings and Chara | acteristics ^b | | | | | | · |
| Pulsed Current ^a | I _{SM} | | | - | - | 480 | А |
| Forward Voltage | V _{SD} | F | = 75 A, V _{GS} = 0 | - | 0.9 | 1.5 | V |

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

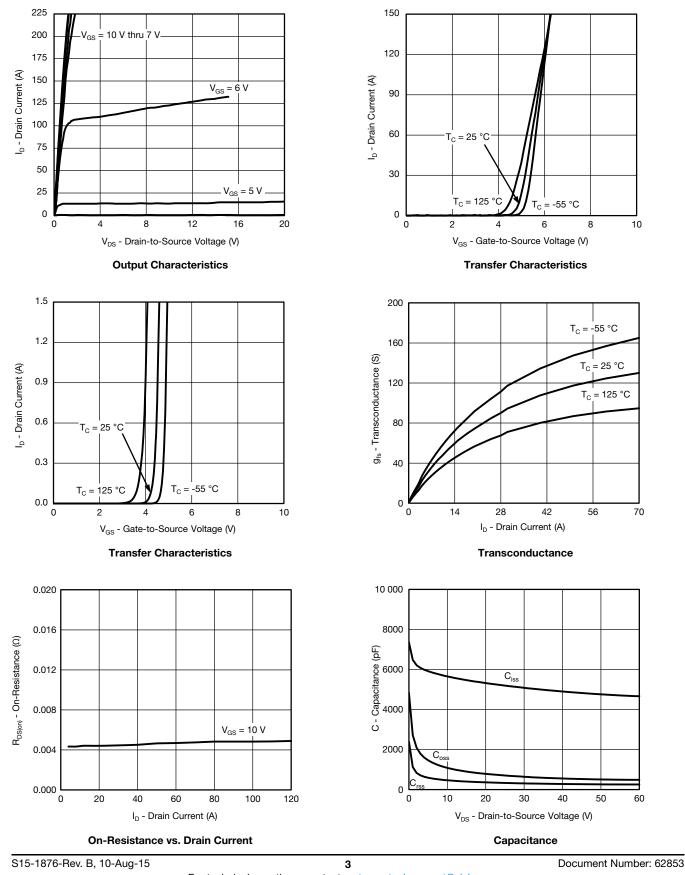
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2



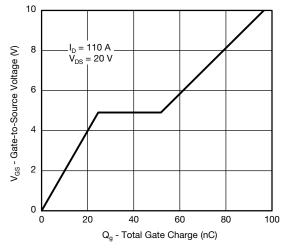
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



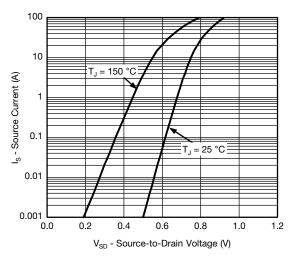
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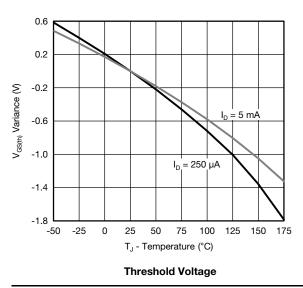
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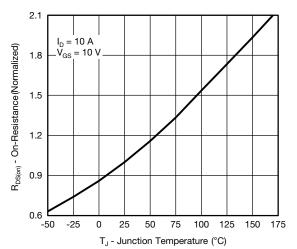


Gate Charge

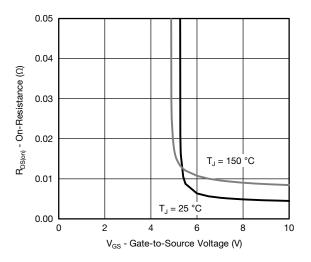


Source Drain Diode Forward Voltage

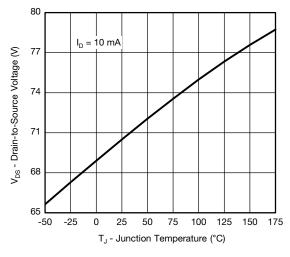




On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

S15-1876-Rev. B, 10-Aug-15

4

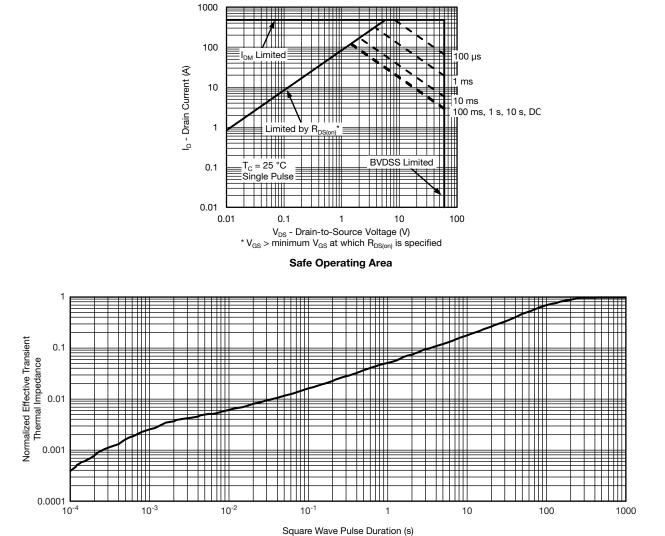
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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



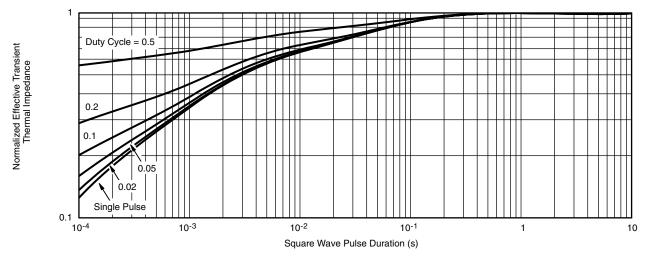
Normalized Thermal Transient Impedance, Junction-to-Ambient



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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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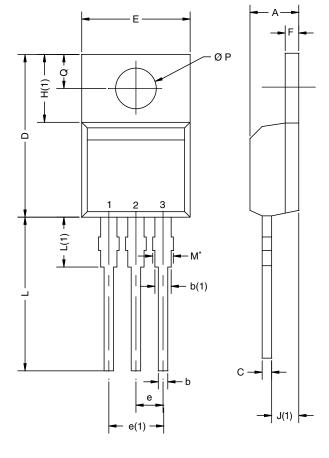
| REVISION | HISTORY ^a | |
|----------|----------------------|--------------------------------------|
| REVISION | DATE | DESCRIPTION OF CHANGE |
| В | 04-Aug-15 | Revised R _g minimum limit |

Note

a. As of April 2014



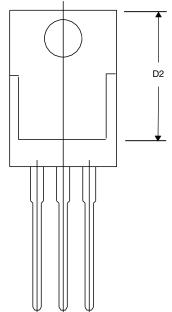
TO-220AB



| | MILLIN | IETERS | INCHES | | |
|-----------------------|-------------------|-----------|--------|-------|--|
| DIM. | MIN. | MAX. | MIN. | MAX. | |
| А | 4.25 | 4.65 | 0.167 | 0.183 | |
| b | 0.69 | 1.01 | 0.027 | 0.040 | |
| b(1) | 1.20 | 1.73 | 0.047 | 0.068 | |
| С | 0.36 | 0.61 | 0.014 | 0.024 | |
| D | 14.85 | 15.49 | 0.585 | 0.610 | |
| D2 | 12.19 | 12.70 | 0.480 | 0.500 | |
| Е | 10.04 | 10.51 | 0.395 | 0.414 | |
| е | 2.41 | 2.67 | 0.095 | 0.105 | |
| e(1) | 4.88 | 5.28 | 0.192 | 0.208 | |
| F | 1.14 | 1.40 | 0.045 | 0.055 | |
| H(1) | 6.09 | 6.48 | 0.240 | 0.255 | |
| J(1) | 2.41 | 2.92 | 0.095 | 0.115 | |
| L | 13.35 | 14.02 | 0.526 | 0.552 | |
| L(1) | 3.32 | 3.82 | 0.131 | 0.150 | |
| ØΡ | 3.54 | 3.94 | 0.139 | 0.155 | |
| Q | 2.60 | 3.00 | 0.102 | 0.118 | |
| ECN: T14- DWG: 547 | 0413-Rev. P, 1 | 16-Jun-14 | | | |

Note

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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