**VS-ETH1506FP-M3** 

**Vishay Semiconductors** 

- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- True 2 pin package

· Hyperfast soft recovery time

**FEATURES** 

- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION / APPLICATIONS**

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V			
Average rectified forward current in DC	I <sub>F(AV)</sub>	T <sub>C</sub> = 94 °C	15	۸			
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_J = 25 \ ^{\circ}C$	160	A			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	1.8	2.45	V	
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	1.25	1.6		
Povereo lookago ourrent		$V_{R} = V_{R}$ rated	-	0.01	15		
Reverse leakage current I		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	20	200	μA	
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	12	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH	

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## Hyperfast Rectifier, 15 A FRED Pt<sup>®</sup>



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2L TO-220 FullPAK

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	15 A				
V <sub>R</sub>	600 V				
V <sub>F</sub> at I <sub>F</sub>	1.25 V				
t <sub>rr</sub> (typ.)	21 ns				
T <sub>J</sub> max.	175 °C				
Package	2L TO-220 FullPAK				
Circuit configuration	Single				



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}$	/μs, V <sub>R</sub> = 30 V	-	21	26		
Reverse recovery time	+	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A}$	Α/μs, V <sub>R</sub> = 30 V	-	25	36	ns	
Reverse recovery time t <sub>rr</sub>	۲r	T <sub>J</sub> = 25 °C		-	29	-	115	
		T <sub>J</sub> = 125 °C	l <sub>F</sub> = 15 A, dl <sub>F</sub> /dt = 200 A/μs, V <sub>R</sub> = 390 V	-	65	-		
Peak recovery current		T <sub>J</sub> = 25 °C		-	3.9	-	- A	
Feak recovery current	IRRM	T <sub>J</sub> = 125 °C		-	7.0	-		
Poweree recevery charge	0	T <sub>J</sub> = 25 °C		-	60	-	nC	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	240	-	10	
Reverse recovery time	t <sub>rr</sub>		I <sub>F</sub> = 15 A,	-	42	-	ns	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 800 A/µs,	-	21	-	А	
Reverse recovery charge	Q <sub>rr</sub>		V <sub>R</sub> = 390 V	-	480	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C		
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	3.7	4.3			
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W		
Typical thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-			
Weight			-	2	-	g		
Weight			-	0.07	-	oz.		
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)		
Marking device		Case style 2L TO-220 FullPAK	ETH1506FP					



## VS-ETH1506FP-M3

### **Vishay Semiconductors**

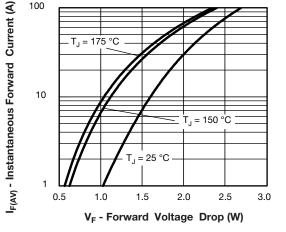


Fig. 1 - Typical Forward Voltage Drop Characteristics

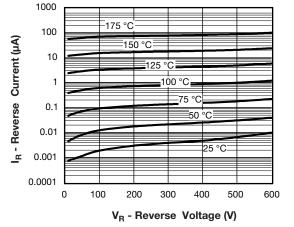


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

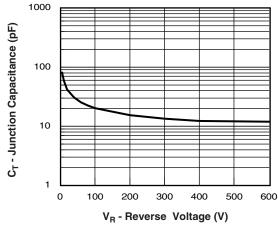
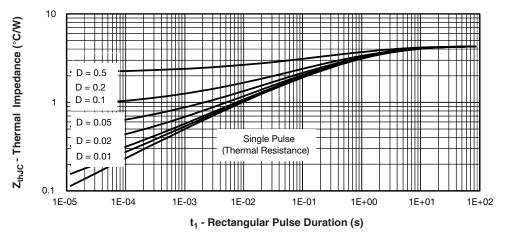
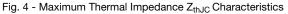
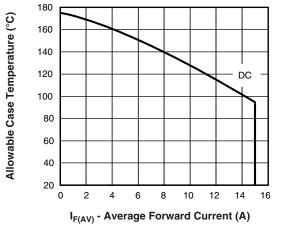


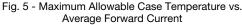
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage











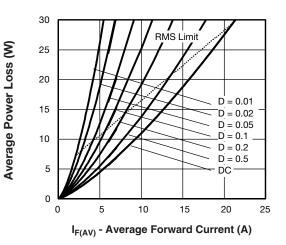


Fig. 6 - Forward Power Loss Characteristics

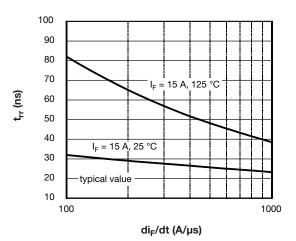
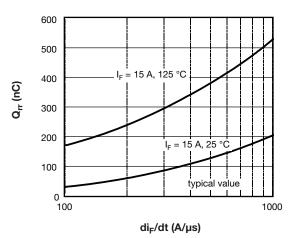
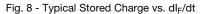
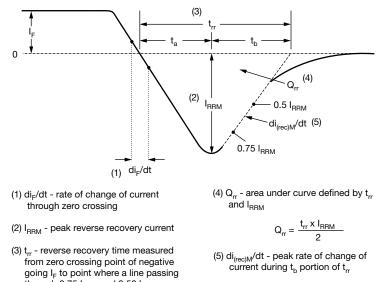


Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt







	going I <sub>F</sub> to point where a line passing through 0.75 I <sub>RRM</sub> and 0.50 I <sub>RRM</sub> extrapolated to zero current.	current during $t_b$ portion of $t_{rr}$	
	Fig. 9 - Reverse Recovery Wave	form and Definitions	
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#### **ORDERING INFORMATION TABLE**

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<b>.</b>		_	_					
Device code	VS-	E	Т	н	15	06	FP	-M3
		2	3	4	5	6	7	8
	1 .	- Visl	nay Sen	nicondu	ctors pr	oduct		
	2 -	- Circ	cuit con	figuratio	n:			
		E =	single					
	3 -	• T =	TO-220	)				
	4 -	• H=	hyperfa	ast recov	very tim	е		
	5 -	- Cur	rent co	de: 15 =	15 A			
	6	- Vol	tage coo	de: 06 =	600 V			
	7 -	FP	= 2L TC	)-220 Fi	IIIPAK			
	8 -	- Env	rironmer	ntal digit	:			
		-M3	3 = halo	gen-free	e, RoHS	-compli	ant, and	d termir

ORDERING INFORMATION (Example)							
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION							
VS-ETH1506FP-M3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?96157						
Part marking information	www.vishay.com/doc?95392					

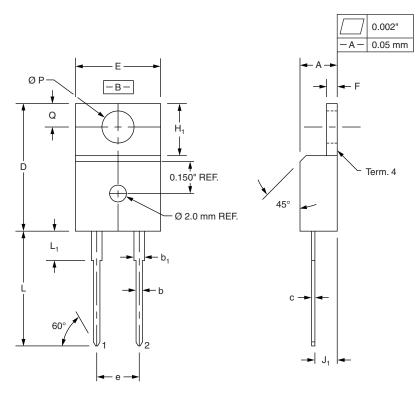




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# True 2 Pin TO-220

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INCH	IES	
STMBOL	MIN.	MAX.	MIN.	MAX.	
А	4.32	4.57	0.170	0.180	
b	0.71	0.91	0.028	0.036	
b <sub>1</sub>	1.15	1.39	0.045	0.055	
с	0.36	0.53	0.014	0.021	
D	14.99	15.49	0.590	0.610	
E	10.04	10.41	0.395	0.410	
e	5.08	BSC	0.200 BSC		
F	1.22	1.37	0.048	0.054	
H <sub>1</sub>	5.97	6.47	0.235	0.255	
J <sub>1</sub>	2.54	2.79	0.100	0.110	
L	13.47	13.97	0.530	0.550	
L <sub>1</sub> <sup>(1)</sup>	3.31	3.81	0.130	0.150	
ØP	3.79	3.88	0.149	0.153	
Q	2.60	2.84	0.102	0.112	

#### Notes

 $^{\left(1\right)}$  Lead dimension and finish uncontrolled in  $L_{1}$ 

• These dimensions are within allowable dimensions of JEDEC TO-220AB rev. J outline dated 3-24-87

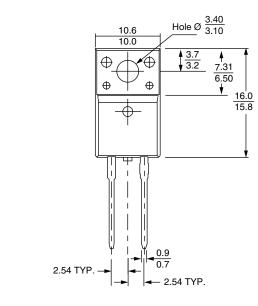
Controling dimension: Inch

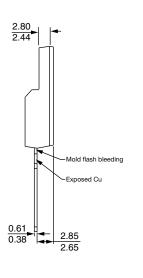


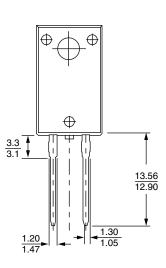
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# 2L TO-220 FullPAK

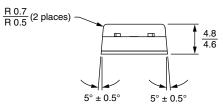
#### **DIMENSIONS** in millimeters







Bottom view





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