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HFA200FA120P

Vishay Semiconductors

HEXFRED[®] Ultrafast Soft Recovery Diode, 200 A



1200 V

2.7 V

150 ns

100 A at 69 °C

PRODUCT SUMMARY

 V_{R}

V_F (typical)

t_{rr} (typical)

 $I_{F(DC)}$ at T_C

| FEATURES | 5 |
|----------|---|
|----------|---|

- Fast recovery time characteristic
- Electrically isolated base plate
- Large creepage distance between terminal
- Simplified mechanical designs, rapid assembly
- UL approved file E78996
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

The dual diode series configuration (HFA200FA120P) is used for output rectification or freewheeling/clamping operation and high voltage application.

The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as HV power supplies, electronic welders, motor control and inverters.

| ABSOLUTE MAXIMUM RATINGS | | | | | |
|--|-----------------------------------|---|-------------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MAX. | UNITS | |
| Cathode to anode voltage | V _R | | 1200 | V | |
| Continuous forward current | IF | T _C = 69 °C | 100 | | |
| Single pulse forward current | I _{FSM} | T _J = 25 °C | 900 | A | |
| Maximum repetitive forward current | I _{FRM} | Rated $V_{R,}$ square wave, 20 kHz, T_{C} = 60 °C | 150 | | |
| Maximum power dissipation | Р | T _C = 25 °C | 416 | w | |
| Maximum power dissipation | PD | T _C = 100 °C | 166 | | |
| RMS isolation voltage | V _{ISOL} | Any terminal to case, t = 1 minute | 2500 | V | |
| Operating junction and storage temperature range | T _J , T _{Stg} | | - 55 to 150 | °C | |

| ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified) | | | | | | |
|--|-----------------|---|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Cathode to anode breakdown voltage | V _{BR} | I _R = 100 μA | 1200 | - | - | |
| Forward voltage | V _{FM} | I _F = 100 A | - | 2.68 | 3.6 | V |
| | | I _F = 200 A | - | 3.37 | 4.7 | |
| | | I _F = 100 A, T _J = 150 °C | - | 2.7 | 2.9 | 1 |
| Reverse leakage current | | $V_{R} = V_{R}$ rated | - | 10 | 75 | μA |
| | I _{RM} | $T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$ | - | 2 | - | mA |



COMPLIANT

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| DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified) | | | | | | | |
|---|------------------|-------------------------|---|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | MIN. | TYP. | MAX. | UNITS |
| Reverse recovery time t _{rr} | | T _J = 25 °C | I _F = 50 A dI _F /dt = - 200 A/μs V _R = 200 V | - | 150 | - | ns |
| | ۲rr | T _J = 125 °C | | - | 237 | - | |
| Peak recovery current | I _{RRM} | T _J = 25 °C | | - | 14 | - | A |
| | | T _J = 125 °C | | - | 21 | - | |
| Reverse recovery charge | Q _{rr} | T _J = 25 °C | | - | 1050 | - | nC |
| | | T _J = 125 °C | | - | 2430 | - | |

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | |
|---|-------------------|---------------------------|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Junction to case, single leg conducting | Б | | - | - | 0.3 | |
| Junction to case, both legs conducting | R _{thJC} | | - | - | 0.15 | °C/W |
| Case to heatsink | R _{thCS} | Flat, greased and surface | - | 0.05 | - | |
| Weight | | | - | 30 | - | g |
| Mounting torque | | | - | 1.3 | - | Nm |

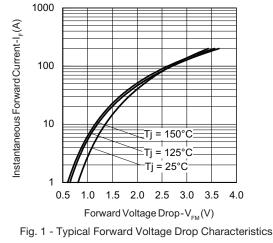
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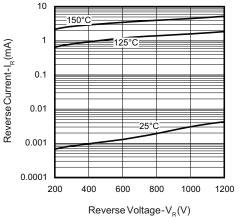


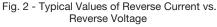
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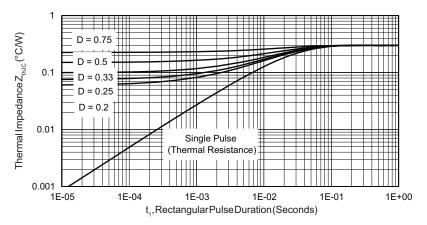


Fig. 3 - Maximum Thermal Impedance Z_{thJC} Characteristics

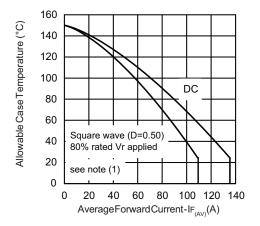


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

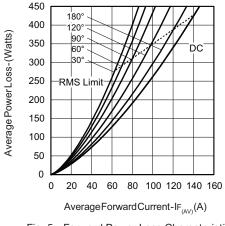


Fig. 5 - Forward Power Loss Characteristics

For technical questions within your region, please contact one of the following: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>

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ISHA

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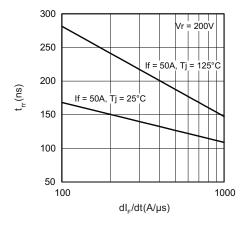


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

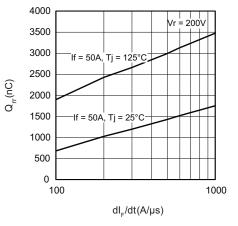


Fig. 7 - Typical Stored Charge vs. dl_F/dt

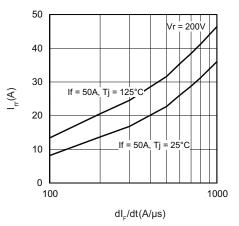


Fig. 8 - Typical Peak Recovery Current vs. dl_F/dt

Note

- (1) Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{th,JC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 5); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D); I_R at V_{R1} = Rated V_R$

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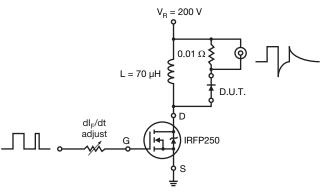


Fig. 9 - Reverse Recovery Parameter Test Circuit

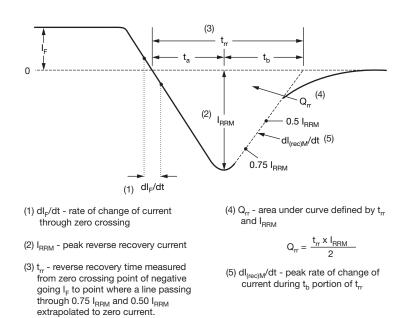


Fig. 10 - Reverse Recovery Waveform and Definitions

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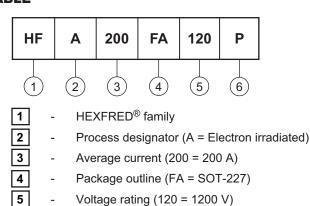
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Ultrafast Soft Recovery Diode, 200 A

ORDERING INFORMATION TABLE

Device code

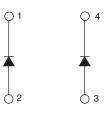


Average current (200 = 200 A)

- Package outline (FA = SOT-227)
- Voltage rating (120 = 1200 V)
- P = Lead (Pb)-free

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CIRCUIT CONFIGURATION



| LINKS TO RELATED DOCUMENTS | | | | | | |
|--|--------------------------|--|--|--|--|--|
| Dimensions <u>www.vishay.com/doc?95036</u> | | | | | | |
| Packaging information | www.vishay.com/doc?95037 | | | | | |

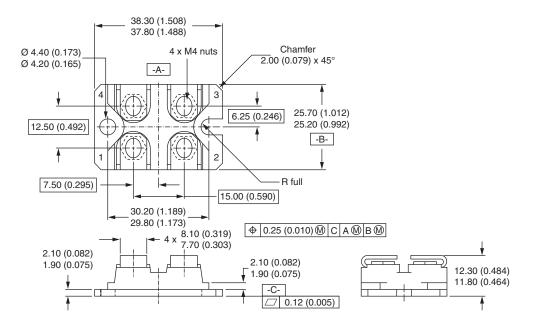


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SOT-227

DIMENSIONS in millimeters (inches)



Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
- Controlling dimension: millimeter



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