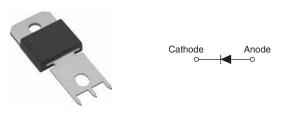
High Performance Schottky Rectifier, 100 A

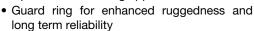


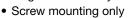
PowerTab®

PRODUCT SUMMARY		
Package	PowerTab [®]	
I _{F(AV)}	100 A	
V_{R}	15 V	
V _F at I _F	0.45 V	
I _{RM}	870 mA at 100 °C	
T _J max.	125 °C	
Diode variation	Single die	
E _{AS}	9 mJ	

FEATURES

- Ultralow forward voltage drop
- · Optimized for OR-ing applications





- AEC-Q101 qualified
- 125 °C max. operating junction temperature ($V_R < 5 \text{ V}$)
- High frequency operation
- Continuous high current operation
- PowerTab[®] package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>







DESCRIPTION

The VS-100BGQ015HF4 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
	Rectangular waveform	100	A	
I _{F(AV)}	T _C	88	°C	
V _{RRM}		15	V	
I _{FSM}	t _p = 5 μs sine	5000	А	
V	100 A _{pk} (typical)	0.39	V	
V _F	TJ	125	°C	
T _J	Range	-55 to +125	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VS-100BGQ015HF4	UNITS
Maximum DC vavaras valtars	V	T _J = 100 °C	15	V
Maximum DC reverse voltage	ximum DC reverse voltage V _R	T _J = 125 °C	5	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 88 °C, rectangular waveform		100	Α
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	5000	A
	IFSM	10 ms sine or 6 ms rect. pulse		1000	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4.5 mH		9	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 3 x V _R typical		Α	



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
) (1)	50 A	T _J = 25 °C	0.36	0.4	V
Forward voltage drop		100 A		0.45	0.52	
Forward voltage drop	V _{FM} ⁽¹⁾	50 A	T _J = 125 °C	0.27	0.31	
		100 A		0.39	0.45	
	I _{RM} ⁽¹⁾	T _J = 100 °C, V _R = 12 V		480	700	mA
		$T_{J} = 125 ^{\circ}\text{C}, V_{R} = 5 ^{\circ}\text{V}$		1	1.2	Α
Maximum reverse leakage current		T _J = 25 °C	V _R = Rated V _R	7	18	A
		T _J = 100 °C		580	870	mA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		38	00	pF
Typical series inductance	L _S	Measured from tab to mounting plane		3	.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature ran	ge T _J		-55 to +125	°C	
Maximum storage temperature ran	ge T _{Stg}		-55 to +150	C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.50	°C AM	
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.30	°C/W	
Approximate weight			5	g	
Approximate weight			0.18	OZ.	
Mounting torque	ım		1.2 (10)	N·m	
Mounting torque maximi	ım		2.4 (20)	(lbf \cdot in)	
Marking device		Case style PowerTab®	100BG	Q015H	

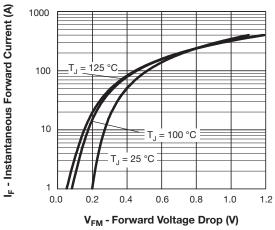


Fig. 1 - Maximum Forward Voltage Drop Characteristics

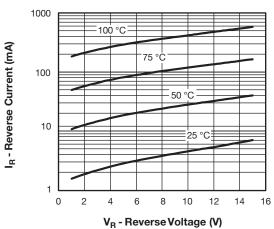


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

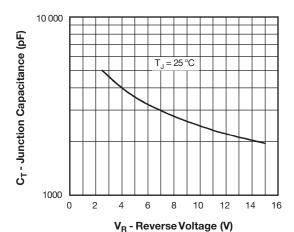


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

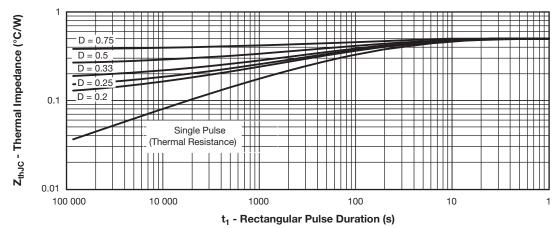


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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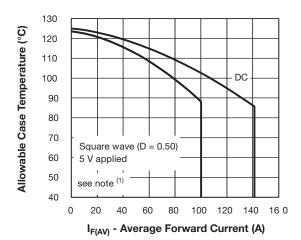
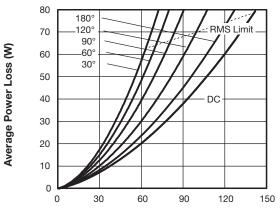
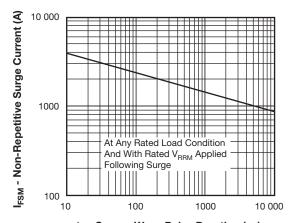


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics



 t_{p} - Square Wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current

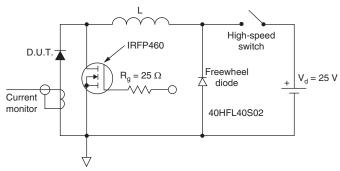


Fig. 8 - Unclamped Inductive Test Circuit

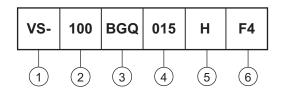
Note

 $^{(1)}$ Formula used: $T_C = T_J$ - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = $I_{F(AV)}$ x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 5 V



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- Current rating (100 = 100 A)
- Essential part number
- 4 Voltage rating (015 = 15 V)
- 5 H = AEC-Q101 qualified
- 6 Environmental digit:
 - F4 = RoHS compliant and totally lead (Pb)-free

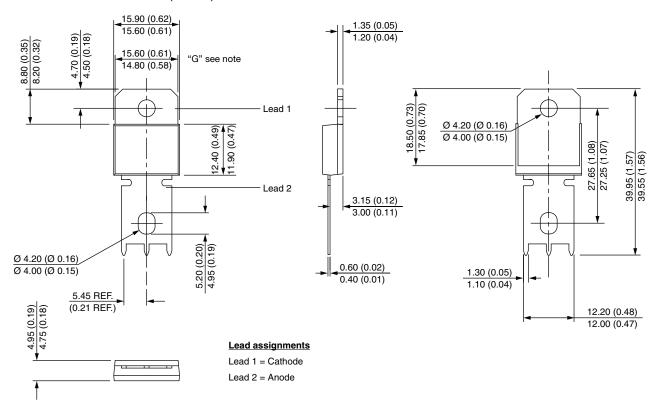
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-100BGQ015HF4	25	375	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95240</u>			
Part marking information	www.vishay.com/doc?95467		
SPICE model	www.vishay.com/doc?95428		
Application note	www.vishay.com/doc?95179		



PowerTab®

DIMENSIONS in millimeters (inches)



Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



Vishay

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