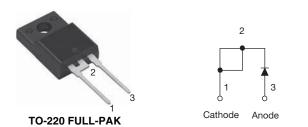
Vishay Semiconductors

High Voltage, Input Rectifier Diode, 20 A



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PRODUCT SUMMARY				
Package	TO-220FP			
I _{F(AV)}	20 A			
V _R	800 V to 1200 V			
V _F at I _F	1.1 V			
I _{FSM}	300 A			
T _J max.	150 °C			
Diode variation	Single die			

FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature
- · Glass passivated pellet chip junction
- Designed and qualified according to JEDEC[®]-JESD 47

Fully isolated package (V_{INS} = 2500 V_{RMS})



- UL E78996 approved
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Input rectification
- · Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS			
Capacitive input filter $T_A = 55 \text{ °C}$, $T_J = 125 \text{ °C}$ common heatsink of 1 °C/W	18	22	A	

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Sinusoidal waveform	20	A	
V _{RRM}	Range	800/1200	V	
I _{FSM}		300	A	
V _F	10 A, T _J = 25 °C	1.0	V	
TJ		-40 to +150	°C	

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA		
VS-20ETS08FPPbF, VS-20ETS08FP-M3	800	900	1		
VS-20ETS12FPPbF, VS-20ETS12FP-M3	1200	1300	1		



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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum average forward current	I _{F(AV)}	$T_C = 51 \text{ °C}$, 180° conduction half sine wave	20		
Maximum peak one cycle	1	10 ms sine pulse, rated V_{RRM} applied	250	A	
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	300		
Maximum I ² t for fusing	l ² t -	10 ms sine pulse, rated V_{RRM} applied	316	– A ² s	
		10 ms sine pulse, no voltage reapplied	442	A-2	
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	4420	A²√s	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM}	20 A, T _J = 25 °C		1.1	V
Forward slope resistance	r _t	— T _J = 150 °C		10.4	mΩ
Threshold voltage	V _{F(TO)}			0.85	V
Maximum reverse leakage current	1	$T_J = 25 \text{ °C}$ $V_B = \text{Rated } V_{BBM}$	0.1	mA	
Maximum reverse leakage current	IRM	T _J = 150 °C	VR - naieu VRRM	1.0	ШA

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage tempera	ture range	T _J , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	2.8	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.5	
Approximate weight				2	g
				0.07	oz.
Mounting torque minimum maximum				6.0 (5.0)	kgf ⋅ cm
				12 (10)	(lbf · in)
Marking device					S08FP
			Case style TO-220 FULL-PAK	20ETS12FP	



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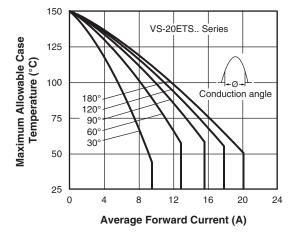


Fig. 1 - Current Rating Characteristics

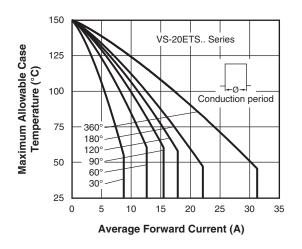


Fig. 2 - Current Rating Characteristics

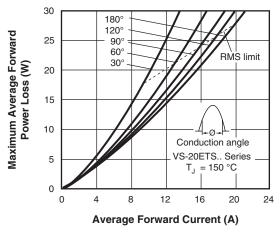


Fig. 3 - Forward Power Loss Characteristics

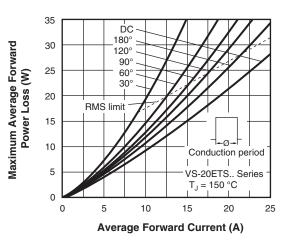
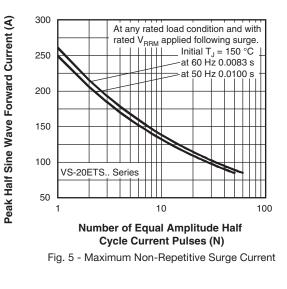


Fig. 4 - Forward Power Loss Characteristics



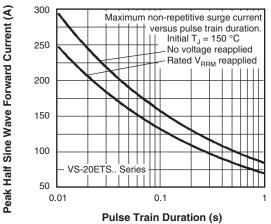


Fig. 6 - Maximum Non-Repetitive Surge Current

Revision: 12-Feb-16

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Document Number: 94339

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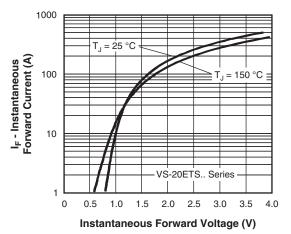


Fig. 7 - Forward Voltage Drop Characteristics

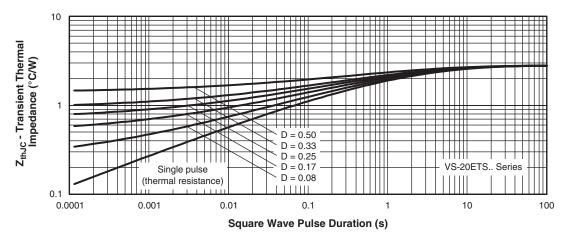


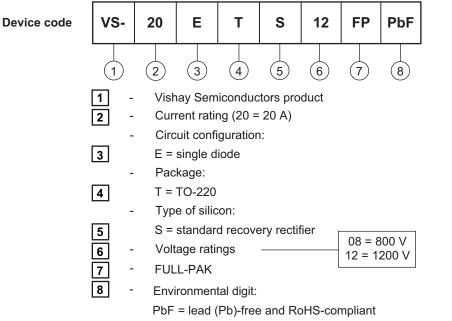
Fig. 8 - Thermal Impedance ZthJC Characteristics



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ORDERING INFORMATION TABLE



-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-20ETS08FPPbF	50	1000	Antistatic plastic tubes		
VS-20ETS08FP-M3	50	1000	Antistatic plastic tubes		
VS-20ETS12FPPbF	50	1000	Antistatic plastic tubes		
VS-20ETS12FP-M3	50	1000	Antistatic plastic tubes		

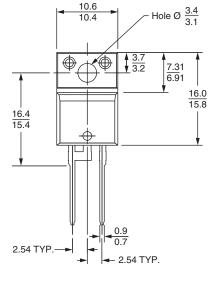
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95005				
Dart marking information	TO-220 FP PbF	www.vishay.com/doc?95009		
Part marking information	TO-220 FP -M3	www.vishay.com/doc?95440		



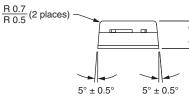
Outline Dimensions

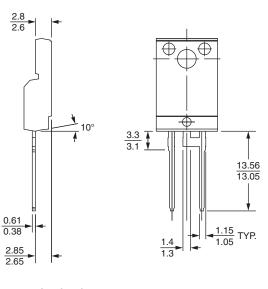
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DIMENSIONS in millimeters



 $\frac{4.8}{4.6}$





Lead assignments Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220 FULL-PAK



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