# 54-94-104MT..KPbF Series



# Three Phase AC Switch (Power Modules), 50 A to 100 A



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МТК

PRODUCT SUMMARY						
Ι <sub>Ο</sub>	50 A to 100 A					
V <sub>RRM</sub>	800 V to 1600 V					
Package	MT-K					
Circuit	Three phase AC switch					

### FEATURES

• Package fully compatible with the industry standard INT-A-PAK power modules series



COMPLIANT

- High thermal conductivity package, electrically insulated case
- Outstanding number of power encapsulated components
- · Excellent power volume ratio
- 4000 V<sub>RMS</sub> isolating voltage
- UL E78996 approved
- Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

A range of extremely compact, encapsulated three phase AC switches offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications as control motor starter.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	54MT.K	94MT.K	104MT.K	UNITS		
I		50	90	100	А		
I <sub>O</sub>	T <sub>C</sub>	80	80	80	°C		
1	50 Hz	390	950	1130	^		
I <sub>FSM</sub>	60 Hz	410	1000	1180	A		
l <sup>2</sup> t	50 Hz	770	4525	6380	A20		
1-1	60 Hz	700	4130	5830	A <sup>2</sup> s		
l²√t		7700	45250	63800	A²√s		
V <sub>RRM</sub>	Range	800 to 1600					
T <sub>Stg</sub>	Range	-40 to 125 °C					
TJ	Range		-40 to 125		°C		



### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS									
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>DRM</sub> , MAXIMUM REPETITIVE PEAK OFF-STATE VOLTAGE, GATE OPEN CIRCUIT V	I <sub>RRM</sub> /I <sub>DRM</sub> , MAXIMUM AT T <sub>J</sub> = 125 °C mA				
	80	800	900	800					
	100	1000	1100	1000					
54MTK	120	1200	1300	1200	20 (1)				
	140	1400	1500	1400					
	160	1600	1700	1600					
	80	800	900	800					
	100	1000	1100	1000					
94/104MTK	120	1200	1300	1200	40 (1)				
	140	1400	1500	1400					
	160	1600	1700	1600					

Note

<sup>(1)</sup> For single AC switch

FORWARD CONDUCT	ION							
PARAMETER	SYMBOL		TEST CONDITION	54MT.K	94MT.K	104MT.K	UNITS	
Maximum I <sub>RMS</sub> output current		For all condu	For all conduction angle		50	90	100	Α
at case temperature	Ι <sub>Ο</sub>	For all condu	iction angle		80	80	80	°C
		t = 10 ms	No voltage		390	950	1130	
Maximum peak, one-cycle forward, non-repetitive		t = 8.3 ms	reapplied		410	1000	1180	A
on state surge current	I <sub>TSM</sub>	t = 10 ms	100 % V <sub>RBM</sub>		330	800	950	A
		t = 8.3 ms	reapplied	Initial $T_J = T_J$	345	840	1000	
		t = 10 ms	No voltage	maximum	770	4525	6380	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	t = 8.3 ms	reapplied		700	4130	5830	A <sup>2</sup> s
Maximum - tior fusing	1-1	t = 10 ms	100 % V <sub>BBM</sub>		540	3200	4510	A <sup>2</sup> S
		t = 8.3 ms reapplied	reapplied		500	2920	4120	
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 to 10	ms, no voltage r	eapplied	7700	45 250	63 800	A²√s
Low level value of threshold voltage	V <sub>T(TO)1</sub>	(16.7 % x π >	(16.7 % x $\pi$ x $I_{T(AV)} < I < \pi$ x $I_{T(AV)}$ ), T <sub>J</sub> maximum				0.99	V
High level value of threshold voltage	V <sub>T(TO)2</sub>	$(I > \pi \times I_{T(AV)})$	, T <sub>J</sub> maximum	1.44	1.19	1.15	v	
Low level value on-state slope resistance	r <sub>t1</sub>	16.7 % x π x	$I_{T(AV)} < I < \pi \times I_{T}$	12.54	4.16	3.90		
High level value on-state slope resistance	r <sub>t2</sub>	$(I > \pi \times I_{T(AV)})$	, T <sub>J</sub> maximum	11.00	3.56	3.48	mΩ	
Maximum on-state voltage drop	V <sub>TM</sub>	l <sub>pk</sub> = 150 A, 1 t <sub>p</sub> = 400 μs s	Γ <sub>J</sub> = 25 °C ingle junction	2.68	1.55	1.53	V	
Maximum non-repetitve rate of rise of turned on current	dl/dt	$T_J = 25 \text{ °C}, \text{ from } 0.67 \text{ V}_{DRM}, I_{TM} = \pi \text{ x } I_{T(AV)}, I_g = 500 \text{ mA}, t_r < 0.5 \text{ µs}, t_p > 6 \text{ µs}$				150		A/µs
Maximum holding current	Ι <sub>Η</sub>		$T_J = 25 \text{ °C}$ , anode supply = 6 V, resistive load, grate open circuit 200					mA
Maximum latching current	١ <sub>L</sub>	T <sub>J</sub> = 25 °C, a	node supply = 6	V, resistive load		400		1

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BEOCKING						
PARAMETER	SYMBOL	TEST CONDITIONS	54MT.K	94MT.K	104MT.K	UNITS
RMS isolation voltage	V <sub>INS</sub>	$T_J = 25 \text{ °C}$ all terminal shorted f = 50 Hz, t = 1 s	4000		V	
Maximum critical rate of rise of off-state voltage	dV/dt <sup>(1)</sup>	$T_J = T_J$ maximum, linear to 0.67 V <sub>DRM</sub> , gate open circuit	500		V/µs	

#### Note

<sup>(1)</sup> Available with dV/dt = 1000 V/ $\mu$ s, to complete code add S90 i. e. 104MT160KBS90

TRIGGERING							
PARAMETER	SYMBOL		TEST CONDITIONS 54MT.K 94MT.K 104MT.K				UNITS
Maximum peak gate power	P <sub>GM</sub>				10		w
Maximum average gate power	P <sub>G(AV)</sub>				2.5		vv
Maximum peak gate current	I <sub>GM</sub>	T <sub>J</sub> = T <sub>J</sub> maxir	num		2.5		А
Maximum peak negative gate voltage	- V <sub>GT</sub>				10		
		$T_J = 40 \ ^{\circ}C$			4.0		V
Maximum required DC gate voltage to trigger	V <sub>GT</sub>	T <sub>J</sub> = 25 °C		2.5			
voltage to trigger		$T_J = 125 \text{ °C}$ Anode supply = 6 V, resistive		$T_J = 125 \text{ °C}$ Anode supply = 6 V, resistive 1.7			
		$T_J = -40 \ ^{\circ}C$	$T_J = -40 \ ^{\circ}C$ load		270		
Maximum required DC gate current to trigger	I <sub>GT</sub>	T <sub>J</sub> = 25 °C			150		mA
		T <sub>J</sub> = 125 °C			80		
Maximum gate voltage that will not trigger	$V_{GD}$	T T maximum rated V applied			0.25		V
Maximum gate current that will not trigger	I <sub>GD</sub>	ij= ij maxir	$T_J = T_J$ maximum, rated $V_{DRM}$ applied		6		mA

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	54MT.K	94MT.K	104MT.K	UNITS
Maximum junction operating and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>			-40 to 125		
		DC operation per single AC switch	0.52	0.39	0.34	
Maximum thermal resistance,	Р	DC operation per junction	1.05	0.77	0.69	
junction to case	R <sub>thJC</sub>	180 °C sine cond. angle per single AC switch	0 °C sine cond. angle per single AC switch 0.56 0.4		0.36	к/w
		180 °C sine cond. angle per junction	1.12	0.80	1011	
Maximum thermal resistance, case to heatsink	R <sub>thCS</sub>	Per module Mounting surface smooth, flat and grased	0.03			
Mounting to heatsink		A mounting compound is recommended and		4 to 6		Nm
to terminal		the torque should be rechecked after a period of 3 hours to allow for the spread of		3 to 4		
Approximate weight		period of 3 hours to allow for the spread of the compound. Lubricated threads.				g

DEVICES		SINUSOIDAL CONDUCTION AT T <sub>J</sub> MAXIMUM					RECTANGULAR CONDUCTION AT T <sub>J</sub> MAXIMUM				UNITS
	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	
54MT.K	0.072	0.085	0.108	0.152	0.233	0.055	0.091	0.117	0.157	0.236	
94MT.K	0.033	0.039	0.051	0.069	0.099	0.027	0.044	0.055	0.071	0.100	K/W
104MT.K	0.027	0.033	0.042	0.057	0.081	0.023	0.037	0.046	0.059	0.082	

Note

Table shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC

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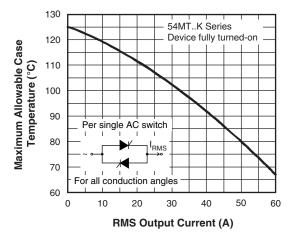


Fig. 1 - Current Ratings Characteristic

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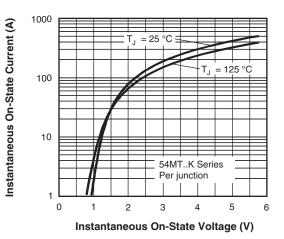
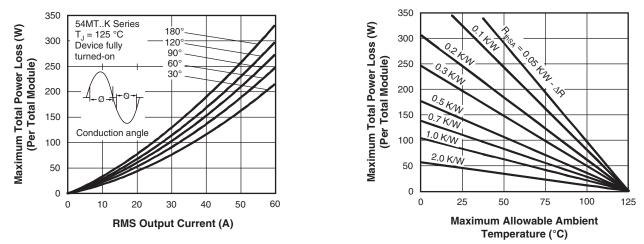
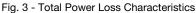
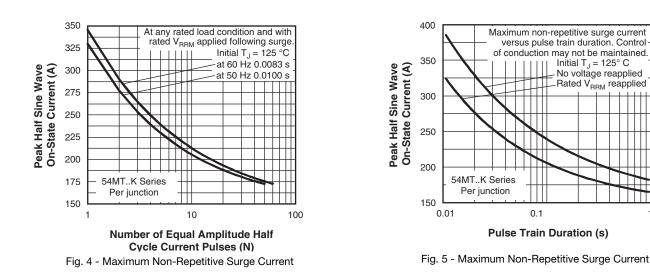


Fig. 2 - Forward Voltage Drop Characteristics







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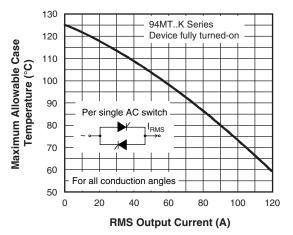


Fig. 6 - Current Ratings Characteristic

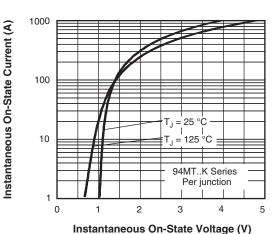
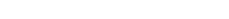
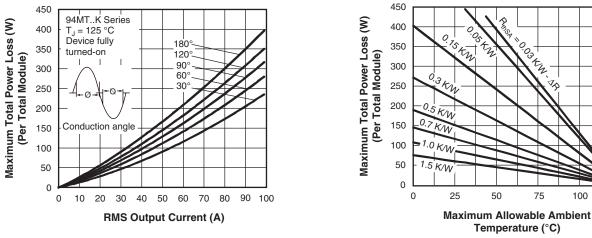
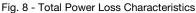
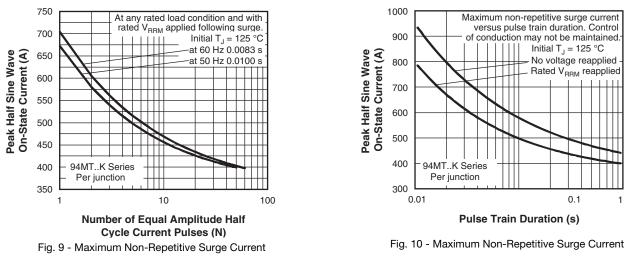


Fig. 7 - Forward Voltage Drop Characteristics









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125

Initial T<sub>J</sub> = 125 °C

No voltage reapplied

Rated V<sub>RRM</sub> reapplied

0.1

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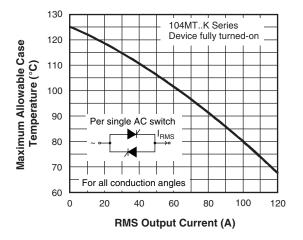
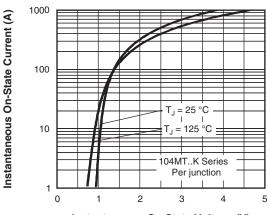
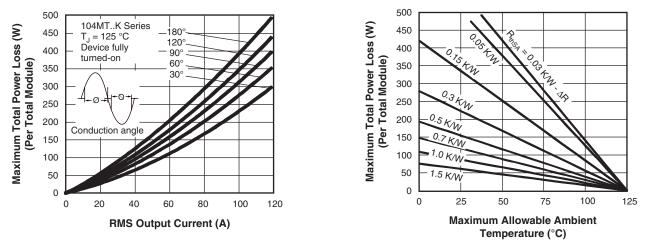


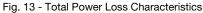
Fig. 11 - Current Ratings Characteristic



Instantaneous On-State Voltage (V)

Fig. 12 - Forward Voltage Drop Characteristics





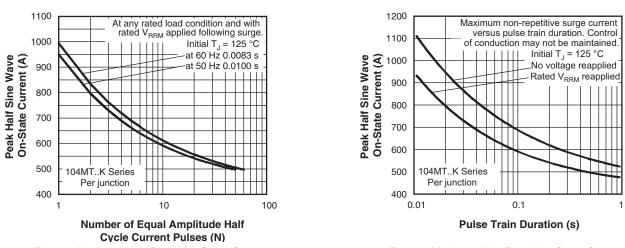


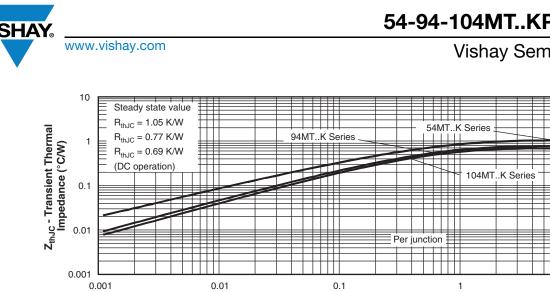
Fig. 14 - Maximum Non-Repetitive Surge Current

Fig. 15 - Maximum Non-Repetitive Surge Current

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Square Wave Pulse Duration (s)

Fig. 16 - Thermal Impedance ZthJC Characteristics

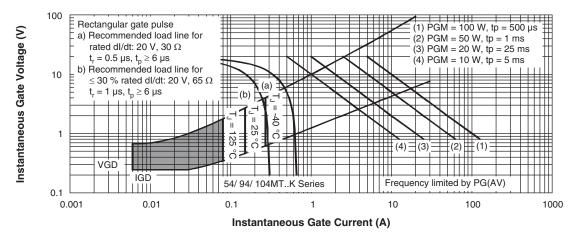
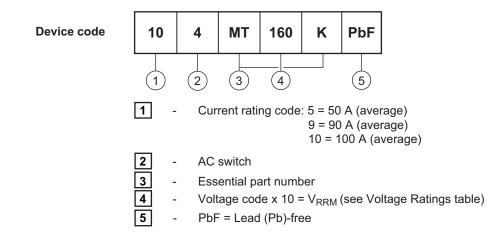


Fig. 17 - Gate Characteristics

### **ORDERING INFORMATION TABLE**



#### Note

• To order the optional hardware go to www.vishay.com/doc?95172

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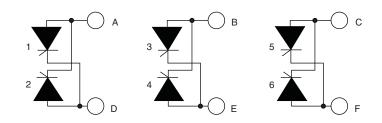
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### **CIRCUIT CONFIGURATION**



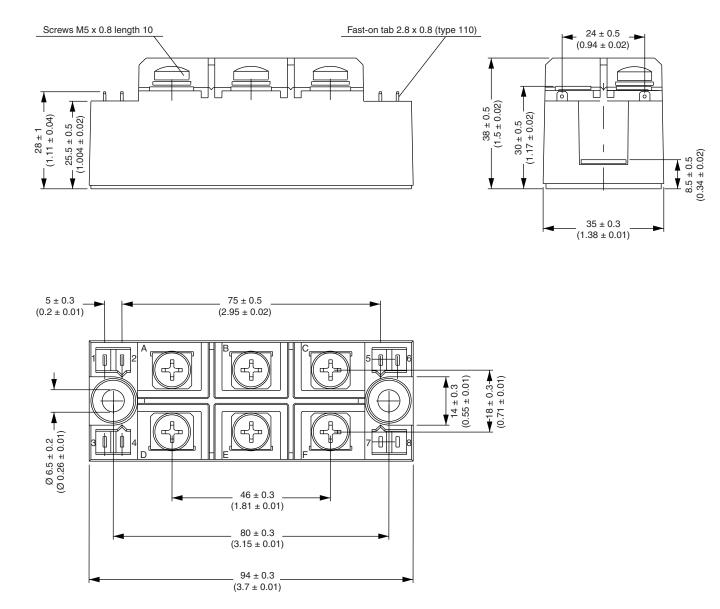
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95004			



# MTK (with and without optional barrier)

### DIMENSIONS WITH OPTIONAL BARRIERS in millimeters (inches)

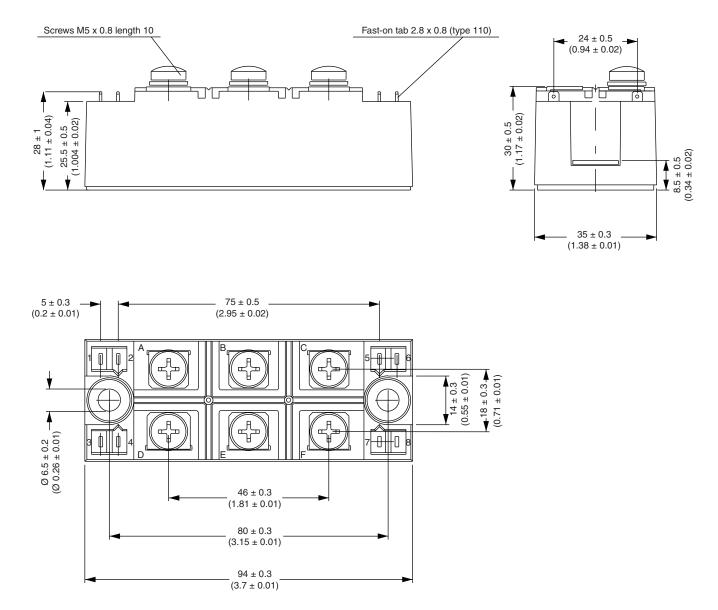
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Vishay Semiconductors MTK (with and without optional barrier)



### DIMENSIONS WITHOUT OPTIONAL BARRIERS in millimeters (inches)





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