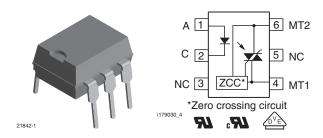


Optocoupler, Phototriac Output, Zero Crossing, High dV/dt, Low Input Current



DESCRIPTION

The VO4154 and VO4156 consists of a GaAs IRLED optically coupled to a photosensitive zero crossing TRIAC packaged in a DIP-6 package.

High input sensitivity is achieved by using an emitter follower phototransistor and a cascaded SCR predriver resulting in an LED trigger current of 1.6 mA for bin D, 2 mA for bin H, and 3 mA for bin M.

The new phototriac zero crossing family uses a proprietary dV/dt clamp resulting in a static dV/dt of greater than 5 kV/µs.

The VO4154 and VO4156 isolates low-voltage logic from 120 V_{AC} , 240 V_{AC} , and 380 V_{AC} lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

FEATURES

- High static dV/dt 5 kV/µs
- High input sensitivity I_{FT} = 1.6 mA, 2 mA, and
- 300 mA on-state current
- · Zero voltage crossing detector
- 400 V and 600 V blocking voltage
- Isolation rated voltage 4420 V_{RMS}
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

APPLICATIONS

- Solid-state relays
- · Industrial controls
- Office equipment
- Consumer appliances

AGENCY APPROVALS

- UL1577, file no. E52744, double protection
- cUL file no. E52744, equivalent to CSA bulletin 5A

w.vishav.com/doc?91000

• DIN EN 60747-5-5 (VDE 0884-5), available with option 1

ORDERING INFORMATION							
V O 4 1 5 # X - X 0 0 # T PART NUMBER PACKAGE OPTION TAPE AND REEL Option 7 Option 9 0							
AGENCY		V _{DRM} 400		V _{DRM} 600			
CERTIFIED/PACKAGE	TRIGGER CURRENT, I _{FT} (mA)						
UL, cUL	1.6	2	3	1.6	2	3	
DIP-6	VO4154D	VO4154H	VO4154M	VO4156D	VO4156H	VO4156M	
DIP-6, 400 mil, option 6	VO4154D-X006	VO4154H-X006	VO4154M-X006	VO4156D-X006	VO4156H-X006	VO4156M-X006	
SMD-6, option 7	VO4154D-X007T	VO4154H-X007T	VO4154M-X007T	VO4156D-X007T	VO4156H-X007T (1)	VO4156M-X007T	
UL, cUL, VDE	1.6	2	3	1.6	2	3	
DIP-6, 400 mil, option 6	-	-	-	-	VO4156H-X016	-	
SMD-6, option 7	-	-	-	VO4156D-X017T	-	-	

Note

· Also available in tubes, do not put "T" to the end



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT			
INPUT								
Reverse voltage			V _R	6	V			
Forward current			I _F	60	mA			
Surge current			I _{FSM}	2.5	Α			
Power dissipation			P _{diss}	100	mW			
Derate from 25 °C				1.33	mW/°C			
OUTPUT								
Peak off-state voltage		VO4154D/H/M	V_{DRM}	400	V			
		VO4156D/H/M	V_{DRM}	600	V			
RMS on-state current			I _{TM}	300	mA			
Total power dissipation			P _{diss}	500	mW			
Derate from 25 °C				6.6	mW/°C			
COUPLER								
Storage temperature range			T _{stg}	-55 to +150	°C			
Ambient temperature range			T _{amb}	-55 to +100	°C			
Soldering temperature	Max. ≤ 10 s dip soldering ≥ 0.5 mm from case bottom		T _{sld}	260	°C			

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

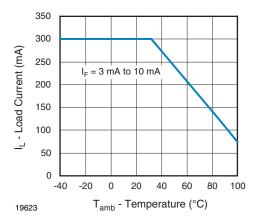


Fig. 1 - Recommended Operating Condition



THERMAL CHARACTERISTICS			
PARAMETER	SYMBOL	VALUE	UNIT
LED power dissipation	P _{diss}	100	mW
Output power dissipation	P _{diss}	500	mW
Maximum LED junction temperature	T _{jmax.}	125	°C
Maximum output die junction temperature	T _{jmax.}	125	°C
Thermal resistance, junction emitter to board	θ_{JEB}	150	°C/W
Thermal resistance, junction emitter to case	θ_{JEC}	139	°C/W
Thermal resistance, junction detector to board	θ_{JDB}	78	°C/W
Thermal resistance, junction detector to case	θ_{JDC}	103	°C/W
Thermal resistance, junction emitter to junction detector	θ_{JED}	496	°C/W
Thermal resistance, case to ambient	θ_{CA}	3563	°C/W

Note

The thermal characteristics table above were measured at 25 °C and the thermal model is represented in the thermal network below. Each
resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal
resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation
of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT			'				
Forward voltage	I _F = 10 mA		V_{F}	-	1.2	1.4	V
Reverse current	V _R = 6 V		I _R	-	0.1	10	μA
Input capacitance	V _F = 0 V, f = 1 MHz		C _I	-	25	-	рF
OUTPUT							
Depatitive peak off state voltage	100	VO4154D/H/M	V_{DRM}	400	-	-	V
Repetitive peak off-state voltage	$I_{DRM} = 100 \mu A$	VO4156D/H/M	V_{DRM}	600	-	-	V
Off-state current	$V_D = V_{DRM}$, $I_F = 0$ A		I _{DRM}	-	-	100	μΑ
On-state voltage	I _T = 300 mA		V_{TM}	-	-	3	V
On-state current	$PF = 1, V_{T(RMS)} = 1.7 V$		I _{TM}	-	-	300	mA
Off-state current in inhibit state	$I_F = 2 \text{ mA}, V_{DRM}$		I _{DINH}	-	-	200	μΑ
Holding current			I _H	-	-	500	μA
Zero cross inhibit voltage	$I_F = \text{rated } I_{FT}$		V _{IH}	-	-	20	V
Critical rate of rise of off-state voltage	V _D = 0.67 V _{DRM} , T _J = 25 °C		dV/dt _{cr}	5000	-	-	V/µs
Critical rate of rise of on-state			dV/dt _{cr}	8	-	-	A/µs
COUPLER							
		VO4154D	I _{FT}	-	-	1.6	mA
		VO4154H	I _{FT}	-	-	2	mA
LED trigger current,	V _D = 3 V	VO4154M	I _{FT}	-	-	3	mA
current required to latch output	ν _D = 3 ν	VO4156D	I _{FT}	-	-	1.6	mA
		VO4156H	I _{FT}	-	-	2	mA
		VO4156M	I _{FT}	-	-	3	mA
Common mode coupling capacitance			ССМ	-	0.01	-	pF
Capacitance (input to output)	f = 1 MHz, V _{IO} = 0 V		C _{IO}	-	0.8	-	рF

Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 100 / 21				
Comparative tracking index		CTI	175				
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}			
Maximum transient isolation voltage		V _{IOTM}	8000	V _{peak}			
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}			
la dation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω			
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω			
Output safety power		P _{SO}	500	mW			
Input safety current		I _{SI}	250	mA			
Safety temperature		T _S	175	°C			
Creepage distance			≥ 7	mm			
Clearance distance			≥ 7	mm			
Insulation thickness		DTI	≥ 0.4	mm			
Pollution degree (DIN VDE 0109)			2				

Note

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

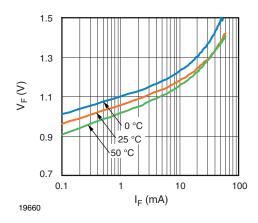


Fig. 2 - Diode Forward Voltage vs. Forward Current

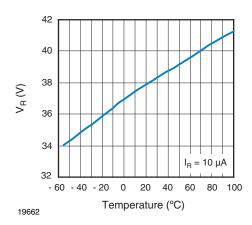


Fig. 3 - Diode Reverse Voltage vs. Temperature

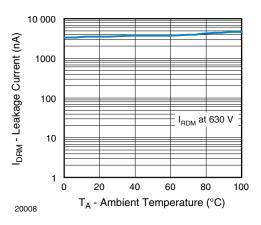


Fig. 4 - Leakage Current vs. Ambient Temperature

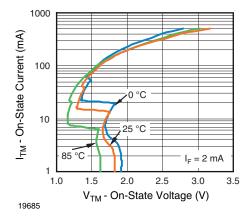


Fig. 5 - On-State Current vs. On-State Voltage

[•] As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

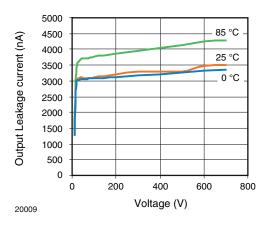


Fig. 6 - Output Off Current (Leakage) vs. Voltage

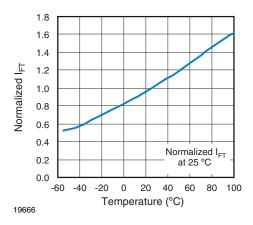


Fig. 7 - Normalized Trigger Input Current vs. Temperature

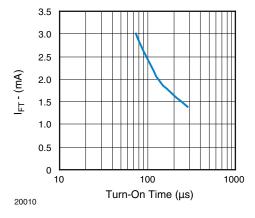


Fig. 8 - I_{FT} (mA) vs. Turn-On Time (μ s)

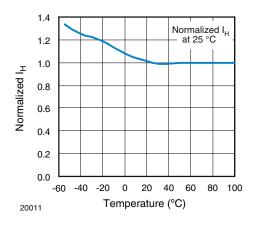


Fig. 9 - Normalized Holding Current vs. Temperature

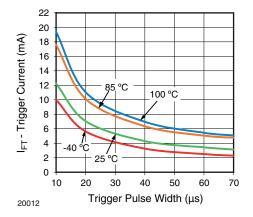
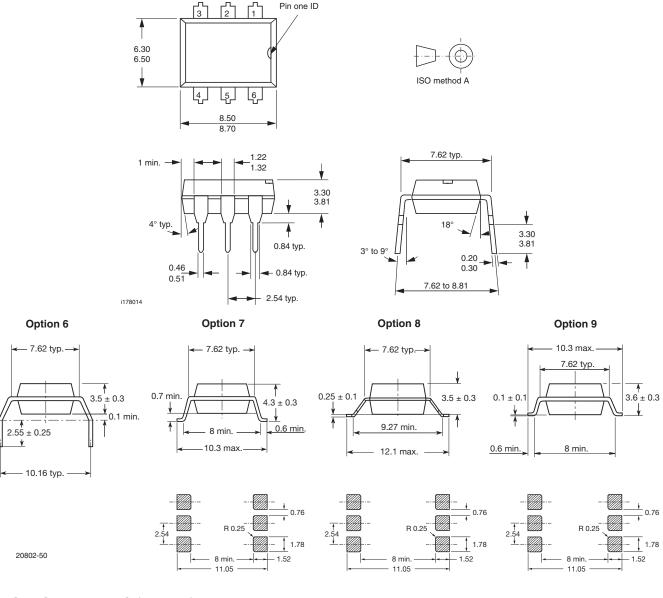


Fig. 10 - I_{FT} vs. LED Pulse Width



PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING (example)



Notes

- Only options 1, 7, and 8 are reflected in the package marking
- The VDE Logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

单击下面可查看定价,库存,交付和生命周期等信息

>>Vishay(威世)