VISHAY.

VS-82CNQ030A PbF Series

Vishay High Power Products

RoHS COMPLIANT

Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

3

Anode

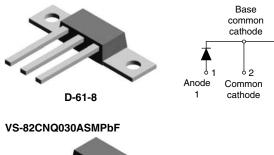
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3

Anode

2

VS-82CNQ030APbF

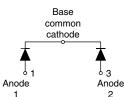






VS-82CNQ030ASLPbF





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Common

cathode

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Anode

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PRODUCT SUMMARY IF(AV) 2 x 40 A VR 30 V

FEATURES

- 150 °C T_J operation
- Dual center tap module
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mould low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	80	A	
V _{RRM}		30	V	
I _{FSM}	t _p = 5 μs sine	5100	А	
V _F	40 Apk, T _J = 125 °C (per leg)	0.37	V	
TJ	Range	- 55 to 150	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-82CNQ030APbF	UNITS		
Maximum DC reverse voltage	V _R	30	V		
Maximum working peak reverse voltage	V _{RWM}	30			

* Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 119 °C, rectangular waveform		80	
Maximum peak one cycle non-repetitive surge current per leg		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	5100	A
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		880	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 8 A, L = 1.12 mH		36	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		8	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	L TEST CONDITIONS VA		VALUES	UNITS
		40 A	T 05 %C	0.47	
Maximum forward voltage drop per leg	V (1)	80 A	T _J = 25 °C	0.55	V
See fig. 1	V _{FM} ⁽¹⁾	40 A	T 105 00 0.37	v	
		80 A	T _J = 125 °C	0.37 0.47 5	
Maximum reverse leakage current per leg		T _J = 25 °C		5	mA
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	280	ma
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		3700	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

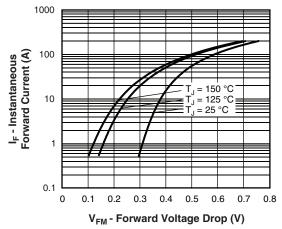
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance,	per leg	P	DC operation (see fig. 4)	0.85		
junction to case	per package	R _{thJC}	DC operation	0.42	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	0/11	
A				7.8	g	
Approximate weight				0.28	oz.	
Mounting torque	minimum			40 (35)	kgf · cm	
	maximum			58 (50)	(lbf · in)	
Marking device			Case style D-61	82CNQ	030A	
			Case style D-61-8-SM	82CNQ0	82CNQ030ASM	
			Case style D-61-8-SL	82CNQ0	30ASL	

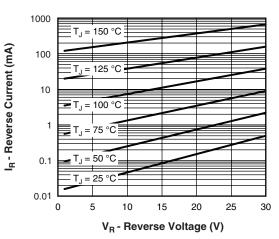


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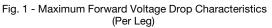


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

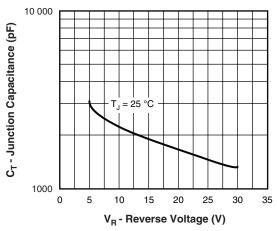


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

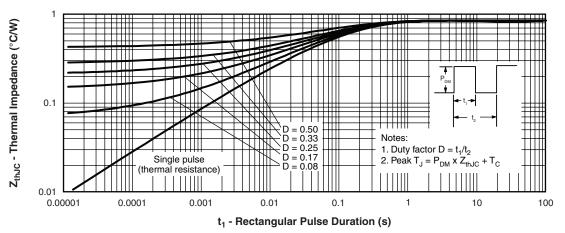
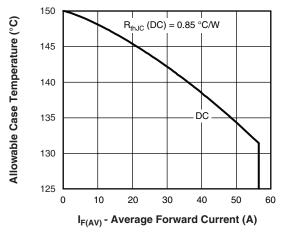


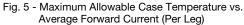
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

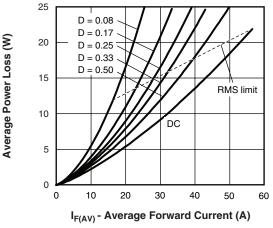
VS-82CNQ030A PbF Series

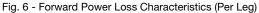
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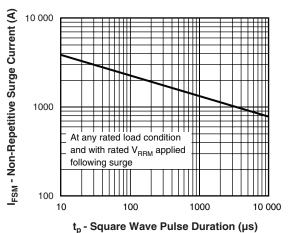


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

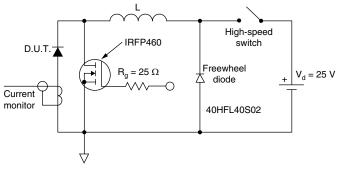


Fig. 8 - Unclamped Inductive Test Circuit

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{CAVA} \times V_{EM}$ at (I_{CAVA}/D) (see fig.
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

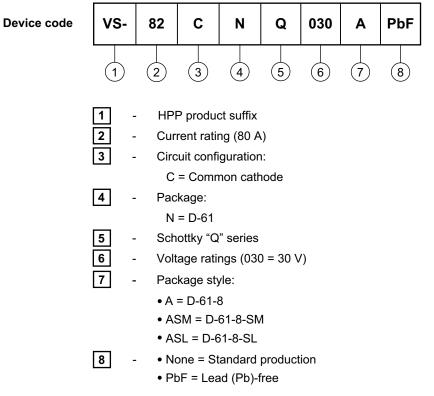


Schottky Rectifier

Vishay High Power Products

New Generation 3 D-61 Package, 2 x 40 A

ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

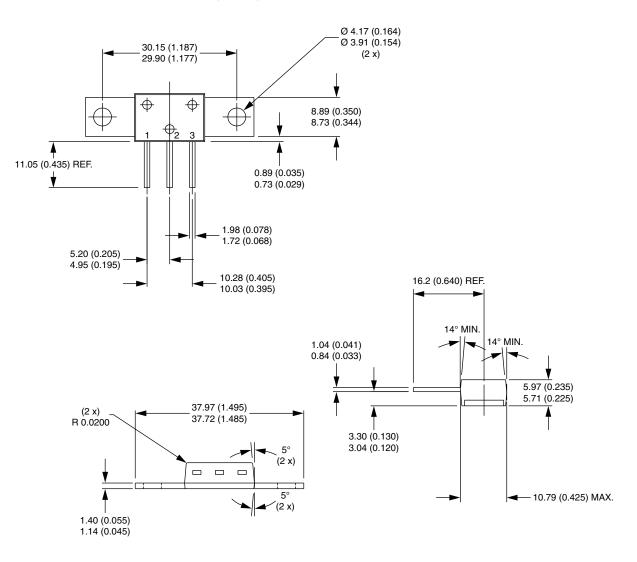
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95354		
Part marking information	www.vishay.com/doc?95356		

Vishay Semiconductors



D-61-8, D-61-8-SM, D-61-8-SL

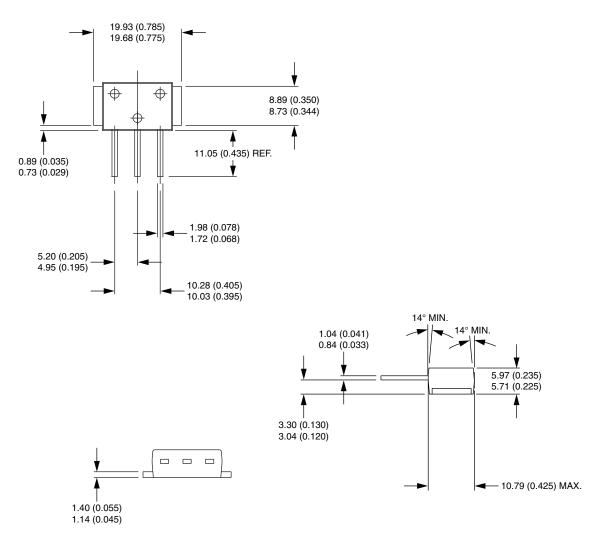
DIMENSIONS - D-61-8 in millimeters (inches)





DIMENSIONS - D-61-8-SM in millimeters (inches)

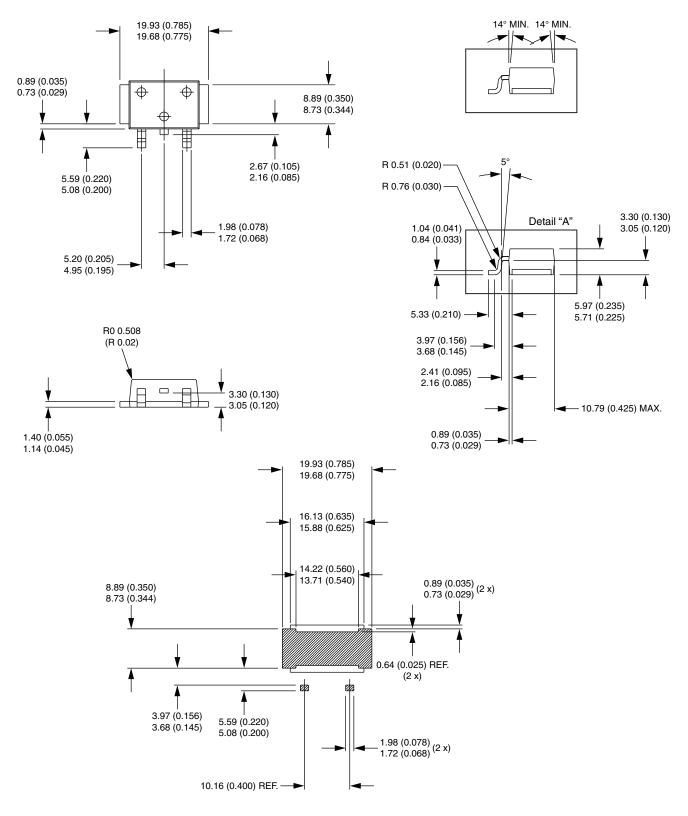
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DIMENSIONS - D-61-8-SL in millimeters (inches)

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

单击下面可查看定价,库存,交付和生命周期等信息

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