

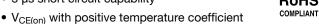
Half Bridge IGBT Power Module, 600 V, 100 A



PRIMARY CHARACTERISTICS					
V_{CES}	600 V				
I_C at T_C = 80 °C	100 A				
$V_{CE(on)}$ (typical) at $I_C = 100$ A, 25 °C	1.65 V				
Speed	8 kHz to 30 kHz				
Package	INT-A-PAK				
Circuit configuration	Half bridge				

FEATURES

- Low V_{CE(on)} trench IGBT technology
- 5 µs short circuit capability



- Maximum junction temperature 175 °C
- · Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (direct copper bonding) technology
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

- UPS (uninterruptable power supply)
- · Switching mode power supplies
- · Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as UPS and SMPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
Gate to emitter voltage	V_{GES}		± 20	V	
Collector current	I.	T _C = 25 °C	160		
	IC	T _C = 80 °C	100		
Pulsed collector current	I _{CM} ⁽¹⁾	$t_p = 1 \text{ ms}$	200	Α	
Diode continuous forward current	I _F	T _C = 80 °C	100		
Diode maximum forward current	I _{FM} ⁽¹⁾	t _p = 1 ms	200		
Maximum power dissipation	P _D	T _J = 175 °C	417	W	
Short circuit withstand time	t _{SC}	T _C = 125 °C	5	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	4000	V	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN.		TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	600	-	-		
Callector to amittar valtage	V	V _{GE} = 15 V, I _C = 100 A, T _J = 25 °C	-	1.65	2.10	V	
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 100 A, T _J = 175 °C	-	2.00	-	V	
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 1.0$ mA, $T_J = 25$ °C	4.0	4.4	6.5		
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA	
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA	



SWITCHING CHARACTERISTICS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	106	-	ns - mJ
Rise time	t _r	1	-	49	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 300 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 2.2 \Omega, V_{GE} = \pm 15 \text{ V}, T_{J} = 25 ^{\circ}\text{C}$	-	102	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	85	-	
Turn-on switching loss	E _{on}	1	-	0.46	-	
Turn-off switching loss	E _{off}	1	-	0.95	-	
Turn-on delay time	t _{d(on)}		-	112	-	- ns
Rise time	t _r	$V_{CC} = 300 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 2.2 \Omega, \\ V_{GE} = \pm 15 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	62	-	
Turn-off delay time	t _{d(off)}		-	126	-	
Fall time	t _f		-	109	-	
Turn-on switching loss	E _{on}		-	0.78	-	mJ
Turn-off switching loss	E _{off}	1	-	1.73	-	1113
Input capacitance	C _{ies}		-	7.71	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, V_{CE} = 30 \text{ V}, f = 1.0 \text{ MHz}$	-	0.53	-	nF
Reverse transfer capacitance	C _{res}	1	-	0.23	-	
SC data	Isc	$t_p \leq 5~\mu s,~V_{GE} = 15~V,~T_J = 125~^{\circ}C,\\ V_{CC} = 360~V,~V_{CEM} \leq 1200~V$	-	900	-	Α
Stray inductance	L _{CE}		-	-	30	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}		-	0.75	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Forward voltage	V _F	I _F = 100 A	$T_J = 25 ^{\circ}C$	-	1.40	1.80	- V
1 orward voitage			T _J = 125 °C	-	1.40	ı	
Poverse receivery charge	0	$I_F = 100 \text{ A}, V_R = 600 \text{ V},$ $R_G = 5.6 \Omega$ $V_{GE} = -15 \text{ V}$	$T_J = 25 ^{\circ}C$	-	5.5	-	
Reverse recovery charge	Q _{rr}		T _J = 125 °C	-	7.3	-	μC
Dook reverse receivers current	I _{rr}		T _J = 25 °C	-	68	-	Α
Peak reverse recovery current			T _J = 125 °C	-	88	-	A
Daylorea racellant anarri	E _{rec}		T _J = 25 °C	-	0.89	-	m l
Reverse recovery energy			T _J = 125 °C	-	1.71	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	9	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction temperature		TJ		-	-	175	°C
Storage temperature range		T _{Stg}		-40	-	125	
Junction to case	IGBT	- R _{thJC} -		-	-	0.36	
Junction to case	Diode			-	-	0.57	K/W
Case to sink (conductive grease	applied)	R _{thCS}		-	0.05	-	
Mounting torque			Power terminal screw: M5	2.5 to 5.0		Nm	
			Mounting screw: M6	3.0 to 5.0			
Weight				-	150	-	g



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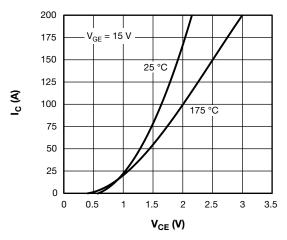


Fig. 1 - IGBT Typical Output Characteristics

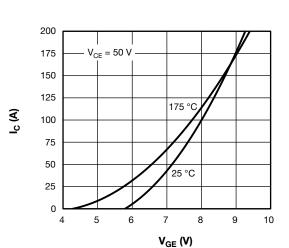


Fig. 2 - IGBT Transfer Characteristics

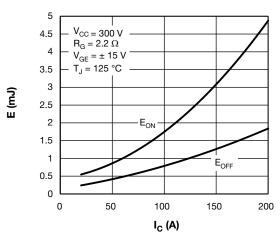


Fig. 3 - IGBT Switching Loss vs. I_C

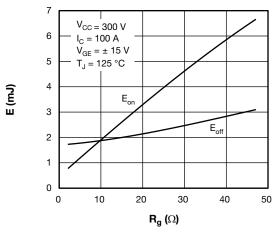


Fig. 4 - IGBT Switching Loss vs. R_G

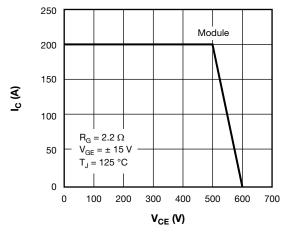


Fig. 5 - RBSOA

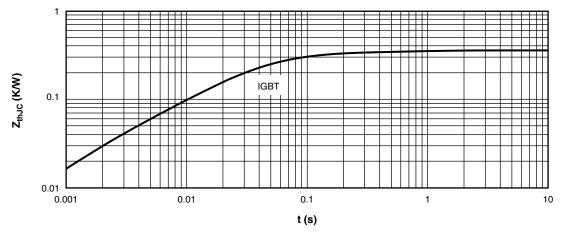
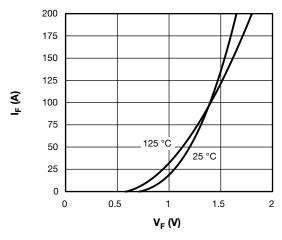


Fig. 6 - IGBT Transient Thermal Impedance





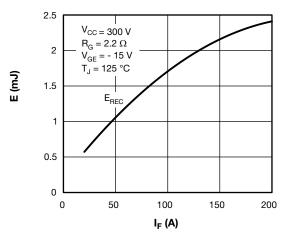


Fig. 8 - Diode Switching Loss vs. I_F

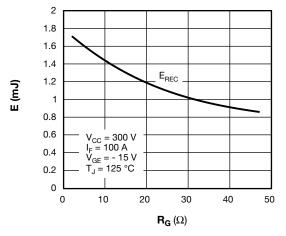


Fig. 9 - Diode Switching Loss vs. $R_{\mbox{\scriptsize G}}$

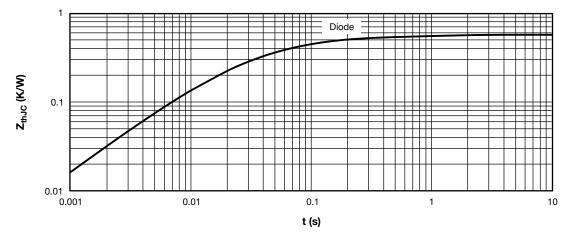
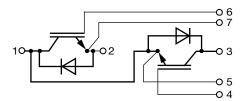


Fig. 10 - Forward Characteristics of Diode

CIRCUIT CONFIGURATION

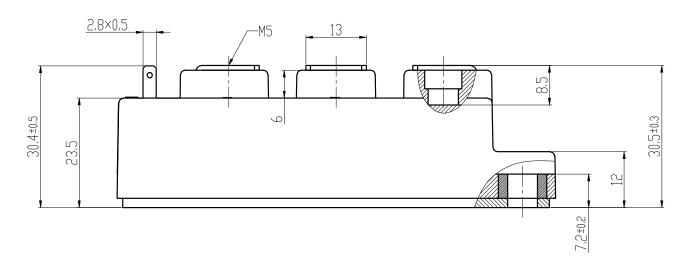


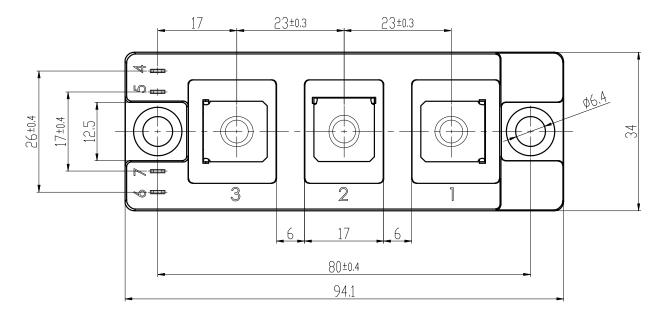
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95524



INT-A-PAK

DIMENSIONS in millimeters (inches)







Vishay

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