

Vishay Siliconix

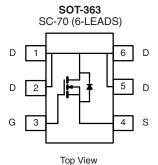
N-Channel 20 V (D-S) MOSFET

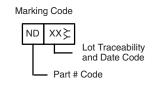
PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 20	0.150 at V _{GS} = 4.5 V	1.7		
	0.235 at V_{GS} = 2.5 V	1.3		

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET: 2.5 V Rated
- Compliant to RoHS Directive 2002/95/EC







Ordering Information: Si1400DL-T1-E3 (Lead (Pb)-free) Si1400DL-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise ı	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	20		V
Gate-Source Voltage		V _{GS}	± 12		v
	T _A = 25 °C	- I _D	1.7	1.6	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		1.2	1.0	
Pulsed Drain Current		I _{DM}	5		A
Continuous Source Current (Diode Conduction) ^a		۱ _S	0.8 0.8		
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	0.625	0.568	w
	T _A = 85 °C		0.40	0.295	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	t ≤ 5 s	- R _{thJA} R _{thJF}	165	200	°C/W	
	Steady State		180	220		
Maximum Junction-to-Foot (Drain)	Steady State		105	130		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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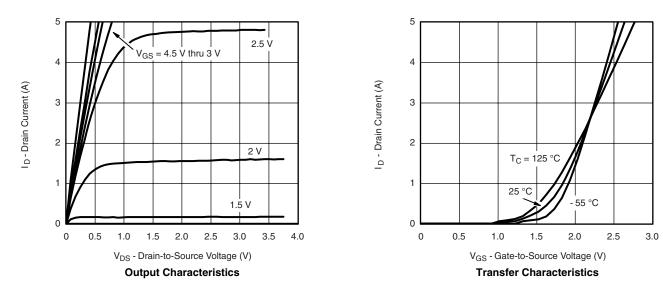
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.6			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
	IDSS	V_{DS} = 16 V, V_{GS} = 0 V, T_{J} = 85 °C			5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, $V_{GS} = 4.5$ V	2			А	
Drain-Source On-State Resistance ^a		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 1.7 \text{ A}$		0.123	0.150	0	
	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 1.3 A		0.195	0.235	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 1.7 A		5		S	
Diode Forward Voltage ^a	V _{SD}	$I_{\rm S} = 0.8 \text{ A}, V_{\rm GS} = 0 \text{ V}$		0.78	1.1	V	
Dynamic ^b	L		1				
Total Gate Charge	Qg			2.1	4.0	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 1.7 A		0.3			
Gate-Drain Charge	Q _{gd}			0.4		1	
Turn-On Delay Time	t _{d(on)}			10	17		
Rise Time	t _r	V_{DD} = 10 V, R_L = 20 Ω		30	50	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$		14	25		
Fall Time	t _f			8	15		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 0.8 A, dI/dt = 100 A/μs		30	50		

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

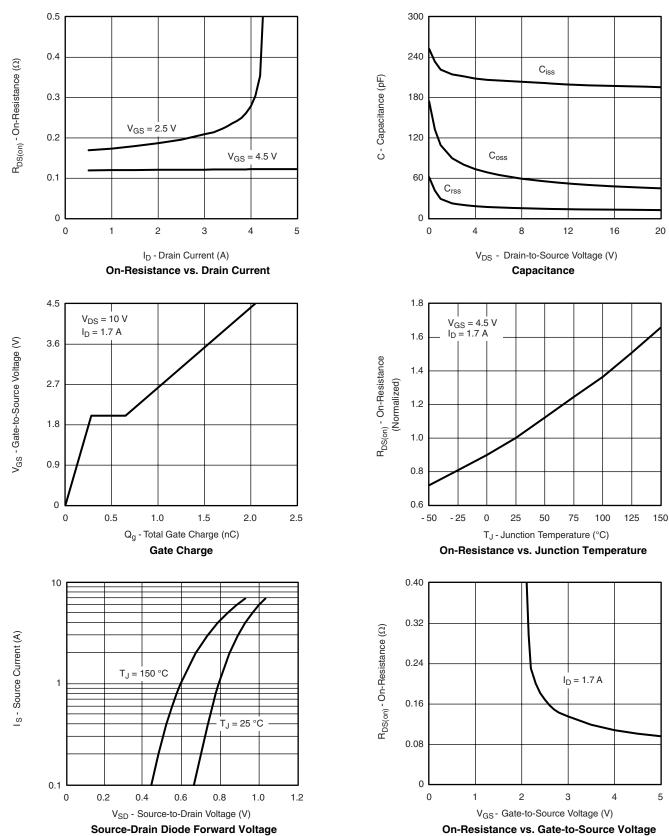
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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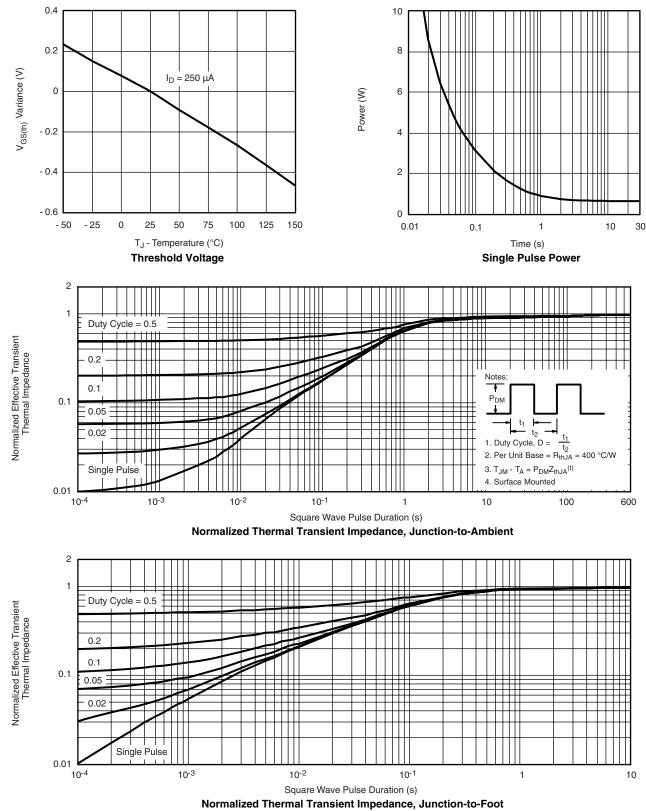
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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?71179</u>.

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