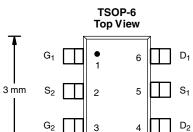


Vishay Siliconix

Dual P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 20	0.185 at V _{GS} = - 4.5 V	- 1.9		
	0.260 at V _{GS} = - 2.5 V	- 1.6		
	0.385 at V _{GS} = - 1.8 V	- 0.7		



- 2.85 mm

Si3981DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

MCxxx

Ordering Information:

Marking Code:

FEATURES

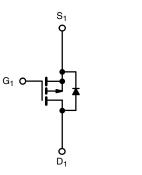
- TrenchFET[®] Power MOSFET
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

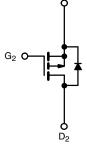


FREE

APPLICATIONS

- Battery Switch for Portable Devices •
- Computers
- Bus Switch
 - Load Switch





P-Channel MOSFET

P-Channel MOSFET

S₂

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)							
Parameter		Symbol	5 s	Steady State	Unit		
Drain-Source Voltage		V _{DS}	- 20		V		
Gate-Source Voltage		V _{GS}	± 8				
	T _A = 25 °C	– I _D	- 1.9	- 1.6			
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 1.5	- 1.3	•		
Pulsed Drain Current		I _{DM}	- 8		A		
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 1	- 0.72			
Maximum Power Dissipation ^a	T _A = 25 °C	– P _D	1.08	0.80	W		
	T _A = 70 °C		0.69	0.51	vv		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 5 s	- R _{thJA} R _{thJF}	97	115	°C/W
Maximum Junction-to-Ambient ^a	Steady State		132	155	
Maximum Junction-to-Foot (Drain)	Steady State		78	95	

Note:

a. Surface mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.40		- 1.1	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA	
		V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 85 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 5			А	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.9 A		0.146	0.185	Ω	
		V _{GS} = - 2.5 V, I _D = - 1.6 A		0.210	0.260		
		V _{GS} = - 1.8 V, I _D = - 0.7 A		0.306	0.385	1	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 1.9 A		4		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 1 A, V _{GS} = 0 V		- 0.84	- 1.1	V	
Dynamic ^b							
Total Gate Charge	Qg			3.2	5		
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 1.9 A		0.42		nC	
Gate-Drain Charge	Q _{gd}			0.84			
Gate Resistance	R _g	f = 1 MHz		6		Ω	
Turn-On Delay Time	t _{d(on)}			30	45		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		50	85		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_g = 6 Ω		45	85	ns	
Fall Time	t _f			21	50	1	
Source-Drain Reverse Recovery Time	ce-Drain Reverse Recovery Time t _{rr} I _r			20	40		

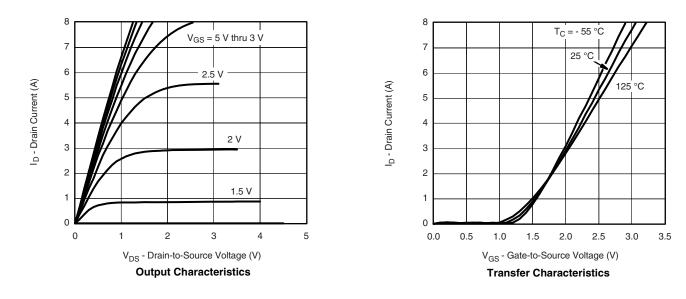
Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



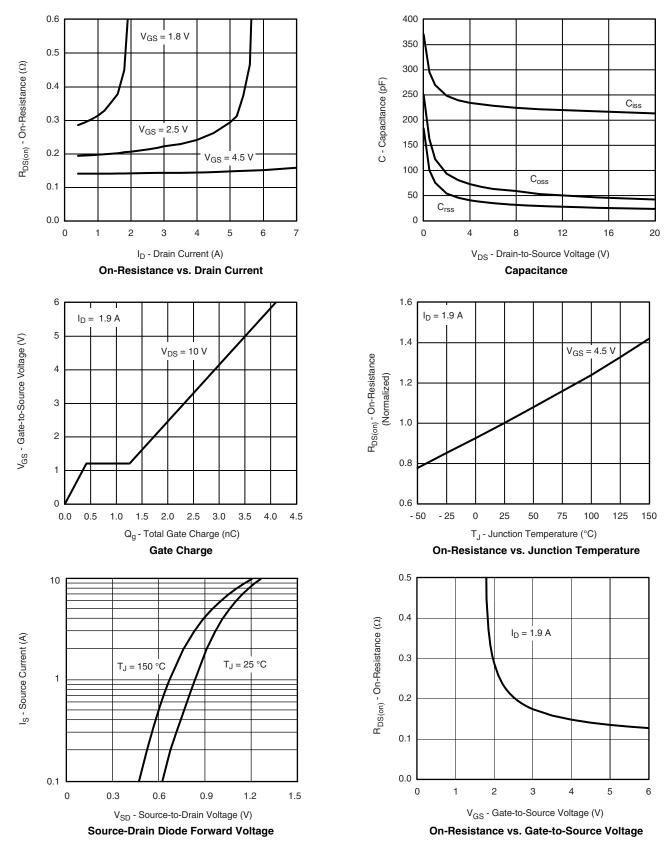
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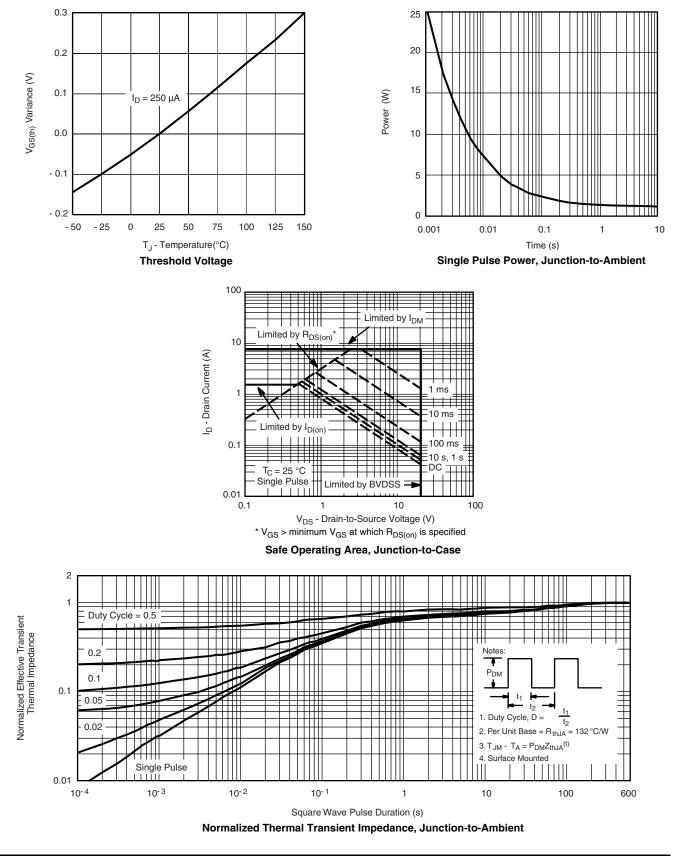
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For technical questions, contact: pmostechsupport@vishay.com

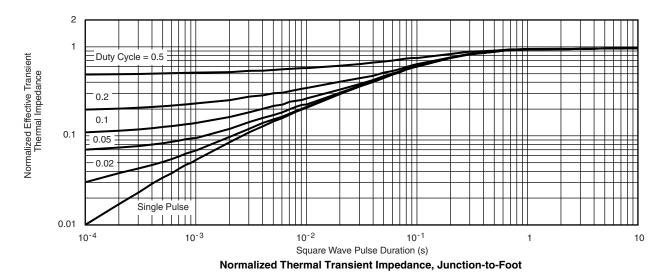
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