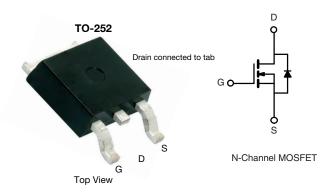


**Vishay Siliconix** 

# Automotive N-Channel 250 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	250				
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0.350				
I <sub>D</sub> (A)	7				
Configuration	Single				
Package	TO-252				



#### **FEATURES**

- TrenchFET<sup>®</sup> power MOSFET
- · Package with low thermal resistance
- AEC-Q101 qualified <sup>d</sup>
- 100 %  $R_g$  and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V <sub>DS</sub>	250	V	
Gate-Source Voltage		V <sub>GS</sub>	± 30		
Continuous Drain Current	T <sub>C</sub> = 25 °C	I	7	А	
Continuous Drain Current	T <sub>C</sub> = 125 °C	I <sub>D</sub>	4		
Continuous Source Current (Diode Conduct	ion) <sup>a</sup>	I <sub>S</sub>	50		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	15		
Single Pulse Avalanche Current	urrent L = 0.1 mH		7		
Single Pulse Avalanche Energy	L = 0.1 IIIH	E <sub>AS</sub>	2.4	mJ	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	D	71	W	
	T <sub>C</sub> = 125 °C	PD	23		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	50	°C/W		
Junction-to-Case (Drain)		R <sub>thJC</sub>	2.1	0/10		

Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- c. When mounted on 1" square PCB (FR4 material).
- d. Parametric verification ongoing.



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	1						1
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$		250	-	-	v
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		3.0	3.5	v
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	$V_{DS} = 0 V, V_{GS} = \pm 30 V$		-	± 100	nA
		$V_{GS} = 0 V$	V <sub>DS</sub> = 250 V	-	-	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	$V_{DS} = 250 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	50	μA
		$V_{GS} = 0 V$	$V_{DS} = 250 \text{ V}, \text{ T}_{\text{J}} = 175 ^{\circ}\text{C}$	-	-	250	-
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	10	-	-	А
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 10 A	-	0.290	0.350	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 10 A, T <sub>J</sub> = 125 °C	-	-	0.858	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C	-	-	1.250	
Forward Transconductance b	9 <sub>fs</sub>	V <sub>DS</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A		20	-	S
Dynamic <sup>b</sup>					•	•	
Input Capacitance	C <sub>iss</sub>			-	964	1205	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 25 V, f = 1 MHz	-	88	110	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	1		-	32	40	
Total Gate Charge <sup>c</sup>	Qg			-	19	29	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{GS} = 10 V$	$V_{DS} = 125 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	6.5	-	nC
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>	1		-	5	-	
Gate Resistance	Rg	f = 1 MHz		0.35	1	3	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>				9	14	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{\text{DD}} = 125 \text{ V}, \text{ R}_{\text{L}} = 12.5 \Omega$ $\text{I}_{\text{D}} \cong 10 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		-	8	12	- ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	15	23	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	4	6	
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>						
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	15	А
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V			0.9	1.5	v

Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

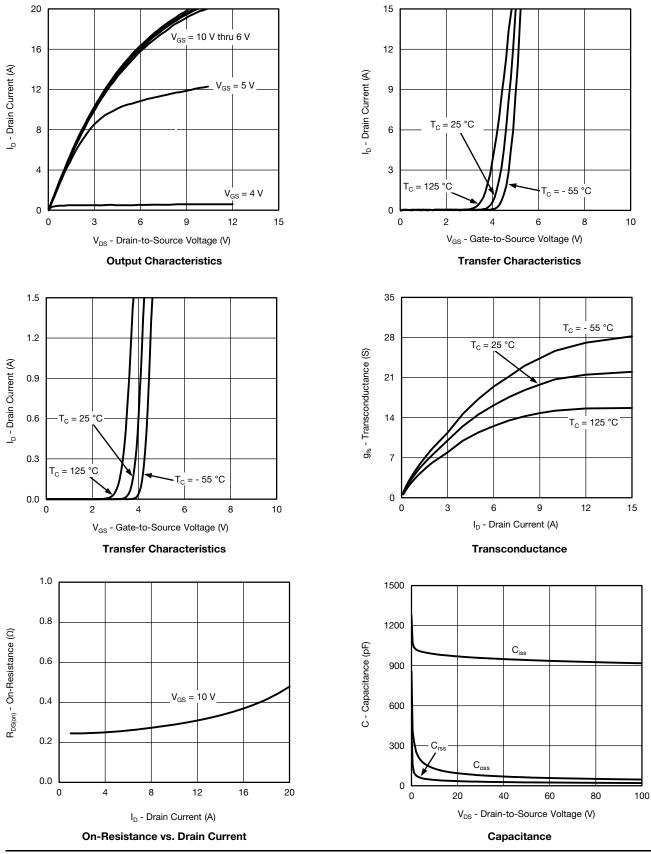
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2



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## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



S15-1874-Rev. C, 10-Aug-15

3

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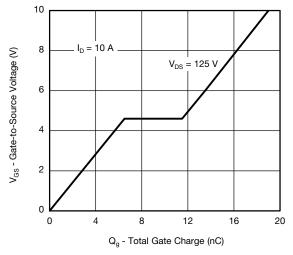
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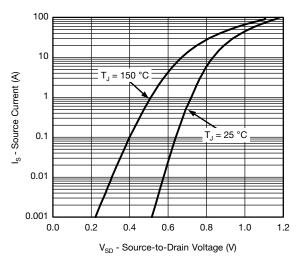
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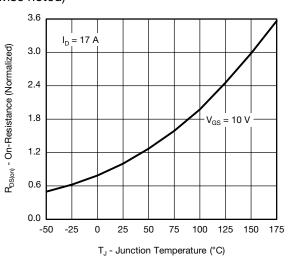
## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



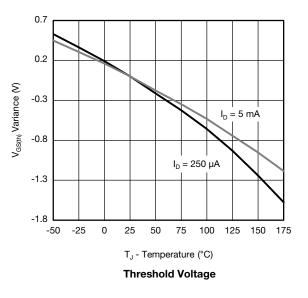


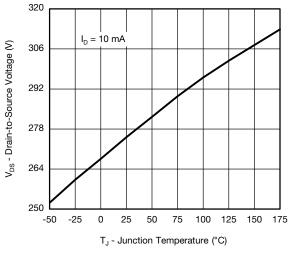


Source Drain Diode Forward Voltage



**On-Resistance vs. Junction Temperature** 





Drain Source Breakdown vs. Junction Temperature 4

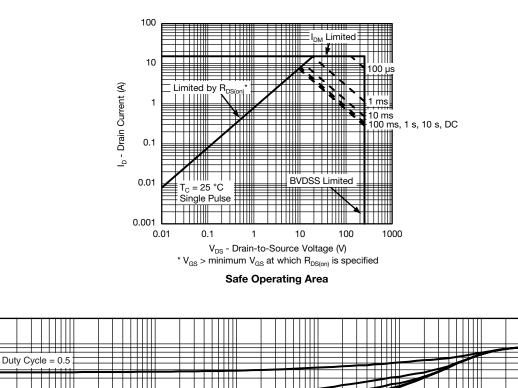
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### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

Square Wave Pulse Duration (s)

 $\square$ 

10

100

1

ТП

10-1

10-2

2

0.1

0.01

0.2

0.05

0.02

Single Pulse

10<sup>-3</sup>

Normalized Effective Transient Thermal Impedance

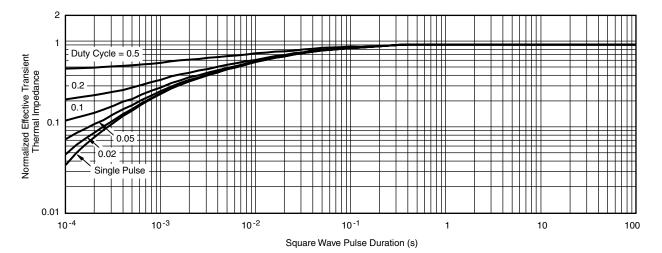
1000



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## THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



#### Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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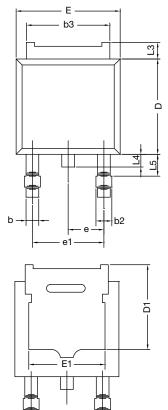
REVISION	HISTORY <sup>a</sup>	
REVISION	DATE	DESCRIPTION OF CHANGE
С	04-Aug-15	Revised R <sub>g</sub> minimum limit

Note

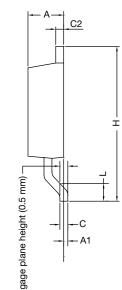
a. As of April 2014



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# **TO-252AA Case Outline**



	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	4.10	-	0.161	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56	BSC	0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.01	1.52	0.040	0.060	
	ECN: T13-0592-Rev. A, 02-Sep-13 DWG: 6019				

Note

• Dimension L3 is for reference only.

Ф **Т** 



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### **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)

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