Ultra Low V<sub>F</sub> = 0.53 V at I<sub>F</sub> = 2.5 A

Vishay General Semiconductor



- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available: - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

#### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

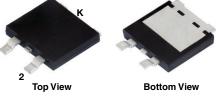
Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 gualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test Polarity: as marked

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V10D120C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	120	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	10	A	
	per diode		5		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	100	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	



eSMP<sup>®</sup> Series

SMPD (TO-263AC)

#### V10D120C

Anode 1 C Cathode

### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 5.0 A			
V <sub>RRM</sub>	120 V			
I <sub>FSM</sub>	100 A			
$V_F$ at $I_F$ = 5.0 A ( $T_A$ = 125 °C)	0.64 V			
T <sub>J</sub> max.	150 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			





RoHS

COMPLIANT

HALOGEN FREE







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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.61	-	v	
	I <sub>F</sub> = 5 A			0.79	0.94		
	I <sub>F</sub> = 2.5 A	- T <sub>A</sub> = 125 °C		0.53	-		
	I <sub>F</sub> = 5 A			0.64	0.72		
Reverse current at rated V <sub>R</sub> per diode	V <sub>R</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	2.3	-	μA	
		T <sub>A</sub> = 125 °C		2.3	-	mA	
	V 100 V	T <sub>A</sub> = 25 °C		-	500	μA	
	V <sub>R</sub> = 120 V	T <sub>A</sub> = 125 °C		5	15	mA	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V10D120C	UNIT	
Typical thermal resistance	per diode	R <sub>θJC</sub>	3.5		
	per device		2.5	°C/W	
	per device		48		

#### Notes

 $^{(1)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
SMPD (TO-263AC)	V10D120C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel
SMPD (TO-263AC)	V10D120CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel

Note

(1) AEC-Q101 qualified



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## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

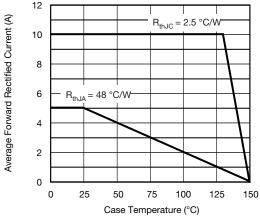


Fig. 1 - Forward Current Derating Curve

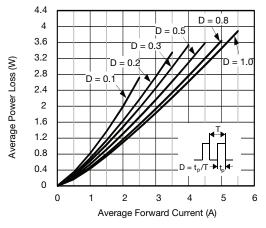


Fig. 2 - Forward Power Loss Characteristics Per Diode

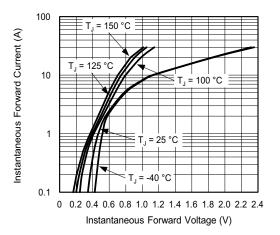


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

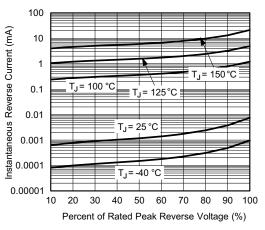


Fig. 4 - Typical Reverse Characteristics Per Diode

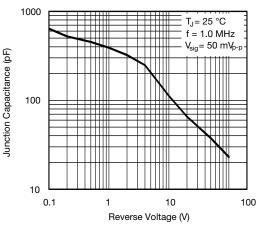


Fig. 5 - Typical Junction Capacitance Per Diode

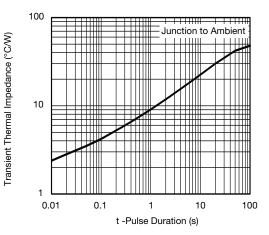


Fig. 6 - Typical Transient Thermal Impedance Per Device

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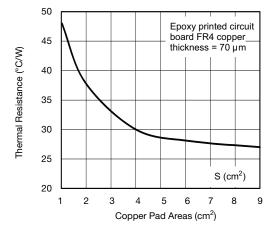
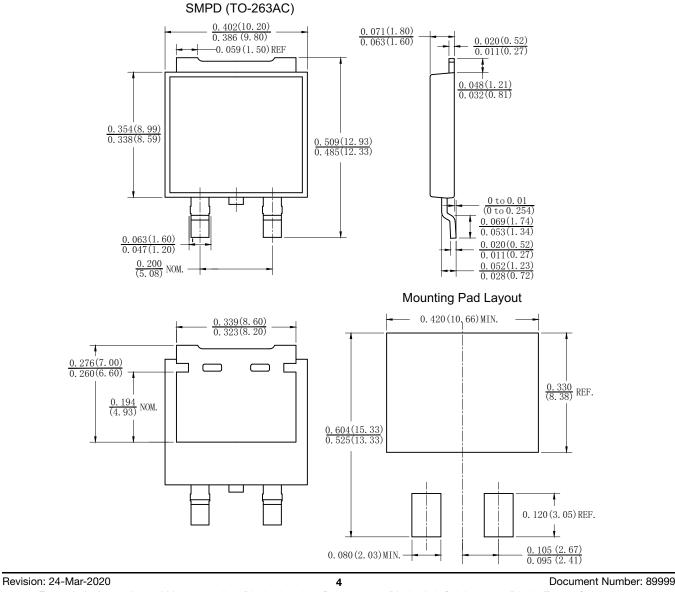


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas



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