# V40PW15C

Vishay General Semiconductor

# High Current Density Surface-Mount TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.60$  V at  $I_F = 5$  A



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SlimDPAK (TO-252AE)

## **DESIGN SUPPORT TOOLS AVAILABLE**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	40 A			
V <sub>RRM</sub>	150 V			
I <sub>FSM</sub>	240 A			
$V_F$ at $I_F$ = 20 A ( $T_A$ = 125 °C)	0.76 V			
T <sub>J</sub> max.	150 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration	Common cathode			

## FEATURES

- Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

## **MECHANICAL DATA**

**Case:** SlimDPAK (TO-252AE) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V40PW15C	UNIT
Device marking code			V40PW15C	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	150	V
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	40	А
	per diode	I <sub>F(AV)</sub> <sup>(1)</sup>	20	A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	240	A
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +150	°C
Storage temperature range		T <sub>STG</sub>	-55 to +150	°C

#### Notes

<sup>(1)</sup> With infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction to ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0JA</sub>

Revision: 11-Feb-2019

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Document Number: 87654

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COMPLIANT

HALOGEN

FREE

V40PW15C



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Maximum instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.77	-	V
	I <sub>F</sub> = 10 A			0.97	-	
	I <sub>F</sub> = 20 A			1.36	1.45	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.6	-	
	I <sub>F</sub> = 10 A			0.69	-	
	I <sub>F</sub> = 20 A			0.76	0.84	
Reverse current	$V_{\rm R} = 100 \text{ V}$	$T_A = 25 \ ^\circ C$	I <sub>R</sub> (2)	0.01	-	- mA
		T <sub>A</sub> = 125 °C		3	-	
	V <sub>R</sub> = 150 V	$T_A = 25 \ ^\circ C$		-	0.5	
		T <sub>A</sub> = 125 °C		6	20	
Typical junction capacitance	4.0 V, 1 MHz		CJ	870	_	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V40PW15C	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	55	°C/W	
	R <sub>0JM</sub> <sup>(3)</sup>	1.5		

#### Notes

 $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub> $\theta$ JA</sub>

 $^{(2)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

 $^{(3)}$  Mounted on infinite heat sink; thermal resistance  $R_{\theta JM}$  - junction-to-mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE		
V40PW15C-M3/I	0.20	I	4500	13" diameter plastic tape and reel		
V40PW15CHM3/I <sup>(1)</sup>	0.20	l	4500	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

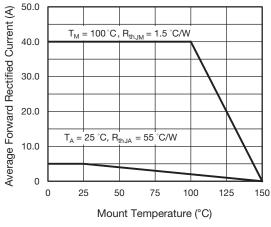


Fig. 1 - Maximum Forward Current Derating Curve

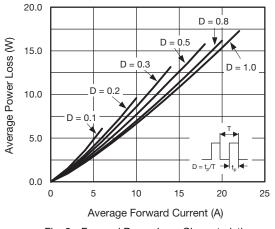
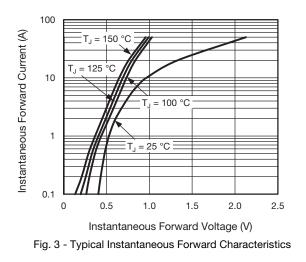
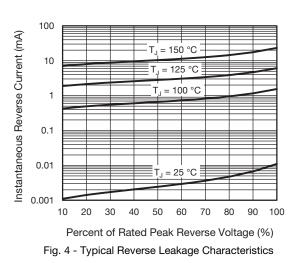
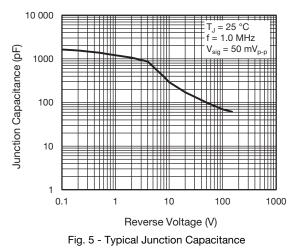


Fig. 2 - Forward Power Loss Characteristics







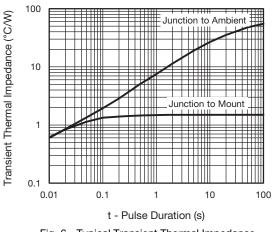


Fig. 6 - Typical Transient Thermal Impedance

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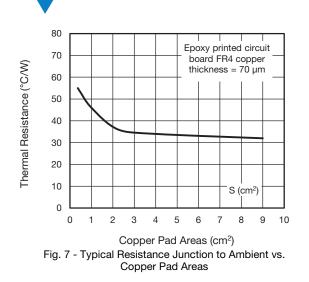
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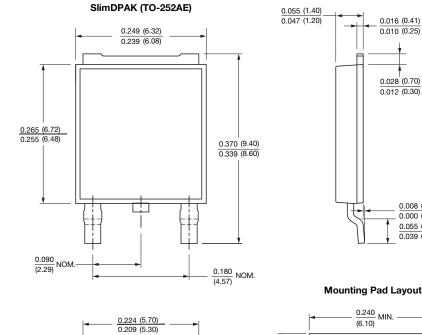


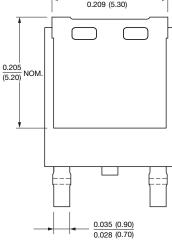


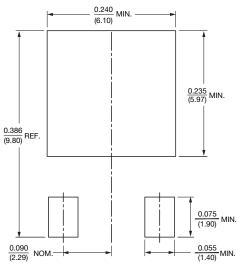
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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)







0.008 (0.20) 0.000 (0.00) 0.055 (1.40) 0.039 (1.00)

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