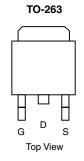


Vishay Siliconix

# N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ)	
60	0.0044 at V <sub>GS</sub> = 10 V	90 <sup>d</sup>	105	



Ordering Information: SUM90N06-4m4P-E3 (Lead (Pb)-free)

### **FEATURES**

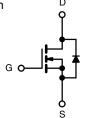
- TrenchFET® Power MOSFET
- 175 °C Junction Temperatur



- 100 % R<sub>q</sub> and UIS Tested
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- Power Supply
  - Secondary Synchronous Rectification
- Industrial
- OR-ing



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V <sub>DS</sub>	60	V			
Gate-Source Voltage	V <sub>GS</sub>	± 20				
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I_	90 <sup>d</sup>	A		
Continuous Diam Current (1) = 173 O)	T <sub>C</sub> = 70 °C	- I <sub>D</sub>	90 <sup>d</sup>			
Pulsed Drain Current	I <sub>DM</sub>	240	^			
Avalanche Current		I <sub>AS</sub>	70			
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	245	mJ		
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	В	300 <sup>b</sup>	w		
	T <sub>A</sub> = 25 °C <sup>c</sup>	- P <sub>D</sub>	3.75			
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.5		

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Package limited.

## SUM90N06-4m4P

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<b>SPECIFICATIONS</b> $(T_C = 25)$	°C, unless	otherwise noted)					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	60			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
	I <sub>DSS</sub>	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$			50		
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	70			Α	
Drain-Source On-State Resistance <sup>a</sup>	B	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0036	0.0044	Ω	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.0059	0.0077		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		60		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			6190		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 30 V, f = 1 MHz		990			
Reverse Transfer Capacitance	C <sub>rss</sub>			340			
Total Gate Charge <sup>c</sup>	Qg			105	160	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 85 \text{ A}$		29			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			28			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.4	2.8	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			23	35		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, R_L = 0.4 \Omega$ $I_D \cong 85 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		15	25	- ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			36	55		
Fall Time <sup>c</sup>	t <sub>f</sub>			8	15		
Source-Drain Diode Ratings and Ch	aracteristics (	T <sub>C</sub> = 25 °C) <sup>b</sup>					
Continuous Current	I <sub>S</sub>				85		
Pulsed Current	I <sub>SM</sub>				240	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V		0.84	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			61	100	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 75 A, di/dt = 100 A/μs		3.0	4.5	Α	
Reverse Recovery Charge	Q <sub>rr</sub>			91	140	μC	

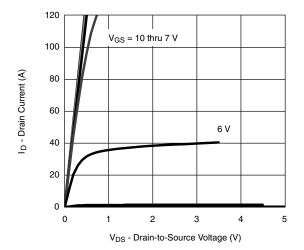
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

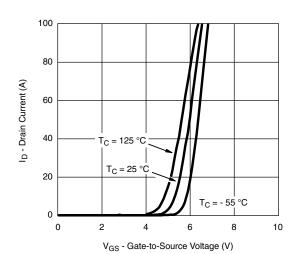
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



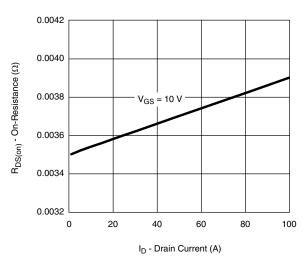
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



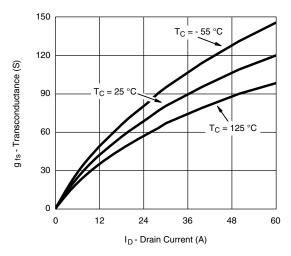
### **Output Characteristics**



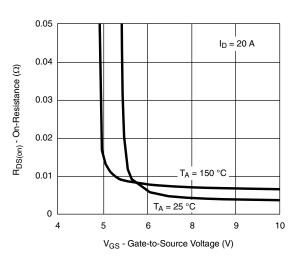
### **Transfer Characteristics**



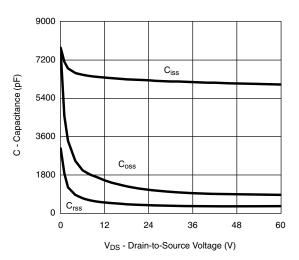
On-Resistance vs. Drain Current



#### Transconductance



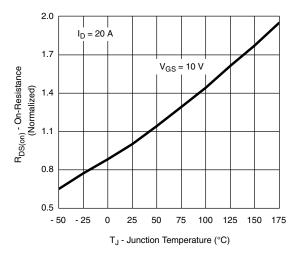
#### On-Resistance vs. Gate-to-Source Voltage



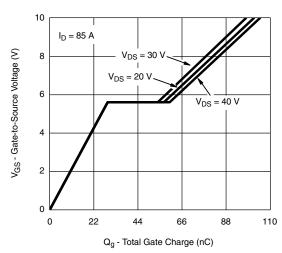
Capacitance

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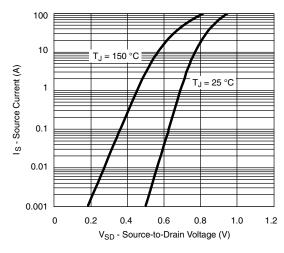
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



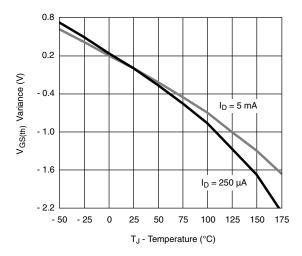
On-Resistance vs. Junction Temperature



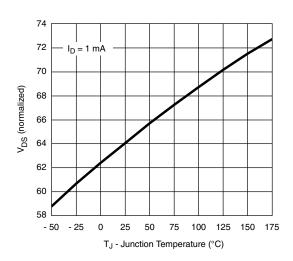
**Gate Charge** 



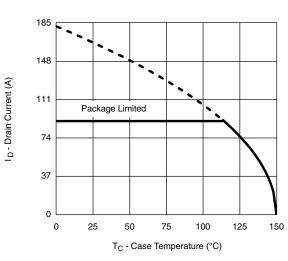
Source-Drain Diode Forward Voltage



**Threshold Voltage** 



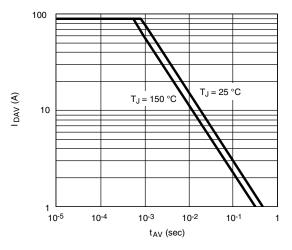
On-Resistance vs. Junction Temperature

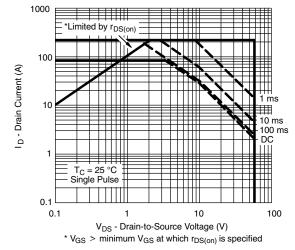


Maximum Drain Current vs. Case Temperature

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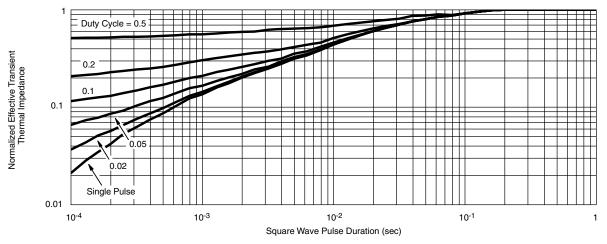
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Single Pulse Avalanche Current Capability vs. Time





Normalized Thermal Transient Impedance, Junction-to-Case

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