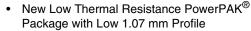


N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)	
30	0.003 at V _{GS} = 10 V	40	46 nC	
	0.004 at V _{GS} = 4.5 V	40	40 NC	

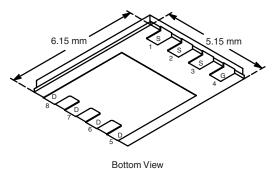
FEATURES

- · Halogen-free available
- Ultra-Low On-Resistance Using High Density TrenchFET Gen II



100 % R_g Tested





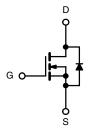
PowerPAK SO-8

Ordering Information: Si7356ADP-T1-E3 (Lead (Pb)-free)

Si7356ADP-T1-GE3 (Lead (Pb)-free and Halogen-free)

APPLICATIONS

- · Low-Side DC/DC Conversion
 - Notebook
 - Server
 - Workstation
- Point-of-Load Conversion



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise no	ted		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	V		
Gate-Source Voltage	V_{GS}	± 20	7 v		
	T _C = 25 °C		40		
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C	I _D	32		
Continuous Diam Current (1) = 130 0)	T _A = 25 °C	'D	31 ^{b, c}		
	T _A = 70 °C		25 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	70		
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	40		
Continuous Source-Drain Diode Current	T _A = 25 °C	'5	4.9 ^{b, c}		
	T _C = 25 °C		83		
Maximum Power Dissipation	T _C = 70 °C	P _D	53	W	
Waximum Fower Dissipation	T _A = 25 °C	υ υ	5.4 ^{b, c}	T **	
	T _A = 70 °C		3.4 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature		260			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, f}	t ≤ 10 s	R_{thJA}	18	23	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	1.0	1.5]	

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. See Solder Profile (http://www.vishay.com/ppg?73257). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 65 °C/W.

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SPECIFICATIONS T _J = 25 °C	, unless otl	herwise noted					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•					•	
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		31		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1 _D = 230 μΑ		- 5.7			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0		3.0	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	1	V _{DS} = 30 V, V _{GS} = 0 V			1		
	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-State Resistance ^a	В	V _{GS} = 10 V, I _D = 20 A		0.0024	0.003	Ω	
	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 20 A		0.0032	0.004		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		100		S	
Dynamic ^b	ľ				I.	•	
Input Capacitance	C _{iss}			6215		pF	
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		825			
Reverse Transfer Capacitance	C _{rss}			460			
Total Cata Chausa	Qg	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 15 A		96	145		
Total Gate Charge				46	70	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 15 \text{ A}$		20			
Gate-Drain Charge	Q_{gd}			15			
Gate Resistance	R_{g}	f = 1 MHz	0.5	1.0	1.5	Ω	
Turn-On Delay Time	t _{d(on)}			25	40		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		18	30	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A, V}_{GEN} = 10 \text{ V, R}_g = 6 \Omega$		95	150		
Fall Time	t _f			31	50		
Drain-Source Body Diode Characteris	tics		l	1	I.		
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			40		
Pulse Diode Forward Current ^a	I _{SM}				70	Α	
Body Diode Voltage	V_{SD}	I _S = 5 A		0.73	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			40	70	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	Q _{rr}		40	70	nC	
Reverse Recovery Fall Time	t _a	$I_F = 3 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25 ^{\circ}\text{C}$		21		ns	
Reverse Recovery Rise Time	t _b			19			

Notes:

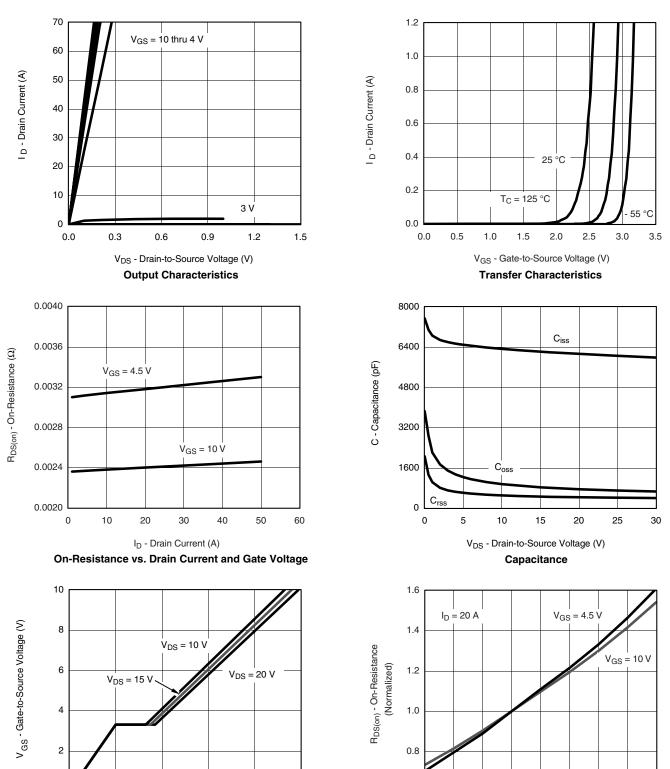
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



0

0

20

40

60

Q_q - Total Gate Charge (nC)

Gate Charge

80

125

150

100

0.6

- 50

- 25

0

25

50

T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature

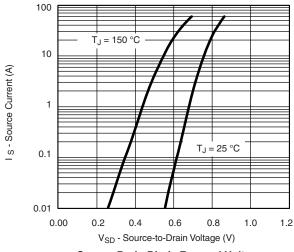
75

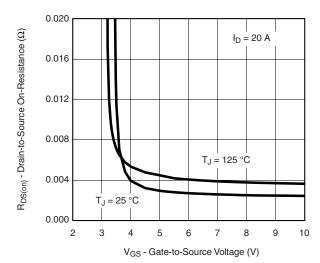
100

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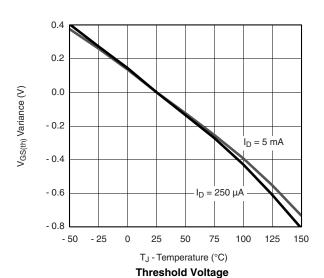
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

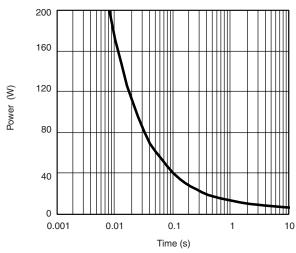




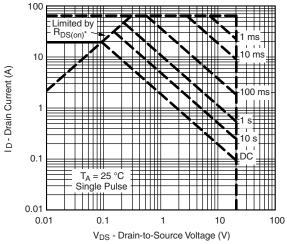
Source-Drain Diode Forward Voltage







Single Pulse Power, Junction-to-Ambient

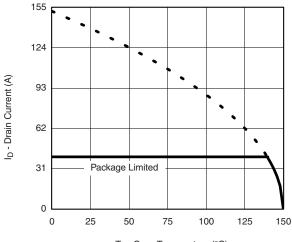


* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

Safe Operating Area, Junction-to-Ambient

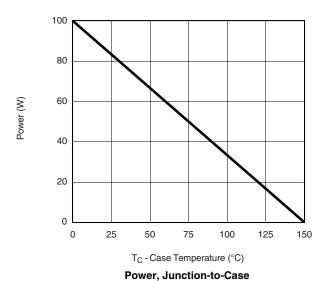


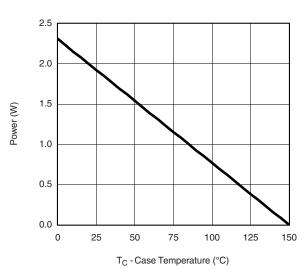
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



T_C - Case Temperature (°C)

Current Derating*





Power, Junction-to-Ambient

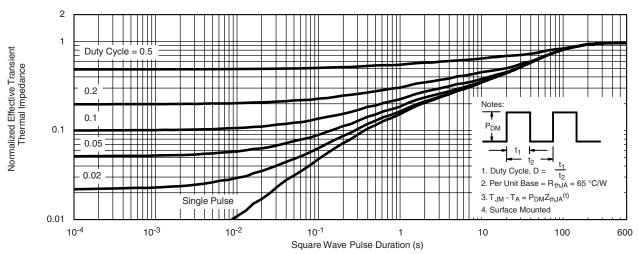
Document Number: 73425 www.vishay.com S-80439-Rev. B, 03-Mar-08 5

^{*} The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

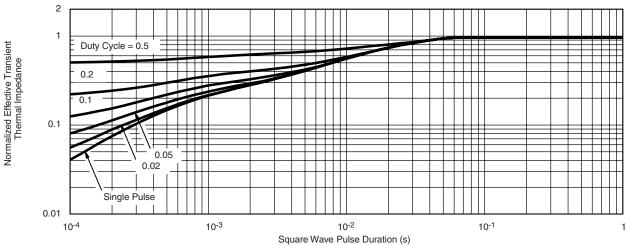
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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