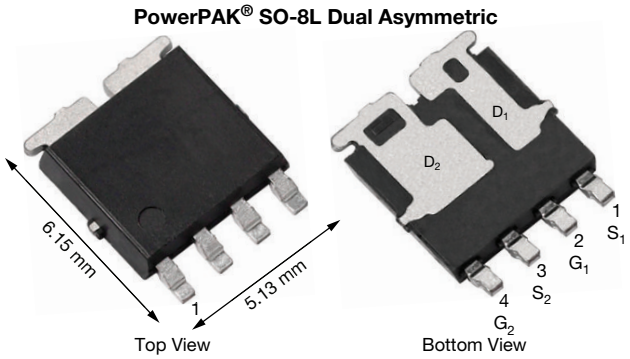
Automotive Dual N-Channel 100 V (D-S) 175 °C MOSFETs

PowerPAK® SO-8L Dual Asymmetric



FEATURESS

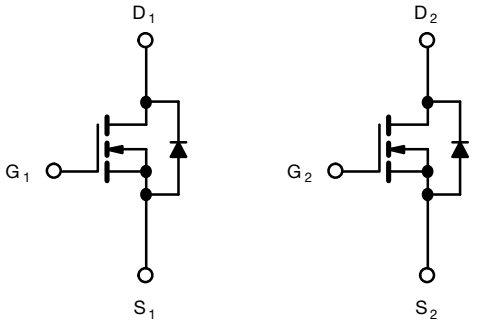
- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Optimized for synchronous buck applications
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE

Pb-free

RoHS COMPLIANT

HALOGEN FREE



PRODUCT SUMMARY		
	N-CHANNEL 1	N-CHANNEL 2
V_{DS} (V)	100	100
$R_{DS(on)}$ (Ω) at V_{GS} = 10 V	0.0400	0.0190
$R_{DS(on)}$ (Ω) at V_{GS} = 4.5 V	0.0505	0.0235
I_D (A)	17	34
Configuration	Dual N	
Package	PowerPAK SO-8L Dual Asymmetric	

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	N-CHANNEL 1	N-CHANNEL 2	UNIT
Drain-Source Voltage		V_{DS}	100	100	V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current	T_C = 25 °C	I_D	17	34	A
	T_C = 125 °C		10	19	
Continuous Source Current (Diode conduction)		I_S	20 [a]	44	
Pulsed Drain Current [b]		I_{DM}	40	80	
Single Pulse Avalanche Current	L = 0.1 mH	I_{AS}	17	28	
Single Pulse Avalanche Energy		E_{AS}	14.4	39.2	mJ
Maximum Power Dissipation [b]	T_C = 25 °C	P_D	27	48	W
	T_C = 125 °C		9	16	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to +175		°C
Soldering Recommendations (Peak temperature) [d, e]			260		

THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	N-CHANNEL 1	N-CHANNEL 2	UNIT
Junction-to-Ambient	PCB mount [c]	R_{thJA}	85	85	°C/W
Junction-to-Case (Drain)		R_{thJC}	5.5	3.1	

Notes

a. Package limited.
b. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
c. When mounted on 1" square PCB (FR4 material).
d. See solder profile (www.vishay.com/doc?73257). The PowerPAK SO-8L is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)								
PARAMETER	**SYMBOL**	**TEST CONDITIONS**			**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static								
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA		N-Ch 1	100	-	-	V
		V_{GS} = 0 V, I_D = 250 µA		N-Ch 2	100	-	-	
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA		N-Ch 1	1.5	2.0	2.5	
		V_{DS} = V_{GS}, I_D = 250 µA		N-Ch 2	1.5	2.0	2.5	
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V		N-Ch 1	-	-	± 100	nA
				N-Ch 2	-	-	± 100	
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS} = 0 V	V_{DS} = 100 V	N-Ch 1	-	-	1	µA
		V_{GS} = 0 V	V_{DS} = 100 V	N-Ch 2	-	-	1	
		V_{GS} = 0 V	V_{DS} = 100 V, T_J = 125 °C	N-Ch 1	-	-	50	
		V_{GS} = 0 V	V_{DS} = 100 V, T_J = 125 °C	N-Ch 2	-	-	50	
		V_{GS} = 0 V	V_{DS} = 100 V, T_J = 175 °C	N-Ch 1	-	-	250	
		V_{GS} = 0 V	V_{DS} = 100 V, T_J = 175 °C	N-Ch 2	-	-	250	
On-State Drain Current [a]	$I_{D(on)}$	V_{GS} = 10 V	V_{DS} ≥ 5 V	N-Ch 1	10	-	-	A
		V_{GS} = 10 V	V_{DS} ≥ 5 V	N-Ch 2	20	-	-	
Drain-Source On-State Resistance [a]	$R_{DS(on)}$	V_{GS} = 10 V	I_D = 6 A	N-Ch 1	-	0.0325	0.0400	Ω
		V_{GS} = 10 V	I_D = 10 A	N-Ch 2	-	0.0154	0.0190	
		V_{GS} = 10 V	I_D = 6 A, T_J = 125 °C	N-Ch 1	-	-	0.0694	
		V_{GS} = 10 V	I_D = 10 A, T_J = 125 °C	N-Ch 2	-	-	0.0326	
		V_{GS} = 10 V	I_D = 6 A, T_J = 175 °C	N-Ch 1	-	-	0.0877	
		V_{GS} = 10 V	I_D = 10 A, T_J = 175 °C	N-Ch 2	-	-	0.0412	
		V_{GS} = 4.5 V	I_D = 4 A	N-Ch 1	-	0.0412	0.0505	
		V_{GS} = 4.5 V	I_D = 8 A	N-Ch 2	-	0.0191	0.0235	
Forward Transconductance [b]	g_{fs}	V_{DS} = 10 V, I_D = 6 A		N-Ch 1	-	17	-	S
		V_{DS} = 10 V, I_D = 10 A		N-Ch 2	-	34	-	
Dynamic [b]								
Input Capacitance	C_{iss}	V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch 1	-	475	650	pF
		V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch 2	-	1065	1390	
Output Capacitance	C_{oss}	V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch 1	-	280	375	
		V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch 2	-	560	750	
Reverse Transfer Capacitance	C_{rss}	V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch 1	-	18	25	
		V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	N-Ch 2	-	37	50	
Total Gate Charge [c]	Q_g	V_{GS} = 10 V	V_{DS} = 50 V, I_D = 1 A	N-Ch 1	-	10	15	nC
		V_{GS} = 10 V	V_{DS} = 50 V, I_D = 1 A	N-Ch 2	-	20	30	
Gate-Source Charge [c]	Q_{gs}	V_{GS} = 10 V	V_{DS} = 50 V, I_D = 1 A	N-Ch 1	-	2	-	
		V_{GS} = 10 V	V_{DS} = 50 V, I_D = 1 A	N-Ch 2	-	3	-	
Gate-Drain Charge [c]	Q_{gd}	V_{GS} = 10 V	V_{DS} = 50 V, I_D = 1 A	N-Ch 1	-	3	-	
		V_{GS} = 10 V	V_{DS} = 50 V, I_D = 60 A	N-Ch 2	-	5	-	
Gate Resistance	R_g	f = 1 MHz		N-Ch 1	1.2	2.5	3.8	Ω
				N-Ch 2	0.6	1.4	2.2	

SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNIT**
Dynamic [b]							
Turn-On Delay Time [c]	$t_{d(on)}$	V_{DD} = 50 V, R_L = 5 Ω, $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch 1	-	8	15	ns
		V_{DD} = 50 V, R_L = 5 Ω, $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch 2	-	12	20	
Rise Time [c]	t_r	V_{DD} = 50 V, R_L = 5 Ω, $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch 1	-	3	5	
		V_{DD} = 50 V, R_L = 5 Ω, $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch 2	-	3	5	
Turn-Off Delay Time [c]	$t_{d(off)}$	V_{DD} = 50 V, R_L = 5 Ω, $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch 1	-	22	35	
		V_{DD} = 50 V, R_L = 5 Ω, $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch 2	-	28	45	
Fall Time [c]	t_f	V_{DD} = 50 V, R_L = 5 Ω, $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch 1	-	21	35	
		V_{DD} = 50 V, R_L = 5 Ω, $I_D \cong$ 1 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch 2	-	22	35	
Source-Drain Diode Ratings and Characteristics [b]							
Pulsed Current [a]	I_{SM}		N-Ch 1	-	-	40	A
			N-Ch 2	-	-	80	
Forward Voltage	V_{SD}	I_F = 6 A, V_{GS} = 0 V	N-Ch 1	-	0.87	1.2	V
		I_F = 10 A, V_{GS} = 0 V	N-Ch 2	-	0.84	1.2	

Notes

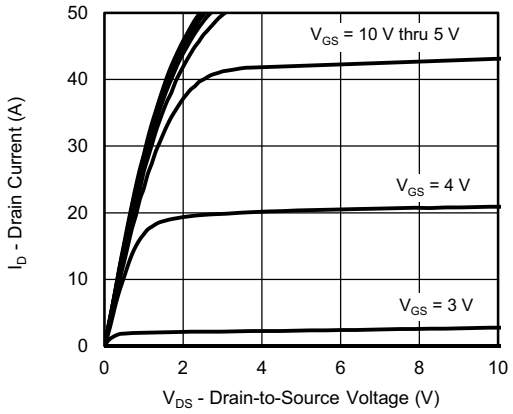
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.

b. Guaranteed by design, not subject to production testing.

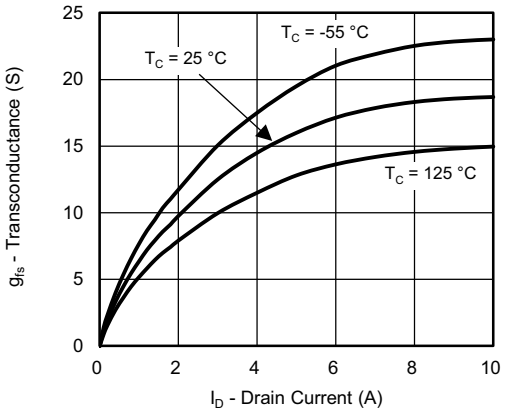
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

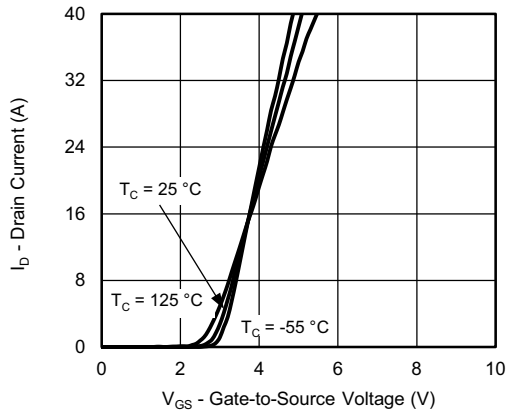
N-CHANNEL 1 TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



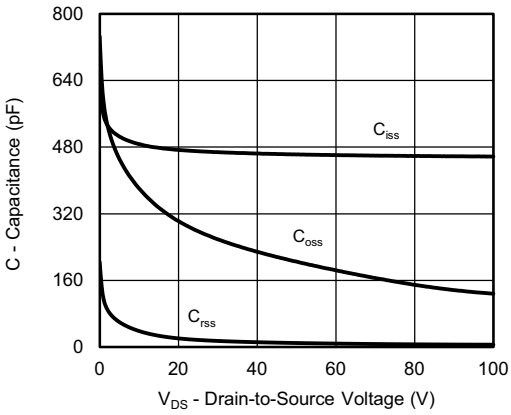
Output Characteristics



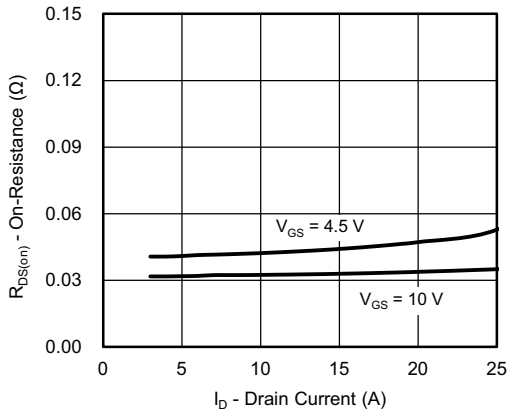
Transconductance



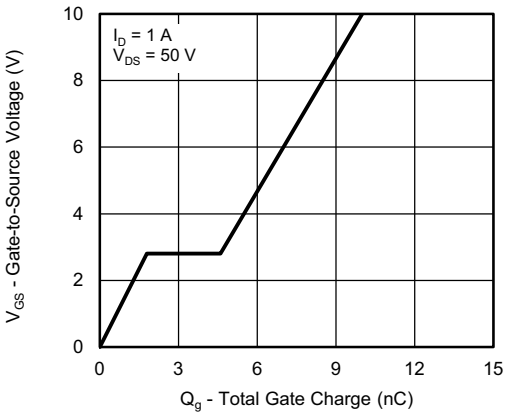
Transfer Characteristics



Capacitance

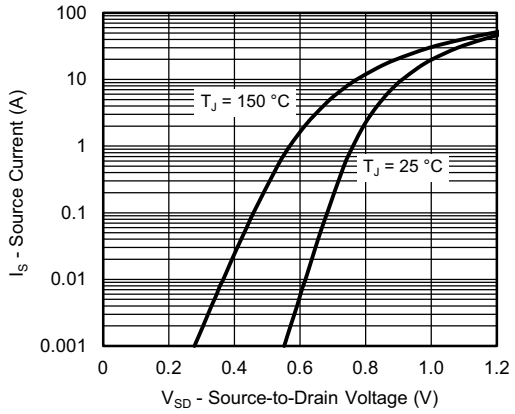


On-Resistance vs. Drain Current

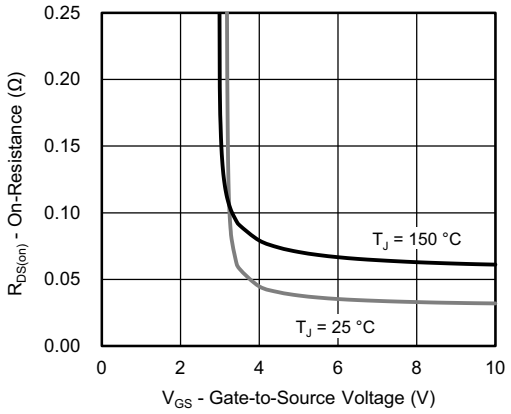


Gate Charge

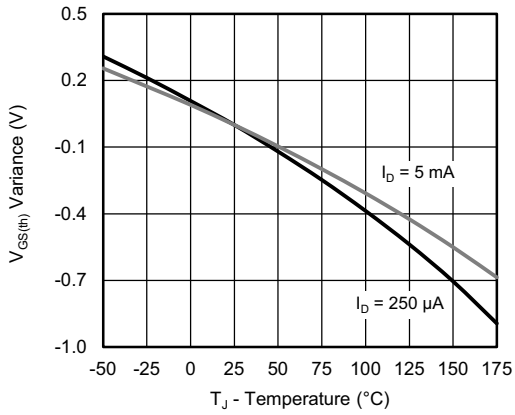
N-CHANNEL 1 TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



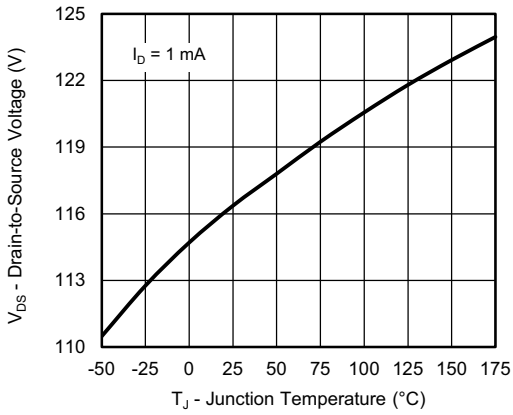
Source Drain Diode Forward Voltage



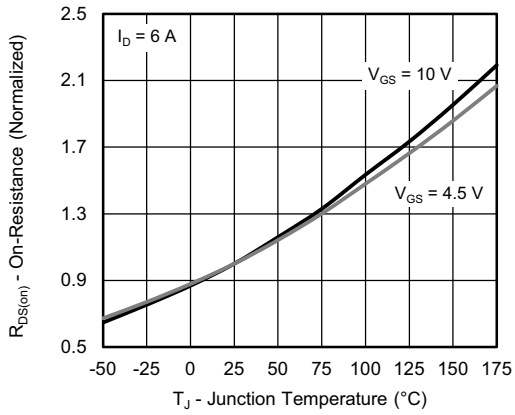
On-Resistance vs. Gate-to-Source Voltage



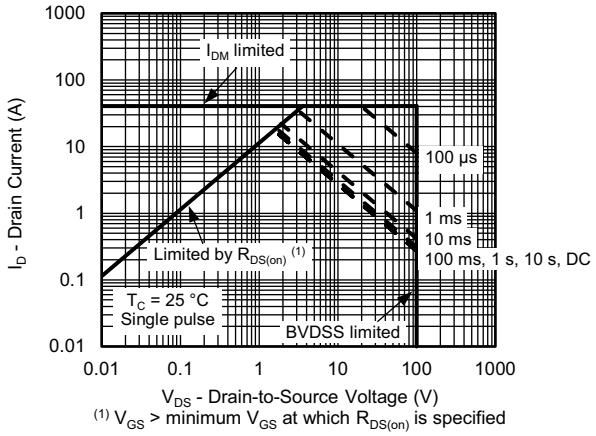
Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

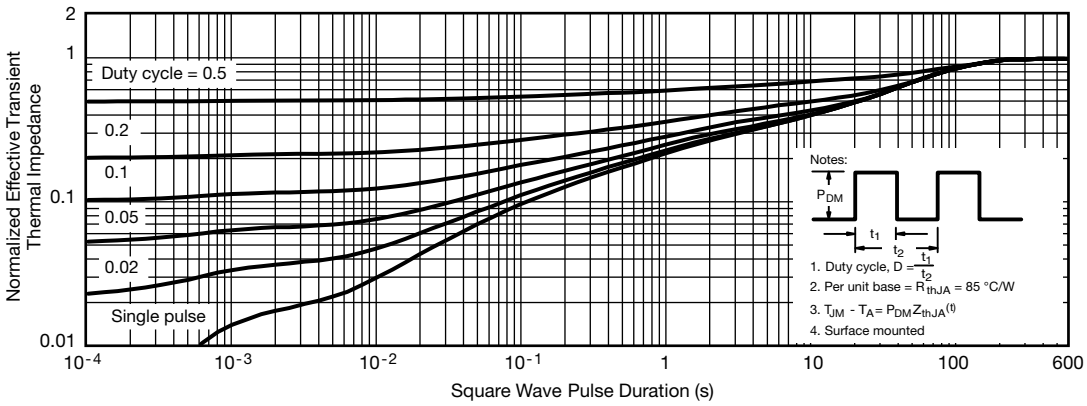


On-Resistance vs. Junction Temperature

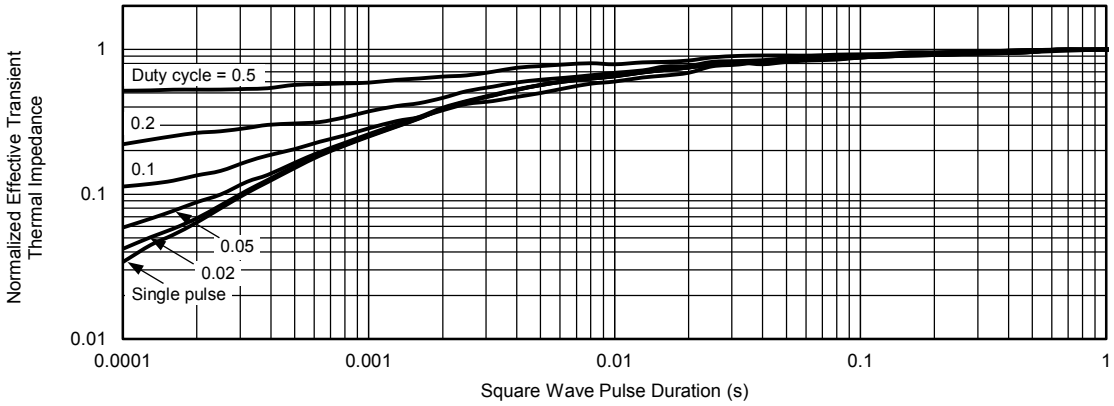


Safe Operating Area

N-CHANNEL 1 TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



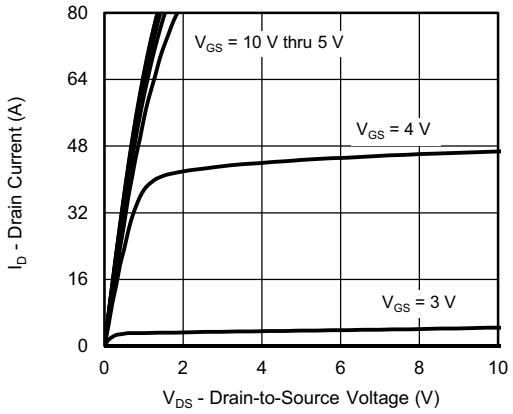
Normalized Thermal Transient Impedance, Junction-to-Case

Note

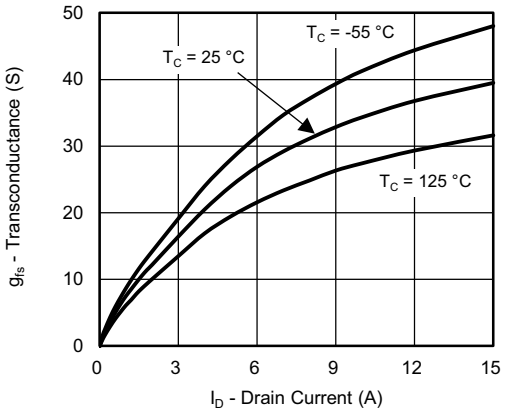
- The characteristics shown in the graph:
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

  is given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

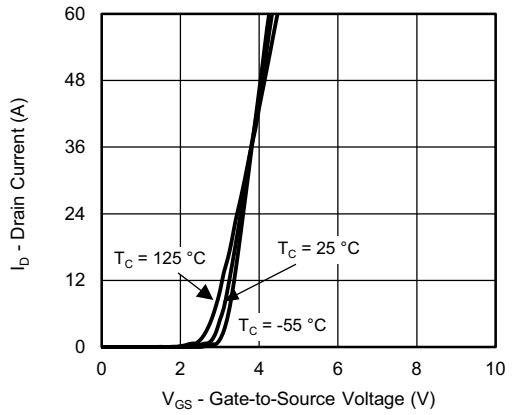
N-CHANNEL 2 TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



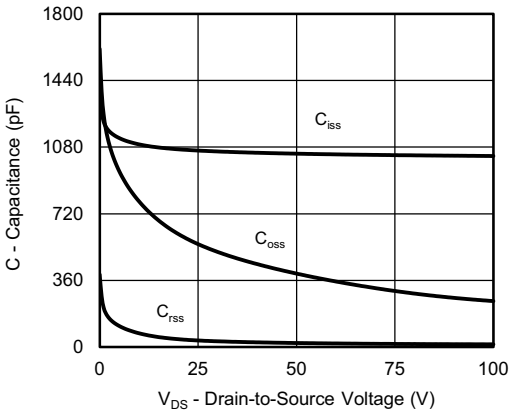
Output Characteristics



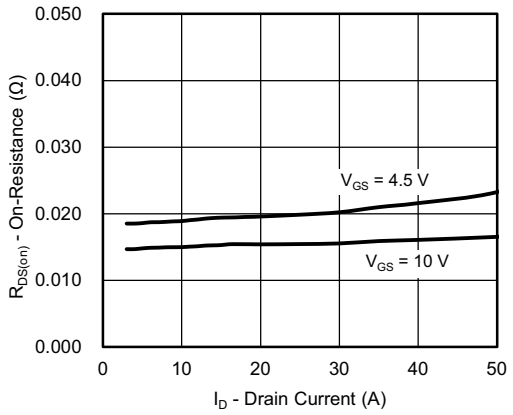
Transconductance



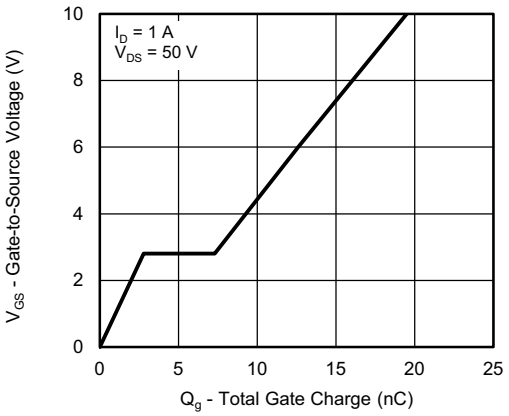
Transfer Characteristics



Capacitance

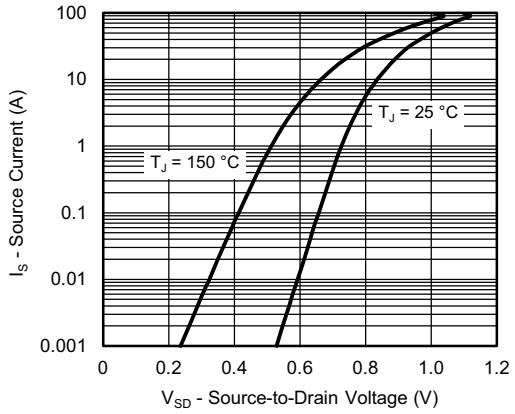


On-Resistance vs. Drain Current

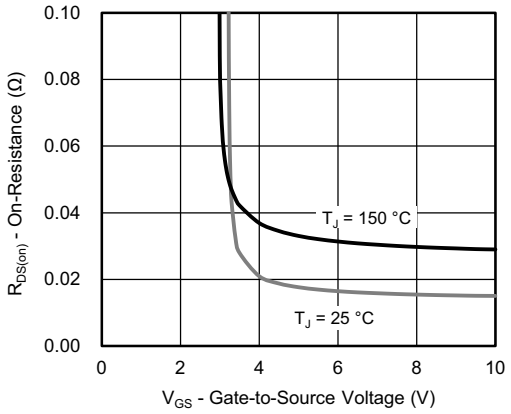


Gate Charge

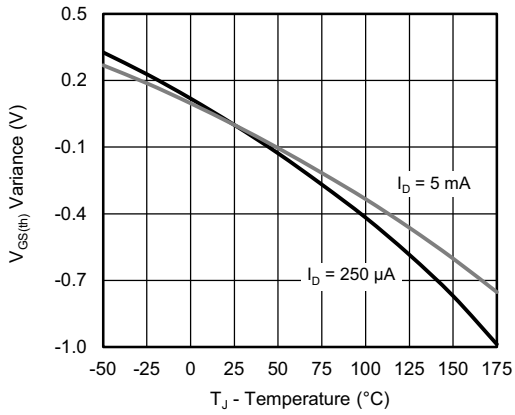
N-CHANNEL 2 TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



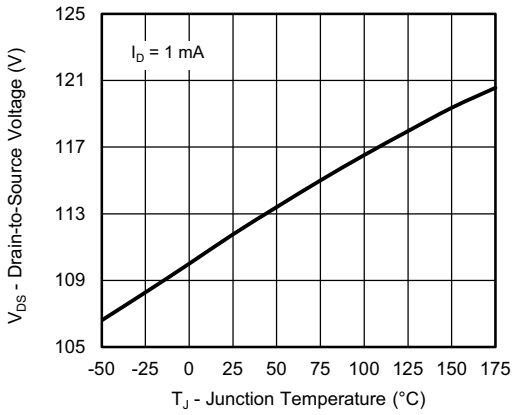
Source Drain Diode Forward Voltage



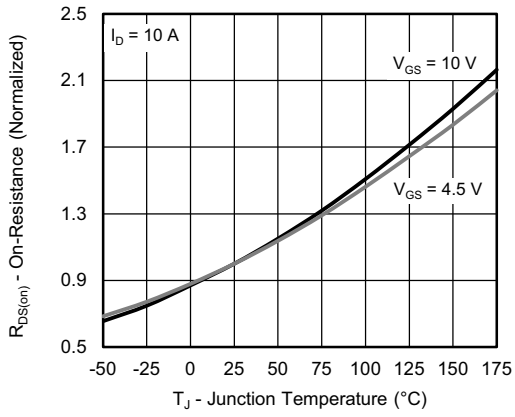
On-Resistance vs. Gate-to-Source Voltage



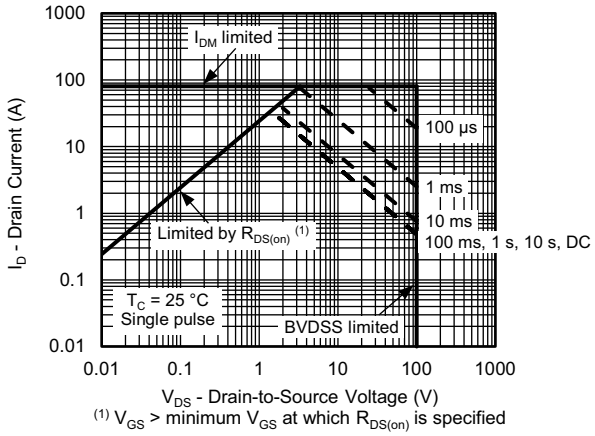
Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

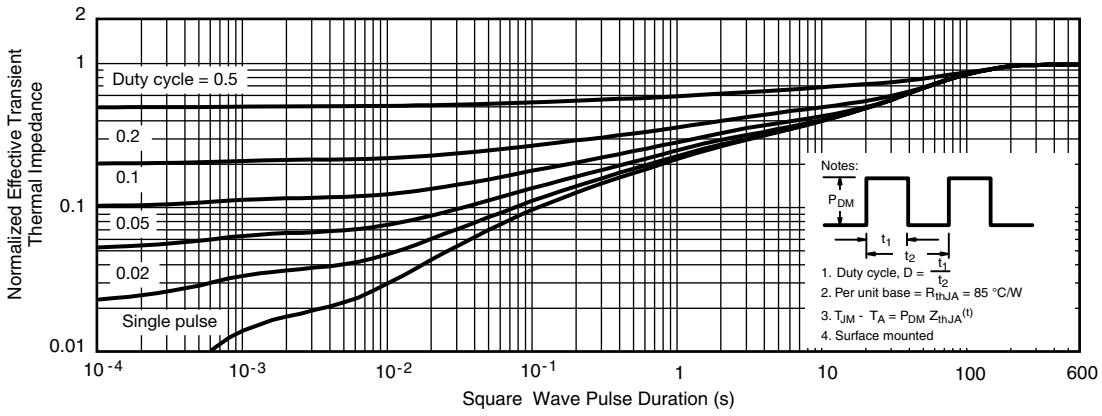


On-Resistance vs. Junction Temperature

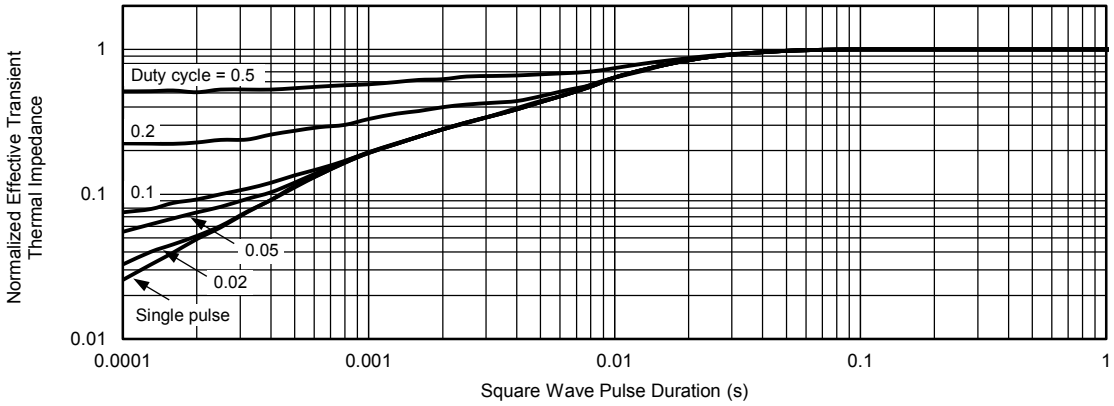


Safe Operating Area

N-CHANNEL 2 TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

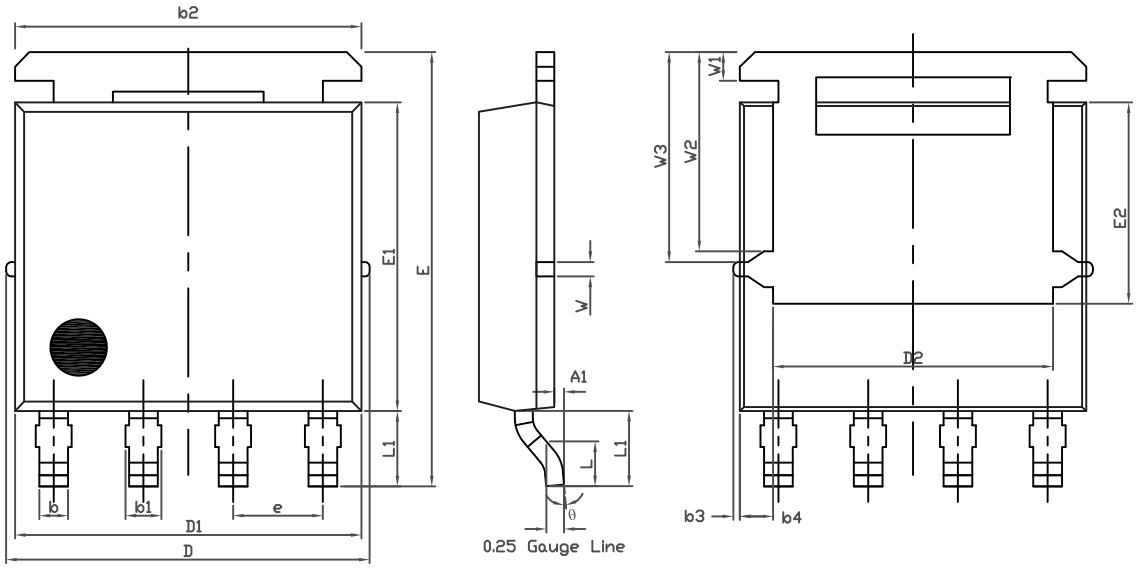
Note

- The characteristics shown in the graph:
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

  is given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

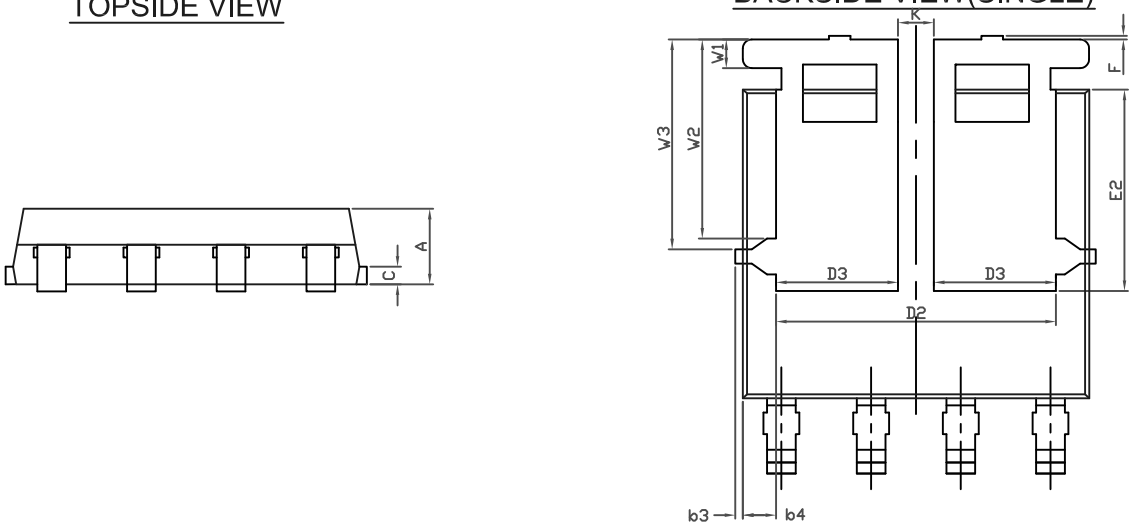
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?77789.

PowerPAK® SO-8L Case Outline 2



TOPSIDE VIEW

BACKSIDE VIEW(SINGLE)



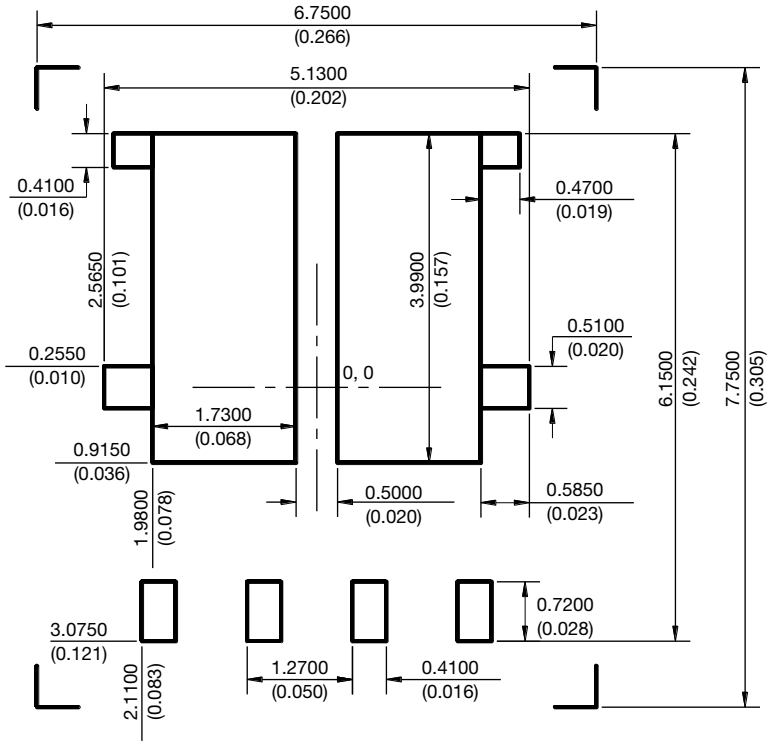
BACKSIDE VIEW(DUAL)

DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	1.00	1.07	1.14	0.039	0.042	0.045
A1	0.00	-	0.127	0.00	-	0.005
b	0.33	0.41	0.48	0.013	0.016	0.019
b1	0.44	0.51	0.58	0.017	0.020	0.023
b2	4.80	4.90	5.00	0.189	0.193	0.197
b3	0.094			0.004		
b4	0.47			0.019		
c	0.20	0.25	0.30	0.008	0.010	0.012
D	5.00	5.13	5.25	0.197	0.202	0.207
D1	4.80	4.90	5.00	0.189	0.193	0.197
D2	3.86	3.96	4.06	0.152	0.156	0.160
D3	1.63	1.73	1.83	0.064	0.068	0.072
e	1.27 BSC			0.050 BSC		
E	6.05	6.15	6.25	0.238	0.242	0.246
E1	4.27	4.37	4.47	0.168	0.172	0.176
E2	2.75	2.85	2.95	0.108	0.112	0.116
F	-	-	0.15	-	-	0.006
L	0.62	0.72	0.82	0.024	0.028	0.032
L1	0.92	1.07	1.22	0.036	0.042	0.048
K	0.51			0.020		
W	0.23			0.009		
W1	0.41			0.016		
W2	2.82			0.111		
W3	2.96			0.117		
q	0°	-	10°	0°	-	10°
ECN: S19-0643-Rev. B, 05-Aug-2019 DWG: 6044						

Note

- Millimeters will gover

RECOMMENDED MINIMUM PAD FOR PowerPAK® SO-8L DUAL



Recommended Minimum Pads
Dimensions in mm (inches)
Keep-out 6.75 (0.266) x 7.75 (0.305)

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, “Vishay”), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

单击下面可查看定价，库存，交付和生命周期等信息

>>Vishay(威世)