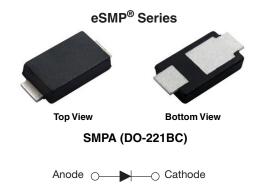


Vishay General Semiconductor

# Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier



**DESIGN SUPPORT TOOLS** 

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PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	4.0 A			
V <sub>RRM</sub>	50 V			
I <sub>FSM</sub>	80 A			
$V_F$ at $I_F$ = 4.0 A ( $T_A$ = 125 °C)	0.46 V			
T <sub>J</sub> max.	150 °C			
Package	SMPA (DO-221BC)			
Circuit configuration	Single			

### FEATURES

- Very low profile typical height of 0.95 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

### **MECHANICAL DATA**

**Case:** SMPA (DO-221BC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V4PAN50	UNIT	
Device marking code		4N5		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	V	
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	4.0	— A	
	I <sub>F</sub> <sup>(2)</sup>	3.0		
Maximum DC reverse voltage	V <sub>DC</sub>	35	V	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	80	A	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

Notes

<sup>(1)</sup> Units mounted on 15 mm x 15 mm pad areas, 2 oz. PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area



COMPLIANT

HALOGEN

FREE



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 2.0 A	- T <sub>A</sub> = 25 °C	V <sub>E</sub> <sup>(1)</sup>	0.43	-	V
	$I_F = 4.0 \text{ A}$			0.51	0.59	
	I <sub>F</sub> = 2.0 A	– T <sub>A</sub> = 125 °C		0.34	-	
	$I_{F} = 4.0 \text{ A}$			0.46	0.54	
Reverse current	V <sub>R</sub> = 35 V	$T_A = 25 \text{°C}$	I <sub>R</sub> (2)	8	-	μA
	$v_{\rm R} = 35 v$	T <sub>A</sub> = 125 °C		8.8	-	mA
	V <sub>B</sub> = 50 V	T <sub>A</sub> = 25 °C		-	600	μA
	$v_{\rm R} = 50 v$	T <sub>A</sub> = 125 °C		12	35	mA
Typical junction capacitance	4.0 V, 1 MH	4.0 V, 1 MHz		480	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise specified)				
PARAMETER	SYMBOL	V4PAN50	UNIT	
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>	100	°C/W	
	R <sub>0JM</sub> <sup>(1)</sup>	9		

#### Note

<sup>(1)</sup> Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient;  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V4PAN50-M3/I	0.032	I	14 000	13" diameter plastic tape and reel		

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise specified)

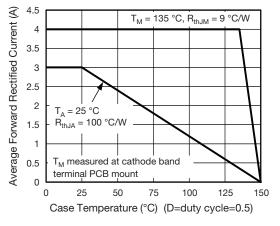


Fig. 1 - Maximum Forward Current Derating Curve

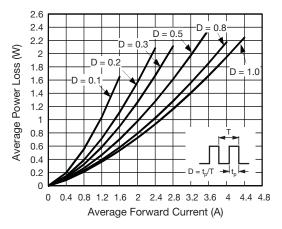


Fig. 2 - Forward Power Loss Characteristics





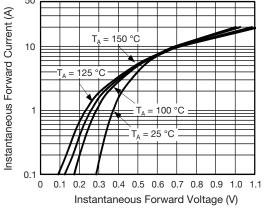


Fig. 3 - Typical Instantaneous Forward Characteristics

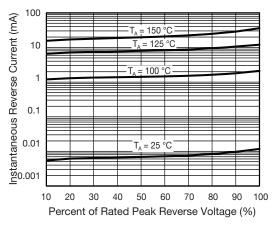


Fig. 4 - Typical Reverse Leakage Characteristics

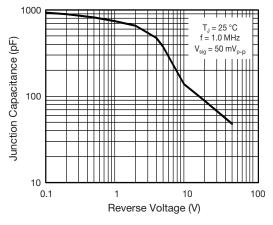


Fig. 5 - Typical Junction Capacitance

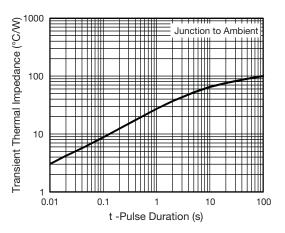


Fig. 6 - Typical Transient Thermal Impedance

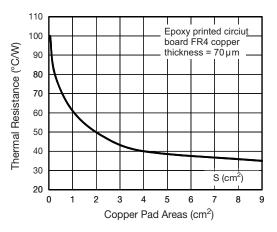


Fig. 7 - Thermal Resistance Junction to Ambient vs. Copper Pad Areas

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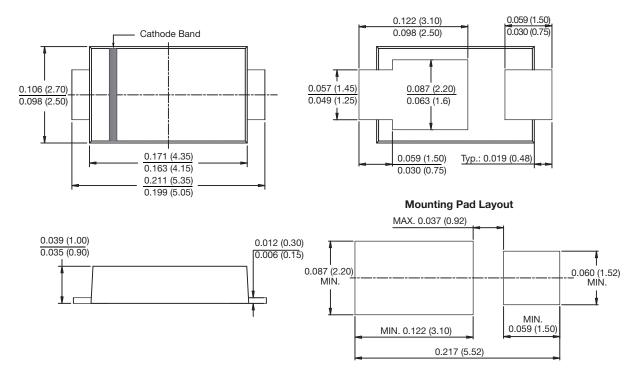
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## PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

SMPA (DO-221BC)





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