

**Vishay Semiconductors** 

# **Small Signal Fast Switching Diode**



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**DESIGN SUPPORT TOOLS** 



### **MECHANICAL DATA**

Case: SOD-123 Weight: approx. 10.3 mg Packaging codes / options: 18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

**FEATURES** 

- Silicon epitaxial planar diode
- · Fast switching diode
- AEC-Q101 qualified available
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3 RoHS-compliant, AEC-Q101 gualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





RoHS

COMPLIANT

PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAS16D	BAS16D-E3-08 or BAS16D-E3-18 BAS16D-HE3-08 or BAS16D-HE3-18	Single	A6	Tape and reel	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	75	V	
Repetitive peak reverse voltage		V <sub>RRM</sub>	100	V	
Forward current (continuous)		I <sub>F</sub>	250	mA	
	t = 1 µs	I <sub>FSM</sub>	2	А	
Non-repetitive peak forward current	t = 1 ms	I <sub>FSM</sub>	1	А	
	t = 1 s	I <sub>FSM</sub>	0.5	А	
Power dissipation <sup>(1)</sup>		P <sub>tot</sub>	350	mW	

<b>THERMAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air <sup>(1)</sup>		R <sub>thJA</sub>	375	K/W	
Maximum junction temperature		Тj	150	°C	
Storage temperature range (1)		T <sub>stg</sub>	-65 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +150	°C	

Note

<sup>(1)</sup> Valid provided electrodes are kept at ambient temperature

Rev. 1.5, 23-Feb-18

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Document Number: 85723

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# BAS16D

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ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	I <sub>F</sub> = 150 mA	V <sub>F</sub>			1.25	V
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>			1	V
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>			0.855	V
	I <sub>F</sub> = 1 mA	V <sub>F</sub>			0.715	V
	V <sub>R</sub> = 75 V	I <sub>R</sub>			1000	nA
Leakage current	$V_{R} = 25 \text{ V}, \text{ T}_{j} = 150 ^{\circ}\text{C}$	I <sub>R</sub>			30	μA
	$V_{R} = 75 \text{ V}, \text{ T}_{j} = 150 ^{\circ}\text{C}$	I <sub>R</sub>			50	μA
Diode capacitance	$V_{R} = 0; f = 1 MHz$	CD			2	pF
Reverse recovery time	$I_F$ = 10 mA, $I_R$ = 10 mA, $i_R$ = 1 mA, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			6	ns

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

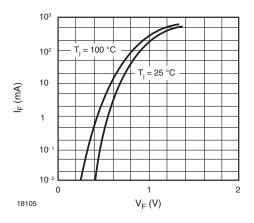


Fig. 1 - Forward Characteristics

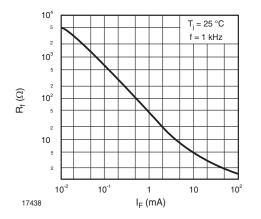


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

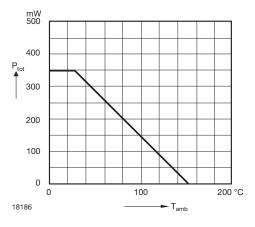


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

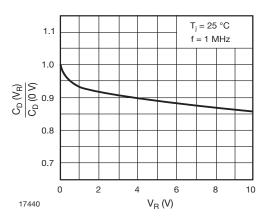


Fig. 4 - Relative Capacitance vs. Reverse Voltage

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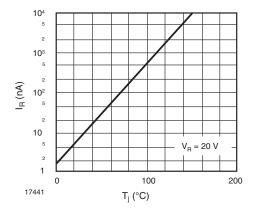


Fig. 5 - Leakage Current vs. Junction Temperature

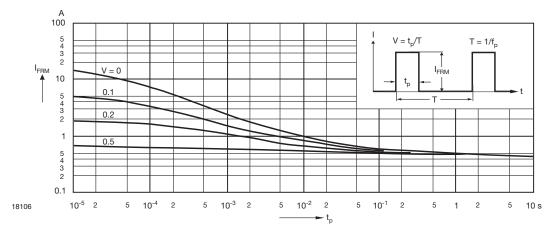
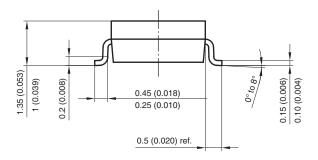


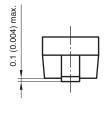
Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration



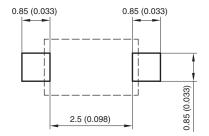
### **Vishay Semiconductors**

### PACKAGE DIMENSIONS in millimeters (inches): SOD-123





Cathode bar 2.85 (0.112) 2.55 (0.100) (6000) 98 0 3.85 (0.152) 3.55 (0.140) Mounting Pad Layout



Rev. 4 - Date: 24. Sep. 2009 Document no.: S8-V-3910.01-001 (4) <sup>17432</sup>



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