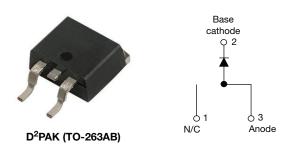


High Performance Schottky Rectifier, 15 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	15 A				
V _R	60 V				
V _F at I _F	0.56 V				
I _{RM} typ.	45 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	6 mJ				
Package	D ² PAK (TO-263AB)				
Circuit configuration	Single				

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation



- High purity, high temperature epoxy FREE encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1A, per J-STD-020, LF maximum peak of 245 °C
- Meets JESD 201 class 1 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-15TQ060SHM3 Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	15	Α			
V_{RRM}		60	V			
I _{FSM}	t _p = 5 μs sine	1000	Α			
V _F	15 A _{pk} , T _J = 125 °C	0.56	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-15TQ060SHM3	UNITS		
Maximum DC reverse voltage	V _R	60	V		
Maximum working peak reverse voltage	V_{RWM}	80	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 104 °C	15	Α		
Maximum peak one cycle	5 μs sine or 3 μs rect. pulse Following any rated			1000		
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	260	A	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.5 A, L = 11.5 mH		6	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximo	o in 1 μs um V _A = 1.5 x V _R typical	1.50	Α	

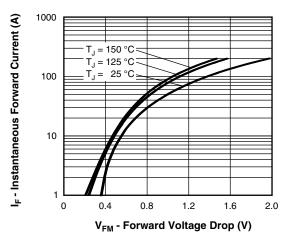


ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		15 A	T _{.1} = 25 °C	0.62	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	30 A	1J=25 C	0.82	V
	V FM (*)	15 A	T _{.1} = 125 °C	0.56	
		30 A	- IJ = 125 C	0.71	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = rated $V_{\rm B}$	0.80	mA
Maximum reverse leakage current		T _J = 125 °C	VR = rated VR	160	
Typical reverse leakage current	I _{RM} (1)	T _J = 125 °C	V_R = rated V_R	45	mA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal ran	ge 100 kHz to 1 MHz), 25 °C	720	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS
Maximum junction and temperature range	d storage	T _J , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	3.25	°C/W
Typical thermal resistate case to heatsink	ance,	R _{thCS}	Mounting surface, smooth and greased	0.50	C/VV
Annuavimenta weight				2	g
Approximate weight				0.07	OZ.
Maunting torque	minimum			6 (5)	kgf. cm
Mounting torque	maximum			12 (10)	(lbf. in)
Marking device			Case style D ² PAK (TO-263AB)	15TQ0	60SH



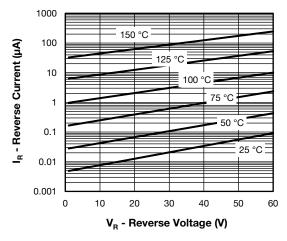


Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

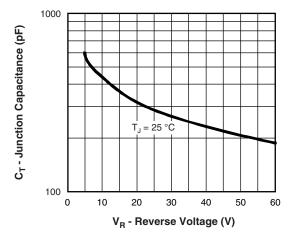


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

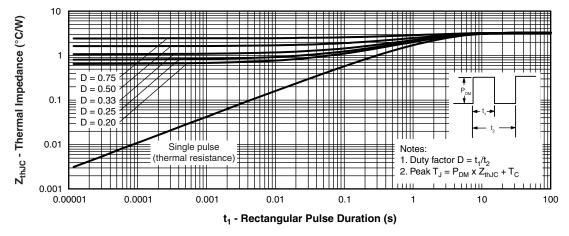


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

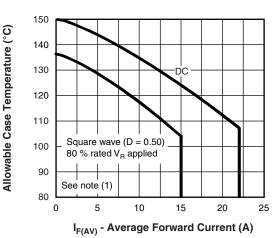


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

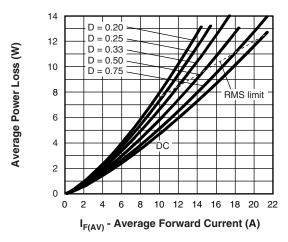


Fig. 6 - Forward Power Loss Characteristics

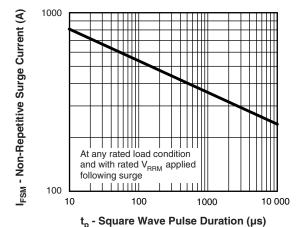


Fig. 7 - Maximum Non-Repetitive Surge Current

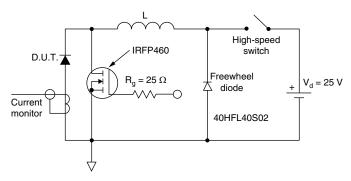


Fig. 8 - Unclamped Inductive Test Circuit

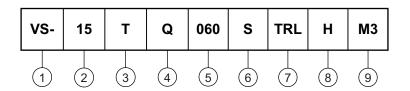
Note

 $^{(2)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (15 A)

Circuit configuration: T = TO-220

4 - Schottky "Q" series

Voltage rating (060 = 60 V)

6 - S = D²PAK

7 • None = tube (50 pieces)

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

B - H = AEC-Q101 qualified

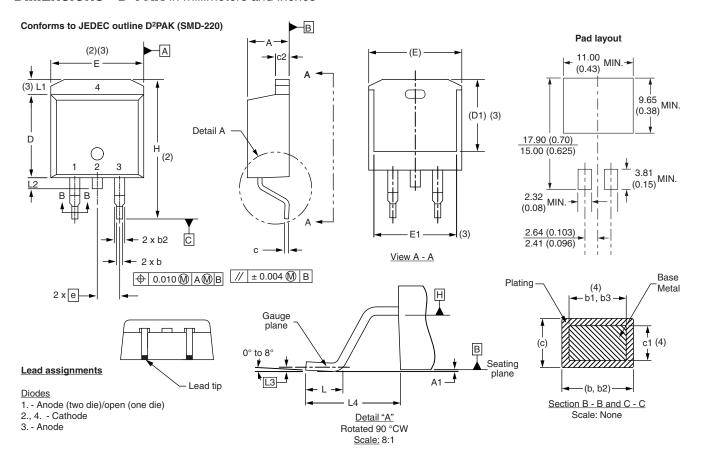
9 - M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-15TQ060SHM3	50	1000	Antistatic plastic tube			
VS-15TQ060STRRHM3	800	800	13" diameter reel			
VS-15TQ060STRLHM3	800	800	13" diameter reel			

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95046</u>					
Part marking information	www.vishay.com/doc?95444				
Packaging information	www.vishay.com/doc?95032				
SPICE model	www.vishav.com/doc?95600				

D²PAK, TO-262

DIMENSIONS - D²PAK in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS		INCHES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

(7) Outline conforms to JEDEC outline TO-263AB

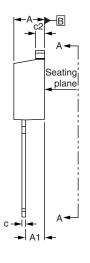
Document Number: 95014 Revision: 31-Mar-09

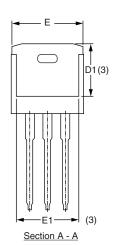
D²PAK, TO-262



DIMENSIONS - TO-262 in millimeters and inches

Modified JEDEC outline TO-262 (Datum A) - (2) (3) (3)D -3 x b2 **-**3 x b





⊕ 0.010 **M** A **M** B

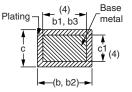
Lead assignments



Diodes

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIMETERS		INC	INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
Е	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.100) BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	=	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



Vishay

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