

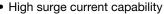
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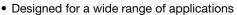
Vishay Semiconductors

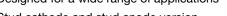
Standard Recovery Diodes, Generation 2 DO-5 (Stud Version), 80 A



FEATUR	RES
• High su	rge d







Stud cathode and stud anode version

Wire version available

- Low thermal resistance
- · Designed and qualified for multiple level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL A	PPLIC	ATIONS
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- · Battery charges
- Converters
- · Power supplies
- · Machine tool controls
- Welding

PRIMARY CHARACTERISTICS				
I _{F(AV)}	80 A			
Package	DO-5 (DO-203AB)			
Circuit configuration	Single			

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
1		80	Α	
I _{F(AV)}	T _C	123	°C	
I _{F(RMS)}		126	А	
	50 Hz	1200	^	
I _{FSM}	60 Hz 1250	1250	Α	
l ² t	50 Hz	7100	A ² s	
1-1	60 Hz	6450	A-5	
V _{RRM}	Range	1400 to 1600	V	
T _J		-55 to +150	°C	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS				
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 150 °C mA
VS-80PF(R)(W)	140	1400	1650	4.5
V3-00FF(N)(VV)	160	1600	1900	4.5

VS-80PF(R)...(W) High Voltage Series

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FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current at case temperature	I _{F(AV)}	180° conduction, half sine wave		80	A	
<u> </u>	` ,				123	°C
Maximum RMS forward current	I _{F(RMS)}				126	Α
		t = 10 ms	No voltage		1200	
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied	1250	Δ.	
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM} reapplied	Sinusoidal half wave, initial T _J = 150 °C	1000	A
		t = 8.3 ms			1050	
t = 8.3 ms reapplied	t = 10 m	t = 10 ms	No voltage		7100	
		6450	A2a			
Maximum I ² t for fusing	1-1	t = 10 ms	100 /0 VRRM		5000	A ² s
		t = 8.3 ms			4550	
Maximum I ² √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied		71 000	A²√s	
Low level value of threshold voltage	V _{F(TO)}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum		0.73	V	
Low level value of forward slope resistance	r _f	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J = T _J maximum		3.0	mΩ	
Maximum forward voltage drop	V_{FM}	$I_{pk} = 220 \text{ A}, T_J = 25 ^{\circ}\text{C}, t_p = 400 \mu \text{s} \text{ rectangular wave}$		1.46	V	

THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction operating and storage temperature range	T _J , T _{Stg}		-55 to 180	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.30	K/W
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, flat and greased	0.25	K/VV
Allowable mounting torque		Not lubricated threads, tighting on nut (1)	3.4 (30)	
		Lubricated threads, tighting on nut (1)	2.3 (20)	N⋅m
		Not lubricated threads, tighting on Hexagon (2)	4.2 (37)	(lbf·in)
		Lubricated threads, tighting on Hexagon (2)	3.2 (28)	
Approximate weight			15.8	g
Approximate weight			0.56	OZ.
Case style		See dimensions - link at the end of datasheet DO-5 (DO-203AB)		D-203AB)

Notes

⁽²⁾ Torque must be applicable only to Hexagon and not to plastic structure, recommended for holed heatsink

△R _{thJC} CONDUCTION					
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS	
180°	0.14	0.10			
120°	0.16	0.17			
90°	0.21	0.22	$T_J = T_J$ maximum	K/W	
60°	0.30	0.31			
30°	0.50	0.50			

Note

• The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

⁽¹⁾ Recommended for pass-through holes

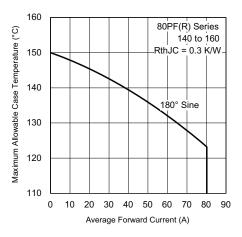


Fig. 1 - Current Ratings Characteristics

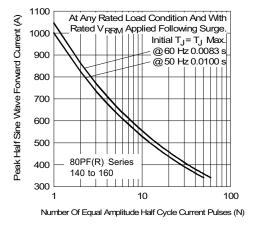


Fig. 2 - Current Ratings Characteristics

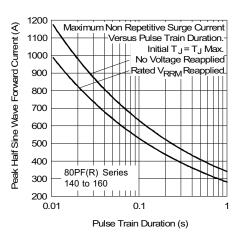


Fig. 3 - Forward Power Loss Characteristics

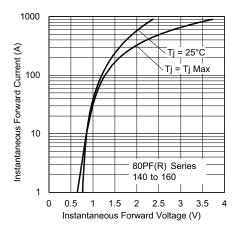


Fig. 4 - Forward Power Loss Characteristics

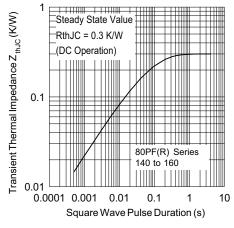


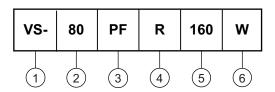
Fig. 5 - Maximum Non-Repetitive Surge Current

VS-80PF(R)...(W) High Voltage Series

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - 80 = standard device

PF = plastic package

None = stud normal polarity (cathode to stud)

• R = stud reverse polarity (anode to stud)

Voltage code x 10 = V_{RRM} (see Voltage Ratings table)

 None = standard terminal (see dimensions for 80PF(R)... - link at the end of datasheet)

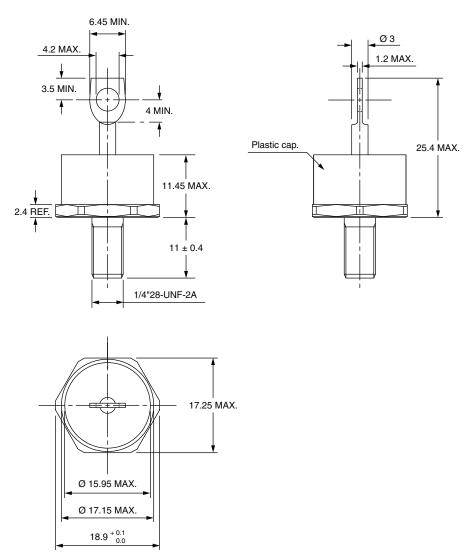
> W = wire terminal (see dimensions for 80PF(R)...W - link at the end of datasheet)

LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95345	



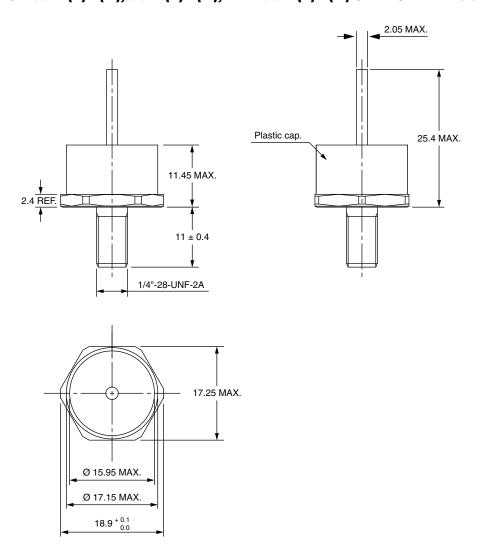
DO-203AB (DO-5) for 50PF(R)...(W), 80PF(R)...(W), and 95PF(R)...(W) Series

DIMENSIONS FOR 80PF(R), 50PF(R), AND 95PF(R) SERIES in millimeters

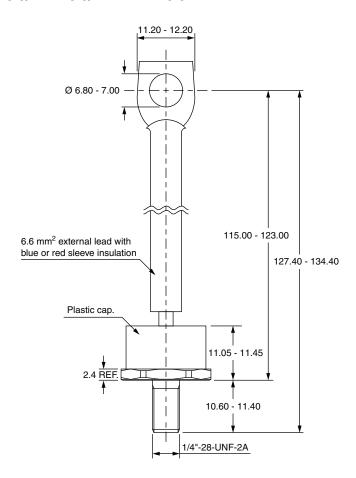




DIMENSIONS FOR 80PF(R)...(W), 50PF(R)...(W), AND 95PF(R)...(W) SERIES in millimeters



DIMENSIONS FOR 52PF(R), 82PF(R), AND 97PF(R) SERIES in millimeters





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