VLMTG41S2U1





Standard SMD LED PLCC-2



DESCRIPTION

This device has been designed to meet the increasing demand for InGaN true green SMD LED.

The package of the VLMTG41.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled with clear epoxy.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-2
- Product series: standard
- Angle of half intensity: ± 60°

FEATURES

- High efficient InGaN technology
- EIA and ICE standard package
- Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape reel
- Preconditioning according to JEDEC[®] level 2a
- AEC-Q101 qualified
- ESD-withstand voltage: Up to 2 kV HBM according to JESD22-A114-B
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Automotive: Backlighting in dashboards and switches
- Telecommunication: Indicator and backlighting in telephone and fax
- · Backlighting for audio and video equipment
- Backlighting in office equipment
- Indoor and outdoor message boards
- Flat backlight for LCDs, switches, and symbols
- Illumination purposes, alternative to incandescent lamps
- General use

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I _F (mA)	WAVELENGTH (nm)		at I _F (mA)	FORWARD VOLTAGE (V)		at I _F (mA)	TECHNOLOGY			
		MIN.	TYP.	MAX. MIN. TYP. MAX.		MIN. TYP. M	MAX.							
VLMTG41S2U1-GS08	True green	224	380	560	10	515	530	541	10	-	3.2	4.2	20	InGaN on Sapphire
VLMTG41S2U1-GS18	True green	224	380	560	10	515	530	541	10	-	3.2	4.2	20	InGaN on Sapphire

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) VLMTG41						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
DC forward current	$T_{amb} \le 80 \ ^{\circ}C$	١ _F	20	mA		
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.1	A		
Power dissipation		Pv	84	mW		
Junction temperature		Tj	110	°C		
Storage temperature range		T _{stg}	-40 to +100	°C		
Operating temperature range		T _{amb}	-40 to +100	°C		
Thermal resistance junction/ambient	Mounted on PC board (pad size > 16 mm ²)	R _{thJA}	360	K/W		

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e3 RoHS

HALOGEN

FREE

GREEN

(5-2008)



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OPTICAL AND ELECTR VLMTG41, TRUE GRE	TICAL AND ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) MTG41, TRUE GREEN						
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 10 mA	VLMTG41S2U1	Ι _V	224	380	560	mcd
Dominant wavelength	I _F = 10 mA		λ _d	515	530	541	nm
Peak wavelength	I _F = 10 mA		λρ	-	520	-	nm
Angle of half intensity	I _F = 10 mA		φ	-	± 60	-	deg
Forward voltage	I _F = 20 mA		V _F	-	3.2	4.2	V
Temperature coefficient of V_F	I _F = 10 mA		TC _{VF}	-	-4	-	mV/K
Temperature coefficient of I_V	I _F = 10 mA		TCIV	-	-0.25	-	%/K

Note

Not designed for reverse operation

LUMINOUS INTENSITY CLASSIFICATION					
GROUP	LIGH	IT INTENSITY (I	ncd)		
STANDARD	OPTIONAL	MIN.	N. MAX.		
S	2	224	280		
т	1	280	355		
1	2	355	450		
U	1	450	560		

Note

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of \pm 11 %.

These type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups are not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups are not be orderable.

COLOR CLASSIFICATION

	TRUE G	REEN
GROUP	DOM. WAVEL	ENGTH (nm)
	MIN.	MAX.
3	515	523
4	521	529
5	527	535
6	533	541

Note

· Wavelengths are tested at a current pulse duration of 25 ms.

CROSSING TABLE	
VISHAY	OSRAM
VLMTG41S2U1	LTT67CS2U1

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

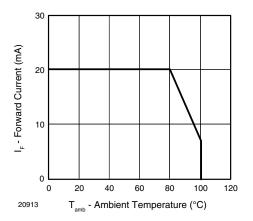
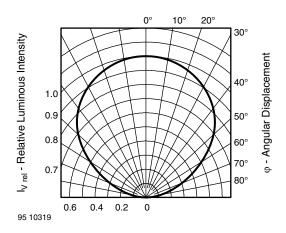


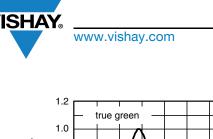
Fig. 1 - Forward Current vs. Ambient Temperature





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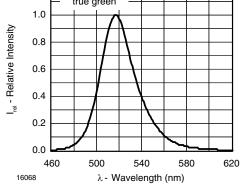


Fig. 3 - Relative Luminous Intensity vs. Wavelength

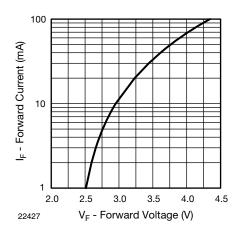


Fig. 4 - Forward Current vs. Forward Voltage

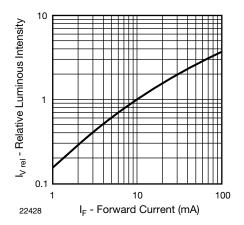


Fig. 5 - Relative Luminous Intensity vs. Forward Current

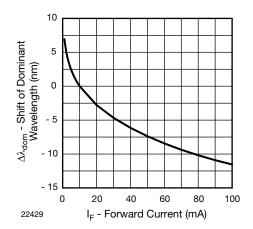


Fig. 6 - Shift of Dominant Wavelength vs. Forward Current

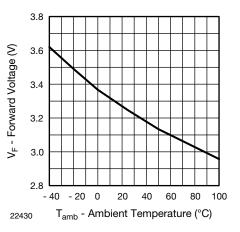


Fig. 7 - Forward Voltage vs. Ambient Temperature

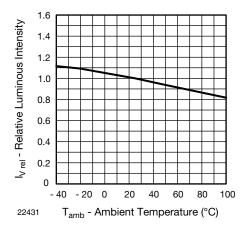


Fig. 8 - Relative Luminous Intensity vs. Ambient Temperature

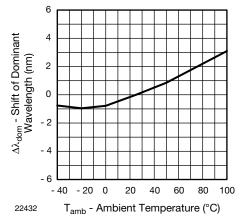
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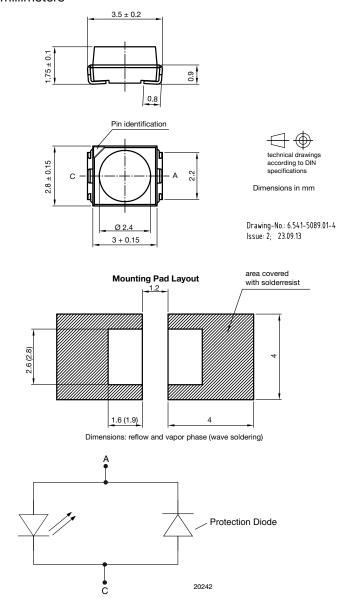


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Fig. 9 - Shift of Dominant Wavelength vs. Ambient Temperature



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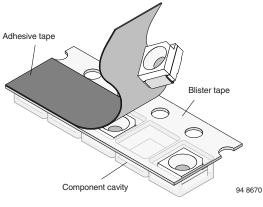




METHOD OF TAPING/POLARITY AND TAPE AND REEL

SMD LED (VLM.3..., VLM.4...-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3..., VLM.4...

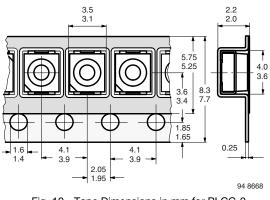


Fig. 10 - Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

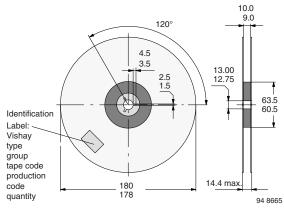
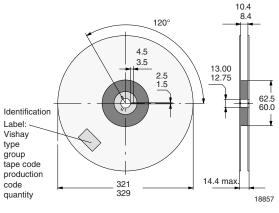


Fig. 11 - Reel Dimensions - GS08

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED





SOLDERING PROFILE

IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC level 2a

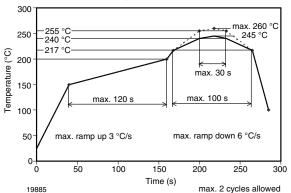


Fig. 13 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

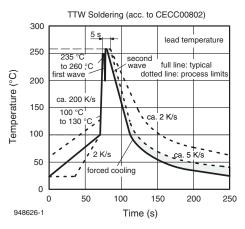


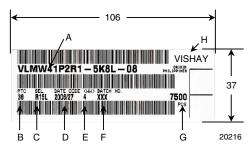
Fig. 14 - Double Wave Soldering of Opto Devices (all Packages)

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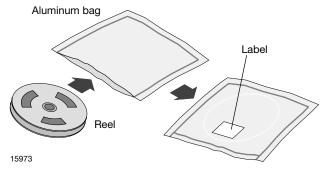
BAR CODE PRODUCT LABEL (example)



- A. Type of component
- B. Manufacturing plant
- C. SEL selection code (bin): e.g.: R1 = code for luminous intensity group 5L = code for chrom. coordinate group
- D. Date code year/week
- E. Day code (e.g. 4: Thursday)
- F. Batch no.
- G. Total quantity
- H. Company code

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

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RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen)

or

96 h at 60 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.

	CAUTION This bag contains RE-SENSITIVE DEVICES	l
1. Shelf life in sealed bag 12 mont	ths at <40°C and < 90% relative humidity	(RH)
vapor-phase reflow, or equivale 260°C) must be:	s that will be subjected to infrared reflow, ent processing (peak package body temp. urs at factory condition of $\leq 30^{\circ}$ C/60%R	
 Devices require baking before r a) Humidity Indicator Carc b) 2a or 2b is not met. 	mounting if: d is >10% when read at $23^{\circ}C \pm 5^{\circ}C$ or	
 If baking is required, devices m 192 hours at 40°C + 5°C/- 96 hours at 60±5°Cand <59 24 hours at 100±5°C 	0°C and <5%RH (dry air/nitrogen)	or or Des
Bag Seal Date:(If blank, se	ee bar code label)	
Note: LEVEL defined b	y EIA JEDEC Standard JESD22-A113	

Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABEL

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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