

Vishay General Semiconductor

Surface Mount Trench MOS Barrier Schottky Rectifier



DO-214AC (SMA)

3.0 A

60 V

80 A

0.41 V

150 °C

DO-214AC (SMA)

Single die

PRIMARY CHARACTERISTICS

I_{F(AV)}

V_{RRM}

I_{FSM}

 V_F at $I_F = 3.0 A$

T_{.1} max.

Package

Diode variations

FEATURES

- Low profile package
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Not recommended for PCB bottom side wave mounting
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: DO-214AC (SMA) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes the cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	VSSA3L6S	UNIT	
Device marking code		3L6		
Maximum repetitive peak reverse voltage	V _{RRM}	60	V	
Maximum DC forward current	I _F ⁽¹⁾	3.0	— A	
	I _F ⁽²⁾	2.5		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	IFSM	80	А	
Voltage rate of change (rated V _R)	dV/dt	10 000	V/µs	
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +150	°C	

Notes

⁽¹⁾ Mounted on 10 mm x 10 mm pad areas, 1 oz. FR4 PCB

⁽²⁾ Free air, mounted on recommended copper pad area

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ROHS COMPLIANT

HALOGEN

FREE



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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 3.0 A	T _A = 25 °C T _A = 125 °C	V _F ⁽¹⁾	0.49	0.58	V
		T _A = 125 °C		0.41	0.50	
Reverse current	$V_{\rm R} = 60 \text{ V} \qquad \frac{T_{\rm A} = 25}{T_{\rm A} = 12}$	T _A = 25 °C	I _R ⁽²⁾	-	1500	μA
		T _A = 125 °C		6.0	30	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	395	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise specified)				
PARAMETER	SYMBOL	VSSA3L6S	UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	115	°C/W	
	R _{0JM} ⁽²⁾	15		

Notes

 $^{(1)}$ Free air, mounted on recommended PCB, 1 oz. pad area; thermal resistance $R_{\theta JA}$ - junction to ambient

⁽²⁾ Mounted on 10 mm x 10 mm pad areas, 1 oz. FR4 PCB; R_{0JM} - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSSA3L6S-M3/61T	0.064	61T	1800	7" diameter plastic tape and reel	
VSSA3L6S-M3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

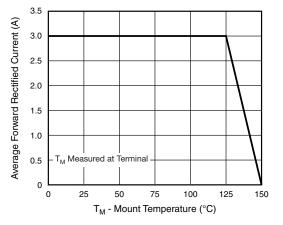


Fig. 1 - Maximum Forward Current Derating Curve

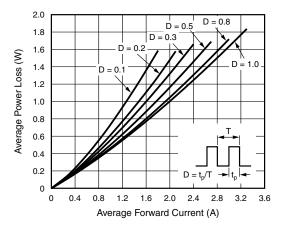
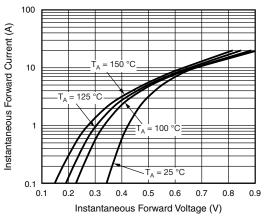


Fig. 2 - Forward Power Loss Characteristics

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Fig. 3 - Typical Instantaneous Forward Characteristics

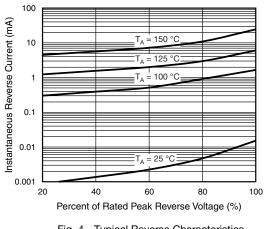
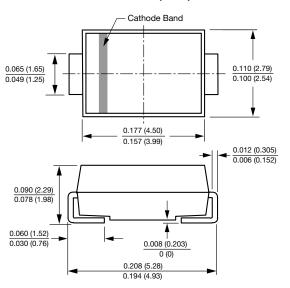


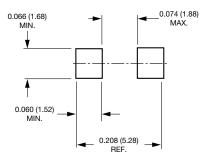
Fig. 4 - Typical Reverse Characteristics

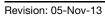




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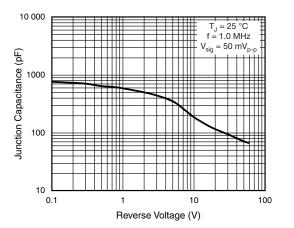


Fig. 5 - Typical Junction Capacitance

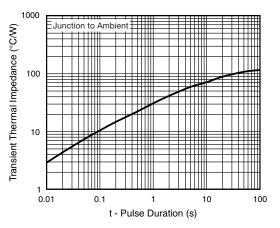


Fig. 6 - Typical Transient Thermal Impedance



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