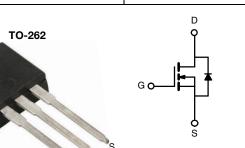


Vishay Siliconix

# Automotive N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	60					
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS}$ = 10 V	0.00470					
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 4.5 \text{ V}$	0.00600					
I <sub>D</sub> (A)	120					
Configuration	Single					
Package	TO-262					



N-Channel MOSFET

#### **FEATURES**

- TrenchFET® power MOSFET
- Package with low thermal resistance
- AEC-Q101 qualified d
- 100 % R<sub>a</sub> and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



FREE

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		$V_{DS}$	60	V	
Gate-Source Voltage		$V_{GS}$	± 20	V	
Continuous Drain Current	T <sub>C</sub> = 25 °C <sup>a</sup>	1	120		
	T <sub>C</sub> = 125 °C	- I <sub>D</sub>	91		
Continuous Source Current (Diode Conduction) a		I <sub>S</sub>	120	Α	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	300		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	80		
Single Pulse Avalanche Energy	L = 0.1 IIIA	E <sub>AS</sub>	320	mJ	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	Pn	250	W	
	T <sub>C</sub> = 125 °C	r'D	83	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount c	$R_{thJA}$	40	°C/W		
Junction-to-Case (Drain)	n-to-Case (Drain)		0.6	G/VV		

#### Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq 300 \,\mu\text{s}$ , duty cycle  $\leq 2 \,\%$ .
- c. When mounted on 1" square PCB (FR4 material).
- d. Parametric verification ongoing.

Top View



## Vishay Siliconix

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static		1				l		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0, I <sub>D</sub> = 250 μA		60	-	-	V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$		1.5	2.0	2.5		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
		$V_{GS} = 0 V$	V <sub>DS</sub> = 60 V	-	-	1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 125 °C	-	-	50	μΑ	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 175 °C	-	-	500		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	120	-	-	Α	
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A	-	0.00378	0.00470	Ω	
Drain-Source On-State Resistance <sup>a</sup>	D	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C	-	-	0.00801		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C	-	-	0.00992		
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 20 A	-	0.00481	0.00600		
Forward Transconductance b	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		-	118	-	S	
Dynamic <sup>b</sup>								
Input Capacitance	C <sub>iss</sub>		V <sub>GS</sub> = 0 V V <sub>DS</sub> = 25 V, f = 1 MHz	-	6705	8800	pF	
Output Capacitance	Coss	$V_{GS} = 0 V$		-	904	1200		
Reverse Transfer Capacitance	C <sub>rss</sub>			-	555	800		
Total Gate Charge <sup>c</sup>	Qg			-	148	230		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	= 10 V $V_{DS} = 30 \text{ V}, I_D = 85 \text{ A}$	-	21.4	-	nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	33.2	-		
Gate Resistance	Rg	f = 1 MHz		0.45	0.99	1.5	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	$V_{DD}=30$ V, $R_L=0.353$ $\Omega$ $I_D\cong85$ A, $V_{GEN}=10$ V, $R_g=1$ $\Omega$		-	16	25		
Rise Time <sup>c</sup>	t <sub>r</sub>			-	9	15	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	46	75		
Fall Time <sup>c</sup>	t <sub>f</sub>			-	12	20		
Source-Drain Diode Ratings and Char	racteristics b							
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	300	Α	
Forward Voltage	$V_{\mathrm{SD}}$	I <sub>F</sub> = 70 A, V <sub>GS</sub> = 0		_	0.92	1.5	V	

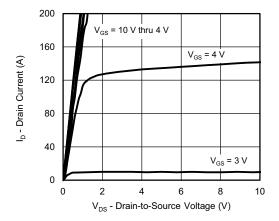
#### **Notes**

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

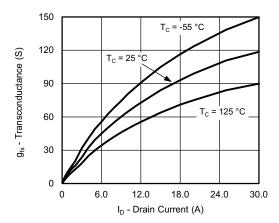
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



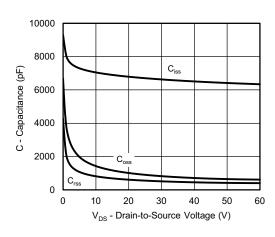
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



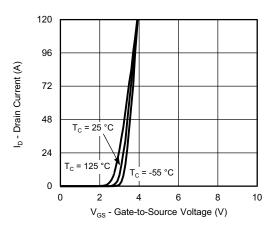
### **Output Characteristics**



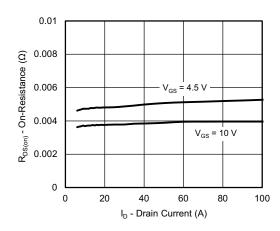
### Transconductance



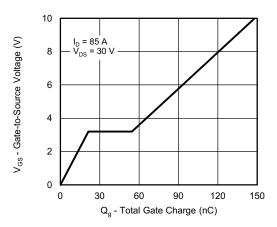
Capacitance



#### **Transfer Characteristics**



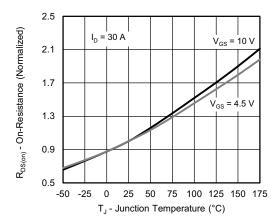
On-Resistance vs. Drain Current



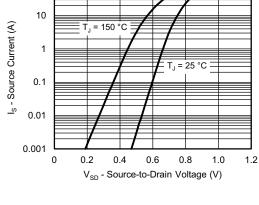
**Gate Charge** 



### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

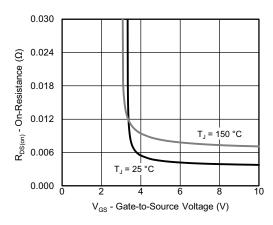


On-Resistance vs. Junction Temperature

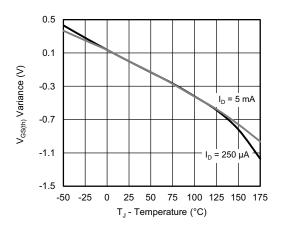


100

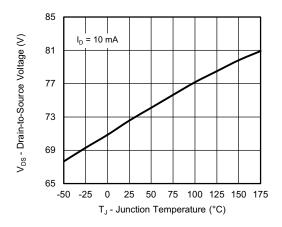
**Source Drain Diode Forward Voltage** 



On-Resistance vs. Gate-to-Source Voltage



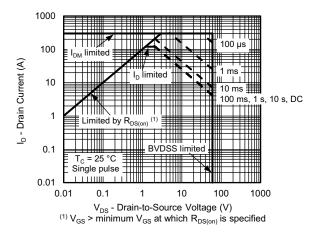
Threshold Voltage



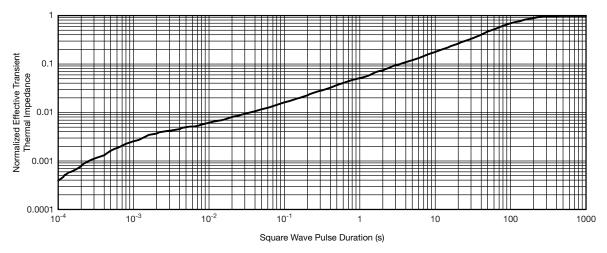
Drain Source Breakdown vs. Junction Temperature



### **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)



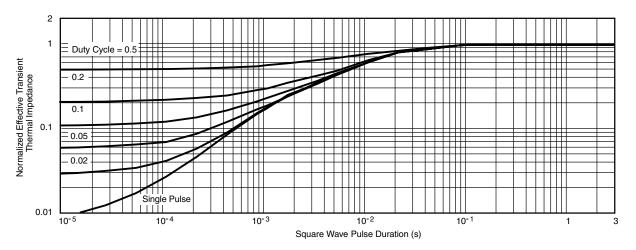
### Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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### THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

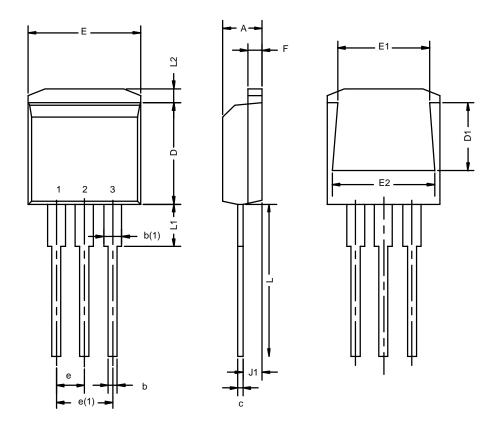
- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?66962">www.vishay.com/ppg?66962</a>.



### TO-262: 3-LEAD



	MILLIM	ETERS*	INCHES		
Dim	Min	Max	Min	Max	
Α	4.32	4.70	0.170	0.185	
b	0.64	1.00	0.025	0.039	
b(1)	1.14	1.40	0.045	0.055	
С	0.36	0.50	0.014	0.020	
D	8.64	9.65	0.340	0.380	
D1	5.59	6.10	0.220	0.240	
е	2.41	2.67	0.095	0.105	
e(1)	4.95	5.33	0.195	0.210	
Е	10.03	10.41	0.395	0.410	
E1	7.87	8.64	0.310	0.340	
E2	9.02	9.53	0.355	0.375	
F	1.14	1.40	0.045	0.055	
J1	2.41	2.79	0.095	0.110	
L	13.08	14.22	0.515	0.560	
L1	-	3.81	-	0.150	
L2	1.02	1.40	0.040	0.055	
ECN: T-02234—Rev. C, 14-Oct-02 DWG: 5855					

 ${}^{\star}\text{Use}$  millimeters as the primary measurement

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