

RoHS

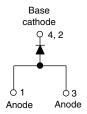
COMPLIANT

HALOGEN

FREE

Schottky Rectifier, 5.5 A



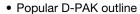


D-PAK	(TO-252AA)
D-FAIL	(10-232AA)

PRODUCT SUMMARY	
Package	D-PAK (TO-252AA)
I _{F(AV)}	5.5 A
V_{R}	60 V
V _F at I _F	See Electrical table
I _{RM}	35 mA at 125 °C
T _J max.	150 °C
Diode variation	Single die
E _{AS}	7 mJ

FEATURES

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability



- · Small foot print, surface mountable
- · High frequency operation
- AEC-Q101 qualified
- ιk
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

•	Me	et	s,	JESL	20.	1 Cla	ass 2	whish	cer te	est			
•	Me	et	s	MSL	. lev	el 1	, per	J-ST	D-02	20,	LF	maximum	n peal
	of	26	0	°C									
								_					

DESCRIPTION

The VS-50WQ06FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS							
I _{F(AV)}	Rectangular waveform	5.5	А						
V_{RRM}		60	V						
I _{FSM}	t _p = 5 μs sine	320	А						
V _F	5 A _{pk} , T _J = 125 °C	0.54	V						
T _J	Range	- 40 to 150	°C						

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-50WQ06FNHM3 UNITS							
Maximum DC reverse voltage	V_{R}	60	V				
Maximum working peak reverse voltage	V_{RWM}	00	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 132 °C	5.5					
Maximum peak one cycle non-repetitive surge current	l	5 μs sine or 3 μs rect. pulse Following any rated load condition and with		320	Α			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	105				
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1.2 \text{A}, L = 10 \text{n}$	7	mJ				
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximo	0.8	А				



ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		5 A	T _{.1} = 25 °C	0.57	V			
Maximum forward voltage drop	V _{FM} ⁽¹⁾	10 A	11 = 23 0	0.74				
See fig. 1	V FM (*)	5 A	T _{.1} = 125 °C	0.54				
		10 A	1j = 125 C	0.68				
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3	- mA			
See fig. 2	IRM '''	T _J = 125 °C	VR = nateu VR	35				
Threshold voltage	V _{F(TO)}	T - T movimum		0.35	V			
Forward slope resistance	r _t	$T_J = T_J$ maximum		25.5	mΩ			
Typical junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	360	pF				
Typical series inductance	L _S	Measured lead to lead 5 r	5.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs			

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C			
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W			
Approximate weight			0.3	g			
Approximate weight			0.01	oz.			
Marking device		Case style D-PAK	50WQ	D6FNH			

Note

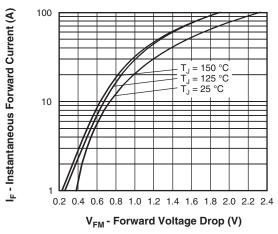


Fig. 1 - Maximum Forward Voltage Drop Characteristics

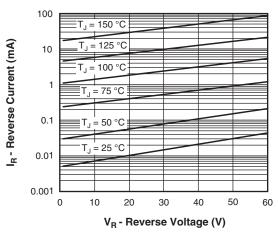


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

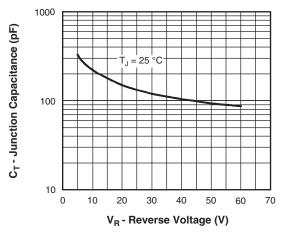


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

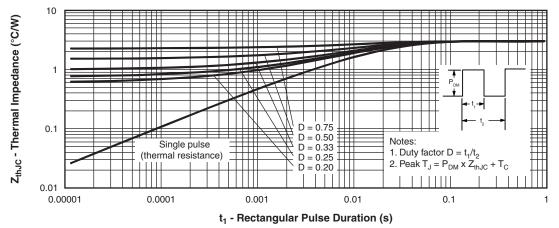


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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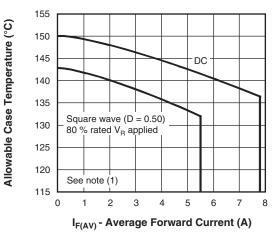


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

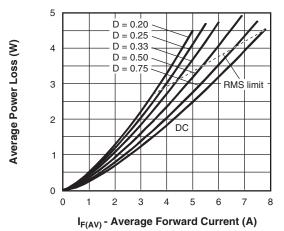


Fig. 6 - Forward Power Loss Characteristics

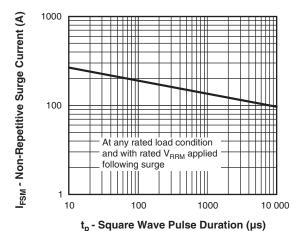


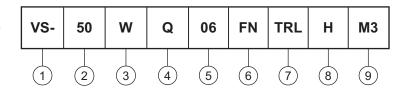
Fig. 7 - Maximum Non-Repetitive Surge Current

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (5.5 A)

Package identifier:

W = D-PAK

4 - Schottky "Q" series

Voltage rating (06 = 60 V)

6 - FN = TO-252AA (D-PAK)

7 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

8 - H = AEC-Q101 qualified

9 - Environmental digit:

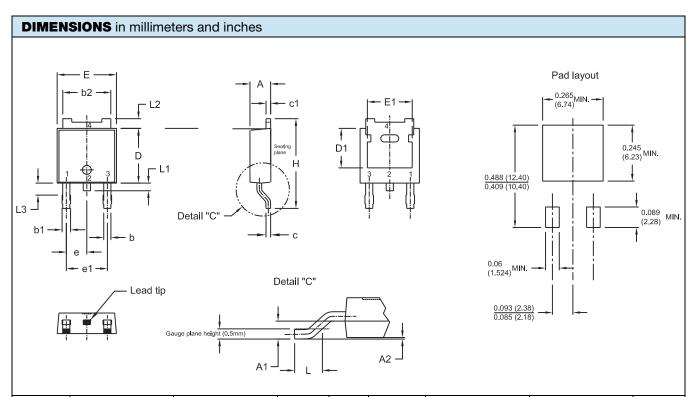
M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-50WQ06FNHM3	75	3000	Antistatic plastic tube					
VS-50WQ06FNTRHM3	2000	2000	13" diameter reel					
VS-50WQ06FNTRRHM3	3000	3000	13" diameter reel					
VS-50WQ06FNTRLHM3	3000	3000	13" diameter reel					

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				



D-PAK (TO-252AA)



SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	2.21	2.38	0.087	0.094		
A2	0.03	0.127	0.001	0.005		
b	0.71	0.88	0.028	0.035		
b1	0.76	1.14	0.030	0.045		
b2	5.23	5.44	0.206	0.214		
С	0.46	0.58	0.018	0.023		
C1	0.46	0.58	0.018	0.023		
D	5.97	6.22	0.235	0.2455		
D1	4.32	4.45	0.170	0.175		
E	6.48	6.73	0.255	0.2655		
E1	4.49	5.50	0.177	0.217		

SYMBOL	MILLIN	IETERS	INC	NOTES	
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
A1	0.89	1.14	0.035	0.045	
Н	9.65	10.41	0.380	0.410	
L	1.40	1.78	0.055	0.070	
е	2.28 BSC		0.09		
e1	4.57	BSC	0.18		
L1	0.64	1.02	0.025	0.040	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.040	0.060	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L3 only for reference
- Dimension D1, E1, L2 and b2 establish a minimum mounting surface for thermal pad
- 4) Dimensions D and E do not include mold flash.
- (5) Outline conforms to JEDEC outline TO-252AA



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