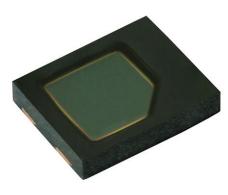


Silicon PIN Photodiode



DESCRIPTION

VEMD5010X01 is a high speed and high sensitive PIN photodiode. It is a low profile surface mount device (SMD) including the chip with a 7.5 mm² sensitive area detecting visible and near infrared radiation.

FEATURES

- Package type: surface mount
- · Package form: top view
- Dimensions (L x W x H in mm): 5 x 4 x 0.9
- Radiant sensitive area (in mm²): 7.5
- AEC-Q101 qualified
- · High photo sensitivity
- · High radiant sensitivity
- Suitable for visible and near infrared radiation
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 65^{\circ}$
- Floor life: 72 h, MSL 4, according to J-STD-020
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



GREEN

(5-2008)

AUTOMOTIVE

APPLICATIONS

· High speed photo detector

PRODUCT SUMMARY			
COMPONENT	I _{ra} (μΑ)	φ (deg)	λ _{0.1} (nm)
VEMD5010X01	48	± 65	430 to 1100

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMD5010X01	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Top view	
VEMD5010X01-GS15	Tape and reel	MOQ: 5000 pcs, 5000 pcs/reel	Top view	

Note

· MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_{R}	20	V
Power dissipation	T _{amb} ≤ 25 °C	P _V	215	mW
Junction temperature		T _j	110	°C
Operating temperature range		T _{amb}	-40 to +110	°C
Storage temperature range		T _{stg}	-40 to +110	°C
Soldering temperature	Acc. reflow solder profile fig. 8	T _{sd}	260	°C
Thermal resistance junction/ambient		R _{thJA}	350	K/W
ESD safety HBM	± 2000 V, 1.5 kΩ, 100 pF, 3 pulses	ESD _{HBM}	≥2	kV



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BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Forward voltage	$I_F = 50 \text{ mA}$	V_{F}		1	1.3	V	
Breakdown voltage	$I_R = 100 \mu A, E = 0$	V _(BR)	20			V	
Reverse dark current	V _R = 10 V, E = 0	I _{ro}		2	30	nA	
Diode capacitance	V _R = 0 V, f = 1 MHz, E = 0	C _D		70		pF	
	$V_R = 3 V, f = 1 MHz, E = 0$	C_D		25	40	pF	
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	Vo		350		mV	
Temperature coefficient of Vo	$E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm}$	TK _{Vo}		-2.6		mV/K	
Short circuit current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	I _k		45		μΑ	
Temperature coefficient of I _k	$E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm}$	TK _{lk}		0.1		%/K	
Reverse light current	$E_e = 1$ mW/cm ² , $\lambda = 950$ nm, $V_R = 5$ V	I _{ra}	40	48		μA	
Angle of half sensitivity		φ		± 65		deg	
Wavelength of peak sensitivity		λ_{p}		940		nm	
Range of spectral bandwidth		λ _{0.1}		430 to 1100		nm	
Noise equivalent power	$V_R = 10 \text{ V}, \ \lambda = 950 \text{ nm}$	NEP		4 x 10 ⁻¹⁴		W/√Hz	
Rise time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t _r		100		ns	
Fall time	$V_{R} = 10 \text{ V}, R_{L} = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t _f		100		ns	

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

Basic characteristics graphs to be extended to 110 °C ambient temperatures where applicable.

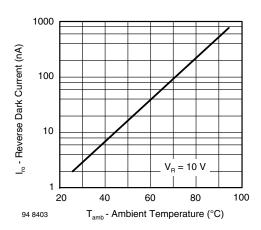


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

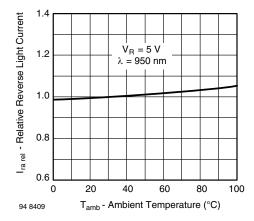


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature



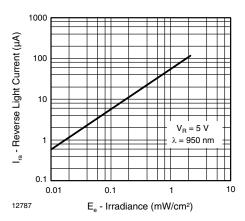


Fig. 3 - Reverse Light Current vs. Irradiance

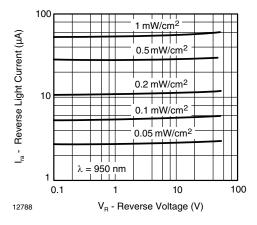


Fig. 4 - Reverse Light Current vs. Reverse Voltage

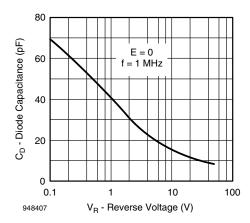


Fig. 5 - Diode Capacitance vs. Reverse Voltage

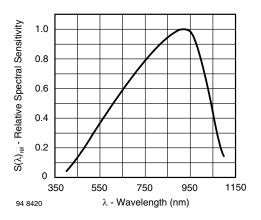


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

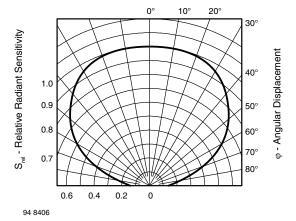
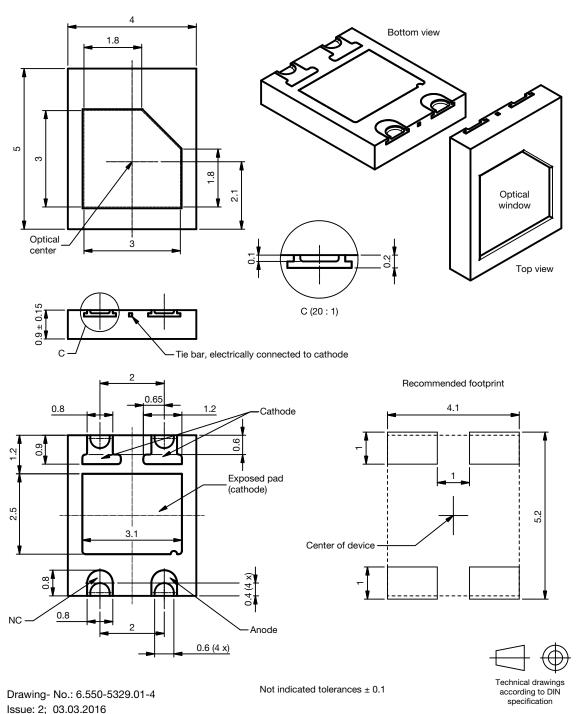


Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement

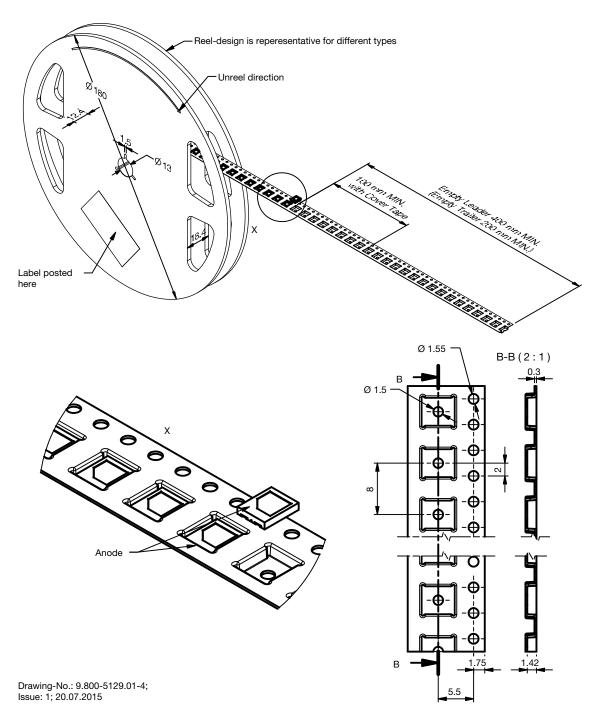
PACKAGE DIMENSIONS in millimeters



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Rev. 1.1, 21-Mar-16 4 Document Number: 84202

TAPE AND REEL DIMENSIONS in millimeters





SOLDER PROFILE

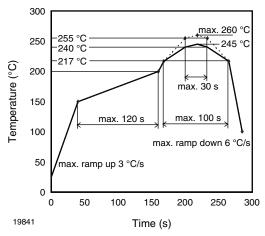


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020D

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: Level 4

Floor life: 72 h

Conditions: T_{amb} < 30 °C, RH < 60 %

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions:

192 h at 40 °C (+ 5 °C), RH < 5 %

0

96 h at 60 °C (+ 5 °C), RH < 5 %.



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